Towards Secure Computation with Optimal Complexity

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Towards Secure Computation with Optimal Complexity

by

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Abstract

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Secure computation enables a set of mutually distrustful parties to collaboratively compute a public function over their private data without leaking anything apart from the output. In this thesis, we present improvements towards obtaining secure computation with optimal computation, communication, and round complexity, in both theory and practice.

In Theory. We introduce a novel primitive called laconic oblivious transfer (or laconic OT for short), which allows an OT receiver to commit to a large input $D$ (of length $M$) via a short message. Subsequently, a single short message from an OT sender allows the receiver to learn $m_{D[L]}$, where the messages $m_0, m_1$ and the location $L \in [M]$ are dynamically chosen by the sender. We present a construction of this primitive based on the Decisional Diffie-Hellman (DDH) assumption, which makes is an innovative use of somewhere statistically binding (SSB) hashing in conjunction with hash proof systems.

Since its introduction, laconic OT has proved to be an extremely powerful tool towards achieving optimal computation and communication complexity in secure computation and beyond. In this thesis, we show a few of its very first applications including non-interactive secure computation and multi-hop homomorphic encryption for RAM programs.

In Practice. As a specific application of secure computation, we introduce a new notion called password-based threshold token authentication, which protects password-based authentication against single point of failures. Specifically, we distribute the role of a single server among $n$ servers and allow any $t$ servers to collectively verify clients’ passwords and generate tokens, while no $t - 1$ servers can forge a valid token or mount offline dictionary attacks. We then introduce PASTA, a general framework wherein clients can sign on using a two-round (optimal) protocol that meets our strong security guarantees.

Our experiments show that the overhead of PASTA, compared to a naïve single-server solution, is extremely low (1-5%) in the most likely setting where parties communicate over the internet. The overhead is higher for certain MAC tokens over a LAN (though still only a few milliseconds) due to inherent public-key operations in PASTA. We show, however, that public-key operations are necessary by proving a symmetric-key-only solution impossible.
To Mom,
for her unconditional love and support, always.
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Chapter 1

Introduction

Secure computation allows mutually distrustful parties to jointly perform computation over their private inputs without disclosing anything more than the output. Since its introduction by Yao in the 1980s [Yao82; Yao86], secure computation has found a wide range of practical applications such as electronic voting, electronic auctions, genome data analysis, secure signal processing, secure outsourcing computation, privacy-preserving machine learning, anonymous transactions, and many more.

Starting with the seminal works [Yao82; Yao86; GMW87; BGW88; CCD88; RB89] that established the feasibility of secure computation for any function and any number of parties, a central question in the cryptographic community has been whether secure computation protocols can be efficient enough to serve for its countless applications. In the past few decades, both theoretical and practical improvements have been pushing the limits of efficiency of those protocols and have demonstrated impressive improvements. Furthermore, for specific secure computation applications, massive research efforts have been devoted towards designing tailored protocols to achieve better efficiency than generic approaches.

Nevertheless, despite its strong security guarantees, wide range of applications, beautiful feasibility results, and substantial research progress, the adoption of secure computation in real industry is very much limited as of today, the biggest challenge of which still boils down to efficiency. Compared to a naïve insecure solution, the expensive cost of a secure computation protocol deters most companies from adopting this technology because very few of them are willing to trade-off their product performance for stronger security. This brings us to a natural question that we would like to, and have to answer:

\[ \text{Can we achieve secure computation with optimal efficiency?} \]

In examining the efficiency of a protocol, we consider several different measurement aspects. Our eventual goal, of course, is to achieve the optimal efficiency in every aspect. We briefly discuss these measurements below.

**Efficiency Measurements.** The most commonly considered efficiency measurement for a protocol is the computational complexity, and in particular, the computational overhead
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of a secure computation protocol, compared to the naïve protocol that does not have any security guarantee. This measurement is especially important in practice because of limited computational resources.

Communication complexity, which refers to the total amount of communication in a protocol, is also one of the major efficiency bottlenecks in most secure computation applications. This is due to the fact that network bandwidth is often times a shared resource for which multiple applications compete and in general the price for expanding network bandwidth tends to be much higher than the price for expanding computational resources. It is especially crucial to minimize communication complexity when secure computation protocols are applied on large datasets, where requiring the communication complexity to scale with the size of the datasets is usually infeasible for even moderate dataset sizes.

Apart from computation and communication complexity, another important metric in the efficiency of a secure computation protocol is its round complexity, that is, the number of rounds of interaction among the parties. As the time for one round of communication is lower bounded by the network latency, a high round complexity can easily become a bottleneck of a protocol especially when adopted on a high-latency network.

With explicit awareness of the limitations of available computational resources, network bandwidth and network latency, in this thesis, we aim to achieve secure computation with optimal computation, communication, and round complexity. We make substantial progress in both theory and practice.

1.1 In Theory

Cryptographic protocols for secure computation are typically based on Boolean circuits, where both the computational complexity and communication complexity scale with the size of the input dataset, which makes it generally unsuitable for large datasets. Substantial research effort has been devoted towards overcoming these challenges, including works on fully-homomorphic encryption (FHE) [Gen09; BV11b; BV11a; GSW13] and on the RAM setting of oblivious RAM [Gol87; Ost90] and secure RAM computation [OS97; GKK+12; LO13; GHL+14; GGMP16]. Protocols based on FHE generally have a favorable communication complexity and are basically non-interactive, yet incur a prohibitively large computational overhead that scales with the dataset size. On the other hand, protocols for the RAM model generally have a favorable computational overhead, but lack in terms of communication efficiency, which grows with the program running time, especially in the multi-party setting. Can we achieve the best of both worlds?

In this work [CDG+17] we make positive progress on this question. Specifically, we introduce a new tool called laconic oblivious transfer (or laconic OT for short) that helps to strike a balance between the two seemingly opposing goals. Laconic OT is a key tool towards the goal of improving communication complexity while at the same time maintaining low computational complexity for a variety of scenarios of secure computation.
1.1.1 Laconic Oblivious Transfer

Oblivious transfer (or OT for short), since its first introduction by Rabin [Rab81], has been a foundational building block for realizing computationally efficient secure computation protocols [Yao82; Yao86; GMW87; IPS08]. However, typical secure computation protocols involve executions of multiple instances of an oblivious transfer protocol. In fact, the number of needed oblivious transfers grows with the input size of one of the parties, which is the receiver of the oblivious transfer. As a result, the communication complexity of such protocols has to grow with the input size of the receiver.\(^1\)

We introduce a novel technique for optimizing communication complexity of secure computation over large inputs. Specifically, we provide a new oblivious transfer (OT) protocol with a laconic receiver. Laconic OT allows a receiver to commit to a large input \(D\) (of length \(M\)) via a short message. Subsequently, a single short message by a sender allows the receiver to learn \(m_D[L]\), where the messages \(m_0, m_1\) and the location \(L \in [M]\) are dynamically chosen by the sender. All prior constructions of OT required the receiver’s outgoing message to grow with \(D\).

Our key contribution is an instantiation of this primitive based on the Decisional Diffie-Hellman (DDH) assumption in the common reference string (CRS) model. The technical core of this construction is a novel use of somewhere statistically binding (SSB) hashing in conjunction with hash proof systems. Next, we show applications of laconic OT to non-interactive secure computation on large inputs and multi-hop homomorphic encryption for RAM programs.

1.1.2 Application I: Non-Interactive Secure Computation

Circuit Setting. Can a receiver publish a small encoding of her large confidential database \(D\) so that any sender, who holds a secret input \(x\), can reveal the output \(f(x, D)\) (where \(f\) is a circuit) to the receiver by sending her a single message? For security, we want the receiver’s encoding to hide \(D\) and the sender’s message to hide \(x\). Using laconic OT, we present the first solution to this problem. In our construction, the receiver’s published encoding is independent of the size of her database, but we do not restrict the size of the sender’s message.\(^2\)

RAM Setting. Consider the scenario where \(f\) can be computed using a RAM program \(P\) of running time \(t\). We use the notation \(P^D(x)\) to denote the execution of the program

\(^1\)We remark that related prior works on OT extension [Bea96; IKNP03; KK13; ALSZ13] makes the number of public key operations performed during protocol executions independent of the receiver’s input size. However, the communication complexity of receivers in these protocols still grows with the input size of the receiver.

\(^2\)Solutions for this problem based on fully-homomorphic encryption (FHE) [Gen09; LNO13], unlike our result, reduce the communication cost of both the sender’s and the receiver’s messages to be independent of the size of \(D\), but require additional rounds of interaction.
P on input x with random access to the database D. We provide a construction where as before the size of the receiver’s published message is independent of the size of the database D. Moreover, the size of the sender’s message (and computational cost of the sender and the receiver) grows only with t and the receiver learns nothing more than the output \( P^D(x) \) and the locations in D touched during the computation. Note that in all prior works on general secure RAM computation [OS97; GKK+12; LO13; WHC+14; GHL+14; GLOS15; GLO15] the size of the receiver’s message grew at least with its input size.\(^3\)

1.1.3 Application II: Homomorphic Encryption for RAM

Consider a scenario where S (a server), holding an input x, publishes an encryption \( \text{ct}_0 \) of her private input x under her public key. Now this ciphertext is passed on to a client \( Q_1 \) that homomorphically computes a (possibly private) program \( P_1 \) accessing (private) memory \( D_1 \) on the value encrypted in \( \text{ct}_0 \), obtaining another ciphertext \( \text{ct}_1 \). More generally, the computation could be performed by multiple clients. In other words, clients \( Q_2, Q_3, \cdots \) could sequentially compute private programs \( P_2, P_3, \cdots \) accessing their own private databases \( D_2, D_3, \cdots \). Finally, we want S to be able to use her secret key to decrypt the final ciphertext and recover the output of the computation. For security, we require simulation based security for a client \( Q_i \) against a collusion of the server and any subset of the clients, and IND-CPA security for the server’s ciphertext.

Though we described the simple case above, we are interested in the general case when computation is performed in different sequences of the clients. Examples of two such computation paths are shown in Figure 1.1. Furthermore, we consider the setting of persistent databases, where each client is able to execute dynamically chosen programs on the encrypted ciphertexts while using the same database that gets updated as these programs are executed.

FHE-Based Solution. Gentry’s [Gen09] fully homomorphic encryption (FHE) scheme offers a solution to the above problem when circuit representations of the desired programs \( P_1, P_2, \cdots \) are considered. Specifically, S could encrypt her input x using an FHE scheme. Now, the clients can publicly compute arbitrary programs on the encrypted value using a public evaluation procedure. This procedure can be adapted to preserve the privacy of the computed circuit [OPP14; DS16; BdMW16] as well. However, this construction only works for circuits. Realizing the scheme for RAM programs involves first converting the RAM program into a circuit of size at least linear in the size of the database. This linear effort can

\(^3\)The communication cost of the receiver’s message can be reduced to depend only on the running time of the program by allowing round complexity to grow with the running time of the program (using Merkle Hashing). Analogous to the circuit case, we remark that FHE-based solutions can make the communication of both the sender and the receiver small, but at the cost of extra rounds. Moreover, in the setting of RAM programs FHE-based solutions additionally incur an increased computational cost for the receiver. In particular, the receiver’s computational cost grows with the size of its database.
be exponential in the running time of the program for several applications of interest such as binary search.

**Our Relaxation.** In obtaining homomorphic encryption for RAM programs, we start by relaxing the compactness requirement in FHE. Compactness in FHE requires that the size of the ciphertexts does not grow with computation. In particular, in our scheme, we allow the evaluated ciphertexts to be bigger than the original ciphertext. Gentry, Halevi and Vaikuntanathan [GHV10] considered an analogous setting for the case of circuits. As in Gentry et al. [GHV10], in our setting computation itself will happen at the time of decryption. Therefore, we additionally require that clients \( Q_1, Q_2, \cdots \) first ship pre-processed versions of their databases to \( S \) for the decryption, and security will additionally require that \( S \) does not learn the access pattern of the programs on client databases. This brings us to the following question:

*Can we realize multi-hop encryption schemes for RAM programs where the ciphertext grows linearly only in the running time of the computation performed on it?*

We show that laconic OT can be used to realize such a multi-hop homomorphic encryption scheme for RAM programs. Our result bridges the gap between growth in ciphertext size and computational complexity of homomorphic encryption for RAM programs.

Our work also leaves open the problem of realizing (fully or somewhat) homomorphic encryption for RAM programs with (somewhat) compact ciphertexts and for which computational cost grows with the running time of the computation, based on traditional computational assumptions. Our solution for multi-hop RAM homomorphic encryption is for the semi-honest (or, semi-malicious) setting only. We leave open the problem of obtaining a solution in the malicious setting.\(^5\)

---

\(^4\)One method for realizing homomorphic encryption for RAM programs [GKP+13; GHRW14; CHJV15; BGL+15; KLW15] would be to use obfuscation [GGH+13] based on multilinear maps [GGH13]. However, in this paper we focus on basing homomorphic RAM computation on DDH and defer the work on obfuscation to future work.

\(^5\)Using non-interactive zero-knowledge (NIZK) proofs alone does not solve the problem, because locations accessed during computation are dynamically decided.
1.1.4 Follow-Up Work

Since its introduction, laconic OT has proved to be an extremely powerful tool towards achieving optimal communication complexity as well as computational complexity in various settings of secure computation and beyond. Specifically, it has been utilized as a crucial tool in achieving adaptively secure circuit garbling with nearly optimal online complexity [GS18], adaptive garbled RAM from standard assumptions [GOS18], laconic function evaluation and its applications [QWW18], and so on.

Furthermore, ideas from laconic OT have been useful in making significant progress in areas beyond secure computation, including obtaining Identity-Based Encryption (IBE) from the Computational Diffie-Hellman (CDH) assumption [DG17], anonymous IBE from the CDH assumption [BLSV18], key-dependent message (KDM) secure encryption [BLSV18; DGHM18], reusable designated-verifier NIZKs [LQR+19], etc.

1.2 In Practice

As a specific application of secure computation, we introduce and formalize the notion of password-based threshold token authentication, which protects password-based token authentication against single point of failures.

Token-Based Authentication. Token-based authentication is arguably the most common way we obtain authorized access to resources, services, and applications on the internet and on enterprise networks.

Open standards such as JSON Web Token (JWT) [JWT] and SAML [SAML] are widely used to facilitate single-sign-on authentication by allowing clients to initially sign on using a standard mechanism such as username/password verification to obtain and locally store a token in a cookie or the local storage. The token can then be used for all future accesses to various applications without client involvement, until it expires.

A similar mechanism is used, via open standards such as OAuth [OAuth] and OpenID [OpenID], by many companies including Google, Facebook and Amazon [Google; Facebook; Amazon] to enable their users to share information about their accounts with (or authenticate themselves to) third party applications or websites without revealing their passwords to them.

Finally, network authentication protocols such as Kerberos [Kerberos] are commonly used by enterprises (e.g. Active Directory in Windows Servers) to, periodically but infrequently, authenticate clients with their credentials and issue them a ticket-granting ticket (TGT) that they can use to request access to various services on the enterprise network such as printers, internal web and more.

It is therefore no surprise that most software-based secret management systems provide tokens as a primary method for authenticating clients. For example, consider the following statement from the popular open source solution Vault by Hashicorp [Vault]:
Figure 1.2: Generic flow diagram of commonly used password-based token generation solutions. The figure shows only the sign-on phase which is preceded by a one-time registration phase (not shown) where the client stores its username (usr) and the hash (h) of its password with the identity provider. msk is the secret key used for generating tokens and vk is used for verifying them.

The token auth method is built-in and is at the core of client authentication. Other auth methods may be used to authenticate a client, but they eventually result in the generation of a client token managed by the token backend.

In all these cases, the authentication flow is effectively the same. A client signs on with its username/password, typically by sending hash of its password to an identity provider. The identity server who stores the username along with its hashed passwords as part of a registration phase, verifies the client’s credential by matching the hash during the sign-on process before issuing an authentication token using a master secret key (see Figure 1.2). The token is generated by computing a digital signature or a message authentication code (all the above-mentioned standards support both digital signatures and MACs) on a message that can contain client’s information/attributes, expiration time and a policy that would control the nature of access. The token is later verified by an application server which holds the verification key (for MACs this is equal to the master secret key). See Figure 1.3 for a sample JWT authenticated using HMAC [KBC97]. Note that the only secret known to the client is its password, and the device the client uses for access stores the temporary authentication token on its behalf. Besides this temporary (and often restricted) token, client devices do not store any long term secrets that are used to authenticate the client.

However, such an identity provider is a single point of failure that if breached, enables an attacker to (i) recover the master secret key and forge arbitrary tokens that enable access to arbitrary resources and information in the system and (ii) obtain hashed passwords to use
CHAPTER 1. INTRODUCTION

Figure 1.3: A sample JSON Web Token [JWT] that uses HMAC. The base64 encoded token on the left is what is sent and stored. When decoded, it contains a header with algorithm and token type, a payload that includes various attributes and a HMAC of header/payload.

as part of an offline dictionary attack to recover client credentials.

In this work [AMMM18], we propose the notion of Password-based Threshold Authentication (PbTA) for distributing the role of the identity provider among $n$ servers who collectively verify clients’ passwords and generate authentication tokens for them (see Figure 1.4 for a generic flow). PbTA enables any $t$ ($2 \leq t \leq n$) servers to authenticate the client and generate valid tokens while any attacker who compromises at most $t - 1$ servers cannot forge valid tokens or mount offline dictionary attacks, thus providing very strong unforgeability and password-safety properties.

This functionality is a specific application of secure computation and in the protocol design round complexity is the major bottleneck. In order to achieve the optimal performance we minimize the round of interaction between clients and servers.

Our Contributions. We formally introduce the notion of Password-based Threshold Authentication (PbTA) with the goal of making password-based token generation secure against server breaches that could compromise both long-term keys and user credentials. Our contributions are as follows:

- Defining Security for PbTA. We formalize password-based threshold authentication, and establish the necessary security requirements of token unforgeability and password-safety in presence of an adversary who may breach a subset of the identity
(a) Generic flow diagram of a PbTA protocol in the registration phase.

(b) Generic flow diagram of a PbTA protocol in the sign-on phase.

Figure 1.4: Generic flow diagrams of a PbTA protocol.
servers. Our game-based definitions are strong and intuitive, and consider security in a multi-client setting where many clients use the same identity provider. Adversary could corrupt clients in an adaptive fashion during the game. We note that an alternative approach would be to use the Universal Composability framework [Can01] as followed in some prior work involving password-based authentication (e.g. [JKKX17]). We chose to focus on game-based definitions that are much simpler to work with but comprehensive enough to cover a very broad set of attack scenarios.

- **The PASTA Framework with Minimal Interaction.** We propose a general framework called PASTA, that uses as building blocks any threshold oblivious pseudorandom function (TOPRF) and any threshold token generation (TTG) scheme, i.e. a threshold MAC or digital signature. PASTA meets our stringent security requirements for PbTA. After a one-time registration phase, a client just needs to remember its password. It can sign-on using a two-round protocol wherein the servers do not talk to each other (assuming only that the servers communicate to the client over an authenticated channel). Therefore, PASTA requires minimal interaction.

  The sign-on protocol ensures that if the client’s password is correct, he obtains a valid authentication token: client sends a request message to a subset of the servers, and servers respond with messages of their own. If the client password is a match, it can combine server responses to obtain a valid token (see Figure 1.4).\(^6\) Otherwise, it does not learn anything.

  At the first glance, it may seem unnatural to define a general framework that works for both symmetric-key tokens (i.e. MAC) and public-key tokens (i.e. digital signature). Though their verification procedures are different in terms of being private or public, note that their token generation procedures are both private. PASTA focuses on generating tokens, hence it works for both types of tokens.

- **Instantiations and Implementation.** We instantiate and implement our framework in C++ with four different threshold token generation schemes: block-cipher based and DDH-based threshold MACs of Naor et al. [NPR99], threshold RSA-based signature of Shoup [Sho00] and threshold pairing-based signature of Boldyreva [Bol03]. Each instantiation has its own advantages and disadvantages. When instantiated with a threshold MAC, we obtain a more efficient solution but the tokens are not publicly verifiable, i.e. \(vk\) in Figure 1.2 and Figure 1.4b stored in the application server would be the same as the master secret key \(msk\), since the verifier needs the secret

\(^6\)Note that in this setting, as opposed to the naïve solution (Figure 1.2), no matching takes place on the identity provider side. In particular, an ID-server does not check against a record stored in the registration phase, because, if it did, one can easily see that offline attacks would be possible even if a single server is breached.
key for verification. PASTA with RSA-based and pairing-based token generation are more expensive but are publicly verifiable. Among the signature-based solutions, the pairing-based one is faster since signing does not require pairings but the RSA-based solution has faster verification and produces signatures that are compatible with legacy applications. To the best of our knowledge, our work is also the first to implement several of the threshold token generation schemes (not password-based) and report on their performance.

Our experiments show that the overhead of obtaining security against server breaches using PASTA, in the sign-on stage, is at most 5% compared to the naïve solution of using hashed passwords and a single-server token generation, in the most likely scenario where clients connect to servers over the internet (a WAN network). This is primarily due to the fact that in this case, network latency dominates the total runtime for all token types. The overhead is a bit higher in the LAN setting but the total runtime of sign-on (steps 1-4 in Figure 1.4b) is still very fast, ranging from 1.3 ms for \((n, t) = (3, 2)\) with a symmetric-key MAC token to 23 ms for \((n, t) = (10, 10)\) with an RSA-based token, where \(n\) is the number of servers and \(t\) is the threshold.

• **Necessity of Computational Overhead.** PASTA has its largest overhead compared to the naïve single-server solution, for symmetric-key based tokens in the LAN setting. This is because public-key operations dominate PASTA’s runtime while the naïve solution only involves symmetric-key operations. Nevertheless, we show that this inefficiency is inherent by proving that public-key operations are necessary to achieve our notion of PbTA.

1.3 Organization

In Chapter 2 we introduce the notion of laconic OT formally and present a DDH-based construction. In Chapter 3 we present the first application of laconic OT to non-interactive secure computation on large inputs in the circuit setting as well as RAM setting. In Chapter 4 we formally define the setting of multi-hop homomorphic encryption for RAM programs and present our construction using laconic OT.

In Chapter 5 we formalize password-based threshold authentication and propose the PASTA framework with minimal round complexity as well as present experimental results for various types of tokens.

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7To achieve better security of the secret key, the verification process can also be made distributed using a standard threshold MAC scheme. We omit the distributed verification in the rest of this paper because it is not our focus.
Chapter 2

Laconic Oblivious Transfer

In this chapter, we introduce the notion of laconic oblivious transfer (or laconic OT for short). Laconic OT allows an OT receiver to commit to a large input $D \in \{0, 1\}^M$ via a short message. Subsequently, the sender responds with a single short message to the receiver depending on dynamically chosen two messages $m_0, m_1$ and a location $L \in [M]$. The sender’s response message allows the receiver to recover $m_{D[L]}$ (while $m_{1-D[L]}$ remains computationally hidden). Furthermore, without any additional communication with the receiver, the sender could repeat this process for multiple choices of $L$.

Our construction of laconic OT is obtained by first realizing a “mildly compressing” laconic OT protocol for which the receiver’s message is factor-2 compressing, i.e., half the size of its input. We base this construction on the Decisional Diffie-Hellman (DDH) assumption. We note that, subsequent to our work, the factor-2 compression construction has been simplified by Döttling and Garg [DG17] (another alternative simplification can be obtained using [AIKW13]). Next we show that such a “mildly compressing” laconic OT can be bootstrapped, via the usage of a Merkle Hash Tree and Yao’s Garbled Circuits [Yao82; Yao86], to obtain a “fully compressing” laconic OT, where the size of the receiver’s message is independent of its input size.

The laconic OT scheme with a Merkle Tree structure allows for good properties like local verification and local updates, which makes it a powerful tool in secure computation with large inputs. We show new applications of laconic OT to non-interactive secure computation in Chapter 3 and homomorphic encryption for RAM programs in Chapter 4.

Organization. In Section 2.1 we give a technical overview of this work. We introduce the notion of laconic OT formally in Section 2.2 and give a construction with factor-2 compression in Section 2.3, which can be bootstrapped to a fully compressing updatable laconic OT in Section 2.4.
2.1 Technical Overview

In this section we provide an overview of laconic OT and our constructions of this new primitive.

Definition at a High Level. Laconic OT consists of two major components: a hash function and an encryption scheme. We call the hash function $\text{Hash}$ and the encryption scheme $(\text{Send}, \text{Receive})$. In a nutshell, laconic OT allows a receiver $R$ to compute a succinct digest of a large database $D$ and a private state $\hat{D}$ using the hash function $\text{Hash}$. After digest is made public, anyone can non-interactively send OT messages to $R$ w.r.t. a location $L$ of the database such that the receiver’s choice bit is $D[L]$. Here, $D[L]$ is the database-entry at location $L$. In more detail, given digest, a database location $L$, and two messages $m_0$ and $m_1$, the algorithm Send computes a ciphertext $e$ such that $R$, who owns $\hat{D}$, can use the decryption algorithm Receive to decrypt $e$ to obtain the message $m_{D[L]}$.

For security, we require sender privacy against semi-honest receiver. In particular, given an honest receiver’s view, which includes the database $D$, the message $m_{1-D[L]}$ is computationally hidden. We formalize this using a simulation based definition. On the other hand, we do not require receiver privacy as opposed to standard oblivious transfer, namely, no security guarantee is provided against a cheating (semi-honest) sender. This is mostly for ease of exposition. Nevertheless, adding receiver privacy to laconic OT can be done in a straightforward manner via the usage of garbled circuits and two-message OT (see Section 2.2.1 for a detailed discussion).

For efficiency, we have the following requirement: First, the size of digest only depends on the security parameter and is independent of the size of the database $D$. Moreover, after digest and $\hat{D}$ are computed by Hash, the workload of both the sender and receiver (that is, the runtime of both Send and Receive) becomes essentially independent of the size of the database (i.e., depending at most polynomially on $\log(|D|)$).

Notice that our security definition and efficiency requirement immediately imply that the Hash algorithm used to compute the succinct digest must be collision resistant. Thus, it is clear that the hash function must be keyed and in our case it is keyed by a common reference string.

Construction at a High Level. We first construct a laconic OT scheme with factor-2 compression, which compresses a $2\kappa$-bit database to a $\kappa$-bit digest. Next, to get laconic OT for databases of arbitrary size, we bootstrap this construction using an interesting combination of Merkle hashing and garbled circuits. Below, we give an overview of each of these steps.

2.1.1 Laconic OT with Factor-2 Compression

We start with a construction of a laconic OT scheme with factor-2 compression, i.e., a scheme that hashes a $2\kappa$-bit database to a $\kappa$-bit digest. This construction is inspired by the notion
of witness encryption [GGSW13]. We first explain the scheme based on witness encryption. Then, we show how this specific witness encryption scheme can be realized with the more standard notion of hash proof systems (HPS) [CS02]. Our overall scheme is based on the security of Decisional Diffie-Hellman (DDH) assumption.

**Construction Using Witness Encryption.** Recall that a witness encryption scheme is defined for an NP-language L (with corresponding witness relation R). It consists of two algorithms Enc and Dec. The algorithm Enc takes as input a problem instance x and a message m, and produces a ciphertext. A recipient of the ciphertext can use Dec to decrypt the message if x ∈ L and the recipient knows a witness w such that R(x, w) holds. There are two requirements for a witness encryption scheme, correctness and security. Correctness requires that if R(x, w) holds, then Dec(x, w, Enc(x, m)) = m. Security requires that if x /∈ L, then Enc(x, m) computationally hides m.

We now discuss how to construct a laconic OT with factor-2 compression using a two-to-one hash function and witness encryption. Let H : K × {0, 1} 2κ → {0, 1}κ be a keyed hash function, where K is the key space. Consider the language L = {(K, L, y, b) ∈ K × [2κ] × {0, 1}κ × {0, 1} | ∃D ∈ {0, 1} 2κ such that H(K, D) = y and D[L] = b}. Let (Enc, Dec) be a witness encryption scheme for the language L.

The laconic OT scheme is as follows: The Hash algorithm computes y = H(K, D) where K is the common reference string and D ∈ {0, 1} 2κ is the database. Then y is published as the digest of the database. The Send algorithm takes as input K, y, a location L, and two messages (m0, m1) and proceeds as follows. It computes two ciphertexts e0 ← Enc((K, L, y, 0), m0) and e1 ← Enc((K, L, y, 1), m1) and outputs e = (e0, e1). The Receive algorithm takes as input K, L, y, D, and the ciphertext e = (e0, e1) and proceeds as follows. It sets b = D[L], computes m ← Dec((K, L, y, b), D, e0) and outputs m.

It is easy to check that the above scheme satisfies correctness. However, we run into trouble when trying to prove sender privacy. Since H compresses 2κ bits to κ bits, most hash values have exponentially many pre-images. This implies that for most values of (K, L, y), it holds that both (K, L, y, 0) ∈ L and (K, L, y, 1) ∈ L, that is, most problem instances are yes-instances. However, to reduce sender privacy of our scheme to the security of witness encryption, we ideally want that if y = H(K, D), then (K, L, y, D[L]) ∈ L while (K, L, y, 1 − D[L]) /∈ L. To overcome this problem, we use a somewhere statistically binding hash function that allows us to artificially introduce no-instances as described below.

**Somewhere Statistically Binding Hash to the Rescue.** Somewhere statistically binding (SSB) hash functions [HW15; KKLW15; OPWW15] support a special key generation procedure such that the hash value information theoretically fixes certain bit(s) of the pre-image. In particular, the special key generation procedure takes as input a location L and generates a key K(L). Then the hash function keyed by K(L) will bind the L-th bit of the pre-image. That is, K(L) and y = H(K(L), D) uniquely determines D[L]. The security requirement for
SSB hashing is the index-hiding property, i.e., keys $K^{(L)}$ and $K^{(L')}$. We can now establish security of the above laconic OT scheme when instantiated with SSB hash functions. To prove security, we will first replace the key $K$ by a key $K^{(L)}$ that statistically binds the $L$-th bit of the pre-image. The index hiding property guarantees that this change goes unnoticed. Now for every hash value $y = H(K^{(L)}, D)$, it holds that $(K, L, y, D[L]) \in \mathcal{L}$ while $(K, L, y, 1 - D[L]) \notin \mathcal{L}$. We can now rely on the security of witness encryption to argue that $\text{Enc}((K^{(L)}, L, y, 1 - D[L]), m_{1 - D[L]})$ computationally hides the message $m_{1 - D[L]}$.

**Working with DDH.** The above described scheme relies on a witness encryption scheme for the language $\mathcal{L}$. We note that witness encryption for general NP languages is only known under strong assumptions such as graded encodings [GGSW13] or indistinguishability obfuscation [GGH+13]. Nevertheless, the aforementioned laconic OT scheme does not need full power of general witness encryption. In particular, we leverage the fact that hash proof systems [CS02] can be used to construct statistical witness encryption schemes for specific languages [GGSW13]. Towards this end, we will carefully craft an SSB hash function that is hash proof system friendly, that is, allows for a hash proof system (or statistical witness encryption) for the language $\mathcal{L}$ required above. Our construction of the HPS-friendly SSB hash is based on the Decisional Diffie-Hellman assumption and is inspired from a construction by Okamoto et al. [OPWW15].

We briefly outline our HPS-friendly SSB hash below. We strongly encourage the reader to see Section 2.3.2 for the full construction or see [DG17] for a simplified construction.

Let $G$ be a (multiplicative) cyclic group of order $p$ generated by a generator $g$. A hashing key is of the form $H = g^H$ (the exponentiation is done component-wisely), where the matrix $\hat{H} \in \mathbb{Z}_p^{2 \times 2\kappa}$ is chosen uniformly at random. The hash function of $x \in \mathbb{Z}_p^{2 \kappa}$ is computed as $H(\hat{H}, x) = \hat{H}^x \in G^2$ (where $(\hat{H}^x)_i = \prod_{k=1}^{2\kappa} \hat{H}^x k _i$), hence $\hat{H}^x = g^{\hat{H}^x}$. The binding key $\hat{H}^{(i)}$ is of the form $\hat{H}^{(i)} = g^{A^i + T}$, where $A \in \mathbb{Z}_p^{2 \times 2\kappa}$ is a random rank 1 matrix, and $T \in \mathbb{Z}_p^{2 \times 2\kappa}$ is a matrix with zero entries everywhere, except that $T_{2,i} = 1$.

Now we describe a witness encryption scheme $(\text{Enc}, \text{Dec})$ for the language $\mathcal{L} = \{(\hat{H}, i, \hat{y}, b) \mid \exists x \in \mathbb{Z}_p^{2\kappa} \text{ s.t. } \hat{H}^x = \hat{y} \text{ and } x_i = b\}$. $\text{Enc}((\hat{H}, i, \hat{y}, b), m)$ first sets

$$\hat{H}' = \left(\begin{array}{c} \hat{H} \\ g^{e_i^T} \end{array}\right) \in G^{3 \times 2\kappa}, \hat{y}' = \left(\begin{array}{c} \hat{y} \\ g^{b} \end{array}\right) \in G^3,$$

where $e_i \in \mathbb{Z}_p^{2\kappa}$ is the $i$-th unit vector. It then picks a random $r \in \mathbb{Z}_p^3$ and computes a ciphertext $c = \left(\left((\hat{H}')^T\right)^r, (\hat{y}'^T)^r \oplus m\right)$. To decrypt a ciphertext $c = (\hat{h}, z)$ given a witness $x \in \mathbb{Z}_p^{2\kappa}$, we compute $m = z \oplus \hat{h}^x$. It is easy to check correctness. For the security proof, see Section 2.3.3.
2.1.2 Laconic OT with Arbitrary Compression

We now provide a bootstrapping technique that constructs a laconic OT scheme with arbitrary compression factor from one with factor-2 compression. Let $\ell_{\text{OT const}}$ denote a laconic OT scheme with factor-2 compression.

Bootstrapping Hash Function via a Merkle Tree. A binary Merkle tree is a natural way to construct hash functions with an arbitrary compression factor from two-to-one hash functions, and this is exactly the route we pursue. A binary Merkle tree is constructed as follows: The database is split into blocks of $\kappa$ bits, each of which forms the leaf of the tree. An interior node is computed as the hash value of its two children via a two-to-one hash function. This structure is defined recursively from the leaves to the root. When we reach the root node (of $\kappa$ bits), its value is defined to be the (succinct) hash value or digest of the entire database. This procedure defines the hash function.

The next step is to define the laconic OT algorithms $\text{Send}$ and $\text{Receive}$ for the above hash function. Our first observation is that given the digest, the sender can transfer specific messages corresponding to the values of the left and right children of the root (via $2\kappa$ executions of $\ell_{\text{OT const-Send}}$). Hence, a naive approach for the sender is to output $\ell_{\text{OT const}}$ encryptions for the path of nodes from the root to the leaf of interest. This approach runs into an immediate issue because to compute $\ell_{\text{OT const}}$ encryptions at any layer other than the root, the sender needs to know the value at that internal node. However, in the scheme a sender only knows the value of the root and nothing else.

Traversing Merkle Tree via Garbled Circuits. Our main idea to make the above naive idea work is via an interesting usage of garbled circuits. At a high level, the sender will output a sequence of garbled circuits (one per layer of the tree) to transfer messages corresponding to the path from the root to the leaf containing the $L$-th bit, so that the receiver can traverse the Merkle tree from the root to the leaf as illustrated in Figure 2.1.

In more detail, the construction works as follows: The $\text{Send}$ algorithm outputs $\ell_{\text{OT const}}$ encryptions using the root digest and a collection of garbled circuits, one per layer of the Merkle tree. The $i$-th circuit has a bit $b$ hardwired in it, which specifies whether the path should go to the left or right child at the $i$-th layer. It takes as input a pair of sibling nodes $(\text{node}_0, \text{node}_1)$ along the path at layer $i$ and outputs $\ell_{\text{OT const}}$ encryptions corresponding to nodes on the path at layer $i + 1$ w.r.t. $\text{node}_b$ as the hash value. Conceptually, the circuit computes $\ell_{\text{OT const}}$ encryptions for the next layer.

The $\ell_{\text{OT const}}$ encryptions at the root encrypt the input keys of the first garbled circuit. In the garbled circuit at layer $i$, the messages being encrypted/sent correspond to the input keys of the garbled circuit at layer $i + 1$. The last circuit takes two sibling leaves as input which contains $D[L]$, and outputs $\ell_{\text{OT const}}$ encryptions of $m_0$ and $m_1$ corresponding to location $L$ (among the $2\kappa$ locations).

Given a laconic OT ciphertext, which consists of $\ell_{\text{OT const}}$ ciphertexts w.r.t. the root digest and a sequence of garbled circuits, the receiver can traverse the Merkle tree as follows.
First he runs $\ell OT_{\text{const}} \cdot \text{Send}$ for the $\ell OT_{\text{const}}$ ciphertexts using as witness the children of the root, obtaining the input labels corresponding to these to be fed into the first garbled circuit. Next, he uses the input labels to evaluate the first garbled circuit, obtaining $\ell OT_{\text{const}}$ ciphertexts for the second layer. He then runs $\ell OT_{\text{const}} \cdot \text{Receive}$ again for these ciphertexts using as witness the children of the second node on the path. This procedure continues till the last layer.

Security of the construction can be established using the sender security of $\ell OT_{\text{const}} \cdot \text{Receive}$ and simulation based security of the circuit garbling scheme.

**Extension.** Finally, for our RAM applications we need a slightly stronger primitive which we call *updatable laconic OT* that additionally allows for modifications/writes to the database while ensuring that the digest is updated in a consistent manner. The construction sketched in this paragraph can be modified to support this stronger notion. For a detailed description of this notion refer to Section 2.2.2.

## 2.2 Definitions of Laconic OT

In this section, we give formal definitions for *Laconic OT* (or, $\ell OT$ for short). We will start by describing laconic OT and then provide an extension of it to the notion of updatable laconic OT.
2.2.1 Laconic OT

Definition 2.2.1 (Laconic OT). A laconic OT ($\ell$OT) scheme syntactically consists of four algorithms $\text{crsGen}$, $\text{Hash}$, $\text{Send}$ and $\text{Receive}$.

- $\text{crs} \leftarrow \text{crsGen}(1^\kappa)$. It takes as input the security parameter $1^\kappa$ and outputs a common reference string $\text{crs}$.
- $(\text{digest}, \hat{D}) \leftarrow \text{Hash}(\text{crs}, D)$. It takes as input a common reference string $\text{crs}$ and a database $D \in \{0, 1\}^*$ and outputs a digest $\text{digest}$ of the database and a state $\hat{D}$.
- $e \leftarrow \text{Send}(\text{crs}, \text{digest}, L, m_0, m_1)$. It takes as input a common reference string $\text{crs}$, a digest $\text{digest}$, a database location $L \in \mathbb{N}$ and two messages $m_0$ and $m_1$ of length $\kappa$, and outputs a ciphertext $e$.
- $m \leftarrow \text{Receive}^\hat{D}(\text{crs}, e, L)$. This is a RAM algorithm with random read access to $\hat{D}$. It takes as input a common reference string $\text{crs}$, a ciphertext $e$, and a database location $L \in \mathbb{N}$. It outputs a message $m$.

We require the following properties of an $\ell$OT scheme $(\text{crsGen}, \text{Hash}, \text{Send}, \text{Receive})$.

- **Correctness.** We require that it holds for any database $D$ of size at most $M = \text{poly}(\kappa)$ for any polynomial function $\text{poly}(\cdot)$, any memory location $L \in [M]$, and any pair of messages $(m_0, m_1) \in \{0, 1\}^\kappa \times \{0, 1\}^\kappa$ that

  $$\Pr \left[ m = m_{D[L]} \right| \begin{array}{l}
  \text{crs} \leftarrow \text{crsGen}(1^\kappa) \\
  (\text{digest}, \hat{D}) \leftarrow \text{Hash}(\text{crs}, D) \\
  e \leftarrow \text{Send}(\text{crs}, \text{digest}, L, m_0, m_1) \\
  m \leftarrow \text{Receive}^\hat{D}(\text{crs}, e, L)
  \end{array} \right] = 1,$$

  where the probability is taken over the random choices made by $\text{crsGen}$ and $\text{Send}$.

- **Sender Privacy Against Semi-Honest Receivers.** There exists a PPT simulator $\ell$OTSIm such that the following holds. For any database $D$ of size at most $M = \text{poly}(\kappa)$ for any polynomial function $\text{poly}(\cdot)$, any memory location $L \in [M]$, and any pair of messages $(m_0, m_1) \in \{0, 1\}^\kappa \times \{0, 1\}^\kappa$, let $\text{crs} \leftarrow \text{crsGen}(1^\kappa)$ and $\text{digest} \leftarrow \text{Hash}(\text{crs}, D)$. Then it holds that

  $$(\text{crs}, \text{Send}(\text{crs}, \text{digest}, L, m_0, m_1)) \approx (\text{crs}, \ell\text{OTSIm}(D, L, m_{D[L]})).$$

- **Efficiency.** The length of $\text{digest}$ is a fixed polynomial in $\kappa$ independent of the size of the database; we will assume for simplicity that $|\text{digest}| = \kappa$. Moreover, the algorithm $\text{Hash}$ runs in time $|D| \cdot \text{poly}(\log |D|, \kappa)$, $\text{Send}$ and $\text{Receive}$ run in time $\text{poly}(\log |D|, \kappa)$. 


**CHAPTER 2. LACONIC OBLIVIOUS TRANSFER**

**Receiver Privacy.** In the above definition, we do not require receiver privacy as opposed to standard oblivious transfer, namely, no security guarantee is provided against a cheating (semi-honest) sender. This is mostly for ease of exposition. We would like to point out that adding receiver privacy (i.e., standard simulation based security against a semi-honest sender) to laconic OT can be done in a straightforward way. Instead of sending digest directly from the receiver to the sender and sending $e$ back to the receiver, the two parties compute $\text{Send}$ together via a two-round secure 2PC protocol, where the input of the receiver is digest and the input of the sender is $(L, m_0, m_1)$, and only the receiver obtains the output $e$. This can be done using standard two-message OT and garbled circuits.

**Multiple Executions of Send Sharing the Same digest.** Notice that since the common reference string is public (i.e., not chosen by the simulator), the sender can involve $\text{Send}$ function multiple times while still ensuring that security can be argued from the above definition (for the case of single execution) via a standard hybrid argument.

It will be convenient to use the following shorthand notations (generalizing the above notions) to run laconic OT for every single element in a database. Let $\text{Keys} = ((\text{Key}_{1,0}, \text{Key}_{1,1}), \ldots, (\text{Key}_{M,0}, \text{Key}_{M,1}))$ be a list of $M = |D|$ key-pairs, where each key is of length $\kappa$. Then we define

$$\text{Send}(\text{crs}, \text{digest}, \text{Keys}) = \left( \text{Send}(\text{crs}, \text{digest}, 1, \text{Key}_{1,0}, \text{Key}_{1,1}), \ldots, \text{Send}(\text{crs}, \text{digest}, M, \text{Key}_{M,0}, \text{Key}_{M,1}) \right).$$

Likewise, for a vector $e = (e_1, \ldots, e_M)$ of ciphertexts define

$$\text{Receive}^D(\text{crs}, e) = \left( \text{Receive}^D(\text{crs}, e_1, 1), \ldots, \text{Receive}^D(\text{crs}, e_M, M) \right).$$

Similarly, let $\text{Labels} = \text{Keys}_D = (\text{Key}_{1,D[1]}, \ldots, \text{Key}_{M,D[M]})$, and define

$$\ell\text{OTSim}(\text{crs}, D, \text{Labels}) = \left( \ell\text{OTSim}(\text{crs}, D, 1, \text{Key}_{1,D[1]}), \ldots, \ell\text{OTSim}(\text{crs}, D, M, \text{Key}_{M,D[M]}) \right).$$

By the sender security for multiple executions, we have that

$$(\text{crs}, \text{Send}(\text{crs}, \text{digest}, \text{Keys})) \approx (\text{crs}, \ell\text{OTSim}(\text{crs}, D, \text{Labels})).$$

**Security Against Malicious Adversaries.** The definition and construction we give is secure against semi-honest adversaries, but it can be upgraded to the malicious setting in a similar way as we will discuss in Chapter 3 for the first application. See Section 3.3 for a detailed discussion.

### 2.2.2 Updatable Laconic OT

For our applications, we will need a version of laconic OT for which the receiver’s short commitment digest to his database can be updated quickly (in time much smaller than the
size of the database) when a bit of the database changes. We call this primitive supporting this functionality updatable laconic OT and define more formally below. At a high level, updatable laconic OT comes with an additional pair of algorithms SendWrite and ReceiveWrite which transfer the keys for an updated digest digest* to the receiver. For convenience, we will define ReceiveWrite such that it also performs the write in $\hat{D}$.

**Definition 2.2.2** (Updatable Laconic OT). An updatable laconic OT (updatable $\ell$OT) scheme consists of algorithms crsGen, Hash, Send, Receive as per Definition 2.2.1 and additionally two algorithms SendWrite and ReceiveWrite with the following syntax.

- $e_w \leftarrow \text{SendWrite} \left( \text{crs}, \text{digest}, L, b, \{m_{j,0}, m_{j,1}\}_{j=1}^{\text{digest}} \right)$. It takes as input the common reference string crs, a digest digest, a location $L \in \mathbb{N}$, a bit $b \in \{0,1\}$ to be written, and $\text{digest}$ pairs of messages $\{m_{j,0}, m_{j,1}\}_{j=1}^{\text{digest}}$, where each $m_{j,c}$ is of length $\kappa$. And it outputs a ciphertext $e_w$.
- $\{m_j\}_{j=1}^{\text{digest}} \leftarrow \text{ReceiveWrite}^{\hat{D}}(\text{crs}, L, b, e_w)$. This is a RAM algorithm with random read/write access to $\hat{D}$. It takes as input the common reference string crs, a location $L$, a bit $b \in \{0,1\}$ and a ciphertext $e_w$. It updates the state $\hat{D}$ (such that $D[L] = b$) and outputs messages $\{m_j\}_{j=1}^{\text{digest}}$.

We require the following properties on top of properties of a laconic OT scheme.

- **Correctness with Regard to Writes.** For any database $D$ of size at most $M = \text{poly}(\kappa)$ for any polynomial function $\text{poly}(\cdot)$, any memory location $L \in [M]$, any bit $b \in \{0,1\}$, and any messages $\{m_{j,0}, m_{j,1}\}_{j=1}^{\text{digest}}$ of length $\kappa$, the following holds. Let $D^*$ be identical to $D$, except that $D^*[L] = b$,

$$\Pr \left[ \begin{array}{c}
m'_j = m_{j,\text{digest}^*} \\
\forall j \in [\text{digest}] 
\end{array} \right| \begin{array}{c}
\text{crs} \leftarrow \text{crsGen}(1^\kappa) \\
(\text{digest}, \hat{D}) \leftarrow \text{Hash}(\text{crs}, D) \\
(\text{digest}^*, \hat{D}^*) \leftarrow \text{Hash}(\text{crs}, D^*) \\
e_w \leftarrow \text{SendWrite} \left( \text{crs}, \text{digest}, L, b, \{m_{j,0}, m_{j,1}\}_{j=1}^{\text{digest}} \right) \\
\{m'_j\}_{j=1}^{\text{digest}} \leftarrow \text{ReceiveWrite}^{\hat{D}}(\text{crs}, L, b, e_w)
\end{array} \right] = 1,$$

where the probability is taken over the random choices made by crsGen and SendWrite. Furthermore, we require that the execution of ReceiveWrite$^{\hat{D}}$ above updates $\hat{D}$ to $\hat{D}^*$. (Note that digest is included in $\hat{D}$, hence digest* is also updated to digest*.)

- **Sender Privacy Against Semi-Honest Receivers with Regard to Writes.** There exists a PPT simulator OTSimWrite such that the following holds. For any database $D$ of size at most $M = \text{poly}(\kappa)$ for any polynomial function $\text{poly}(\cdot)$, any memory location $L \in [M]$, any bit $b \in \{0,1\}$, and any messages $\{m_{j,0}, m_{j,1}\}_{j=1}^{\text{digest}}$
of length $\kappa$, let $\text{crs} \leftarrow \text{crsGen}(1^\kappa)$, $(\text{digest}, \hat{D}) \leftarrow \text{Hash}(\text{crs}, D)$, and $(\text{digest}^*, \hat{D}^*) \leftarrow \text{Hash}(\text{crs}, D^*)$, where $D^*$ is identical to $D$ except that $D^*[L] = b$. Then it holds that
\[
\left( \text{crs, SendWrite}(\text{crs, digest, } L, b, \{m_{j,0}, m_{j,1}\}_{j=1}^{\text{digest}}) \right) 
\approx_c 
\left( \text{crs, } \ell\text{OTSimWrite}\left( \text{crs, } D, L, b, \{m_{j,\text{digest}^*_j}\}_{j \in [\text{digest}]} \right) \right).
\]

- **Efficiency.** We require that both $\text{SendWrite}$ and $\text{ReceiveWrite}$ run in time $\text{poly}(\log |D|, \kappa)$.

### 2.3 Laconic OT with Factor-2 Compression

In this section, based on the DDH assumption we construct a laconic OT scheme for which the hash function $\text{Hash}$ compresses a database of length $2\kappa$ into a digest of length $\kappa$. We refer to this primitive as laconic OT with factor-2 compression. We note that, subsequent to our work, the factor-2 compression construction has been simplified by Döttling and Garg [DG17] (another alternative simplification can be obtained using [AIKW13]). We refer the reader to [DG17] for the simpler construction and preserve the older construction here.

In the following, we give necessary background in Section 2.3.1 and construct two primitives as building blocks: (1) a somewhere statistically binding (SSB) hash function that is friendly to hash proof system (in Section 2.3.2) and (2) a hash proof system that allows for proving knowledge of preimage bits for this SSB hash function (in Section 2.3.3). We present the $\ell$OT scheme with factor-2 compression in Section 2.3.4 and prove its security in Section 2.3.5.

#### 2.3.1 Background

In this section, we first formalize notations and computational assumptions used in our construction. We then give definitions of somewhere statistically binding (SSB) hash functions [HW15] and hash proof systems [CS98]. For simplicity, we only define SSB hash functions that compress $2\kappa$ values in the domain into $\kappa$ bits. The more general definition works analogously.

**2.3.1.1 Notations**

We start with some notations. Let $(\mathbb{G}, \cdot)$ be a cyclic group of order $p$ with generator $g$. Let $\mathbf{M} \in \mathbb{Z}_p^{m \times n}$ be a matrix. We will denote by $\hat{\mathbf{M}} = g^{\mathbf{M}} \in \mathbb{G}^{m \times n}$ the element-wise exponentiation of $g$ with the elements of $\mathbf{M}$. We also define $\hat{\mathbf{L}} = \hat{\mathbf{H}}^{\mathbf{M}} \in \mathbb{G}^{m \times k}$, where $\hat{\mathbf{H}} \in \mathbb{G}^{m \times n}$ and $\mathbf{M} \in \mathbb{Z}_p^{n \times k}$ as follows: Each element $\hat{L}_{i,j} = \prod_{k=1}^{n} \hat{H}_{i,k}^{M_{k,j}}$ (intuitively this operation corresponds to matrix multiplication in the exponent). This is well-defined and efficiently computable.
2.3.1.2 Computational Assumptions

In the following, we define the computational problems on which we base the security of our HPS-friendly SSB hash function.

Definition 2.3.1 (The Decisional Diffie-Hellman (DDH) Problem). Let $(G, \cdot)$ be a cyclic group of prime order $p$ and with generator $g$. Let $a, b, c$ be sampled uniformly at random from $\mathbb{Z}_p$ (i.e., $a, b, c \stackrel{\$}{\leftarrow} \mathbb{Z}_p$). The DDH problem asks to distinguish the distributions $(g, g^a, g^b, g^{ab})$ and $(g, g^a, g^b, g^c)$.

Definition 2.3.2 (Matrix Rank Problem). Let $m, n$ be integers and let $\mathbb{Z}_{p}^{m \times n}$ be the set of all $m \times n$ matrices over $\mathbb{Z}_p$ with rank $r$. Further, let $1 \leq r_1 < r_2 \leq \min(m, n)$. The goal of the matrix rank problem, denoted as $\text{MatrixRank}(G, m, n, r_1, r_2)$, is to distinguish the distributions $g^{M_1}$ and $g^{M_2}$, where $M_1 \stackrel{\$}{\leftarrow} \mathbb{Z}_{p}^{m \times n; r_1}$ and $M_2 \stackrel{\$}{\leftarrow} \mathbb{Z}_{p}^{m \times n; r_2}$.

In a recent result by Villar [Vil12] it was shown that the matrix rank problem can be reduced almost tightly to the DDH problem.

Theorem 2.3.3 ([Vil12] Theorem 1, simplified). Assume there exists a PPT distinguisher $D$ that solves $\text{MatrixRank}(G, m, n, r_1, r_2)$ problem with advantage $\varepsilon$. Then, there exists a PPT distinguisher $D'$ (running in almost time as $D$) that solves DDH problem over $G$ with advantage at least $\varepsilon \left\lceil \log_2(r_2/r_1) \right\rceil$.

2.3.1.3 Somewhere Statistically Binding (SSB) Hash Functions

Definition 2.3.4 (Somewhere Statistically Binding Hashing). An SSB hash function $\text{SSBH}$ consists of three algorithms $\text{crsGen}$, $\text{bindingCrsGen}$ and $\text{Hash}$ with the following syntax.

- $\text{crs} \leftarrow \text{crsGen}(1^\kappa)$. It takes the security parameter $\kappa$ as input and outputs a common reference string $\text{crs}$.

- $\text{crs} \leftarrow \text{bindingCrsGen}(1^\kappa, i)$. It takes as input the security parameter $\kappa$ and an index $i \in [2\kappa]$, and outputs a common reference string $\text{crs}$.

- $y \leftarrow \text{Hash}(\text{crs}, x)$. For some domain $\mathcal{D}$, it takes as input a common reference string $\text{crs}$ and a string $x \in \mathcal{D}^{2\kappa}$, and outputs a string $y \in \{0, 1\}^\kappa$.

We require the following properties of an SSB hash function.

- **Statistically Binding at Position $i$**. For every $i \in [2\kappa]$ and an overwhelming fraction of $\text{crs}$ in the support of $\text{bindingCrsGen}(1^\kappa, i)$ and every $x \in \mathcal{D}^{2\kappa}$, we have that $(\text{crs}, \text{Hash}(\text{crs}, x))$ uniquely determines $x_i$. More formally, for all $x' \in \mathcal{D}^{2\kappa}$ such that $x_i \neq x'_i$ we have that $\text{Hash}(\text{crs}, x') \neq \text{Hash}(\text{crs}, x)$.

- **Index Hiding**. It holds for all $i \in [2\kappa]$ that $\text{crsGen}(1^\kappa) \approx \text{bindingCrsGen}(1^\kappa, i)$, i.e., common reference strings generated by $\text{crsGen}$ and $\text{bindingCrsGen}$ are computationally indistinguishable.
2.3.1.4 Hash Proof Systems

Next, we define hash proof systems [CS98] that are designated verifier proof systems that allow for proving that the given problem instance in some language. We give the formal definition as follows.

Definition 2.3.5 (Hash Proof System). Let $L_z \subseteq M_z$ be an NP-language residing in a universe $M_z$, both parametrized by some parameter $z$. Moreover, let $L_z$ be characterized by an efficiently computable witness-relation $R$, namely, for all $x \in M_z$ it holds that $x \in L_z \iff \exists w : R(x, w) = 1$. A hash proof system $HPS$ for $L_z$ consists of three algorithms $KeyGen$, $H_{public}$ and $H_{secret}$ with the following syntax.

- $(pk, sk) \leftarrow KeyGen(1^\kappa, z)$: Takes as input the security parameter $\kappa$ and a parameter $z$, and outputs a public-key and secret key pair $(pk, sk)$.
- $y \leftarrow H_{public}(pk, x, w)$: Takes as input a public key $pk$, an instance $x \in L_z$, and a witness $w$, and outputs a value $y$.
- $y \leftarrow H_{secret}(sk, x)$: Takes as input a secret key $sk$ and an instance $x \in M_z$, and outputs a value $y$.

We require the following properties of a hash proof system.

- **Perfect Completeness.** For every $z$, every $(pk, sk)$ in the support of $KeyGen(1^\kappa, z)$, and every $x \in L_z$ with witness $w$ (i.e., $R(x, w) = 1$), it holds that
  
  $$H_{public}(pk, x, w) = H_{secret}(sk, x).$$

- **Perfect Soundness.** For every $z$ and every $x \in M_z \setminus L_z$, let $(pk, sk) \leftarrow KeyGen(1^\kappa, z)$, then it holds that
  
  $$\left( z, pk, H_{secret}(sk, x) \right) \equiv (z, pk, u),$$

  where $u$ is distributed uniformly random in the range of $H_{secret}$. Here, $\equiv$ denotes distributional equivalence.

2.3.2 HPS-Friendly SSB Hash Function

In this section, we construct an SSB hash function that supports a hash proof system. In particular, there is a hash proof system that enables proving that a certain bit of the pre-image of a hash-value has a certain fixed value (in our case, either 0 or 1).
**Construction.** Our construction builds on the scheme of Okamoto et al. [OPWW15]. We will not delve into the details of their scheme and directly jump into our construction.

Let \( n \) be an integer such that \( n = 2\kappa \), and let \( (\mathbb{G}, \cdot) \) be a cyclic group of order \( p \) and with generator \( g \). Let \( T_i \in \mathbb{Z}_p^{2 \times n} \) be a matrix which is zero everywhere except the \( i \)-th column, and the \( i \)-th column is equal to \( t = (0, 1)^	op \). The three algorithms of the SSB hash function are defined as follows.

- **crsGen(1\(^\kappa\))**: Pick a uniformly random matrix \( H \leftarrow \mathbb{Z}_p^{2 \times n} \) and output \( \hat{H} = g^H \).

- **bindingCrsGen(1\(^\kappa\), \( i \))**: Pick a uniformly random vector \( (w_1, w_2)^\top = w \leftarrow \mathbb{Z}_p^2 \) with the restriction that \( w_1 = 1 \), pick a uniformly random vector \( a \leftarrow \mathbb{Z}_p^n \) and set \( A \leftarrow w \cdot a^\top \). Set \( H \leftarrow T_i + A \) and output \( \hat{H} = g^H \).

- **Hash(crs, x)**: Parse \( x \) as a vector in \( \mathbb{D}^n \) (\( \mathbb{D} = \mathbb{Z}_p \)) and parse \( \text{crs} = \hat{H} \). Compute \( y \in \mathbb{G}^2 \) as \( y = \hat{H}^x \). Parse \( y \) as a binary string and output the result.

**Compression.** Notice that we can get factor two compression for an input space \( \{0, 1\}^{2\kappa} \) by restricting the domain to \( \mathbb{D}' = \{0, 1\} \subset \mathbb{D} \). The input length \( n = 2\kappa \), where \( \kappa \) is set to be twice the number of bits in the bit representation of a group element in \( \mathbb{G} \). In the following we assume that \( n = 2\kappa \) and that the bit-representation size of a group element in \( \mathbb{G} \) is \( \frac{\kappa}{2} \).

**Security Proof.** We first show that the distributions \( \text{crsGen}(1\(^\kappa\)) \) and \( \text{bindingCrsGen}(1\(^\kappa\), \( i \)) \) are computationally indistinguishable for every index \( i \in [n] \), given that the DDH problem is computationally hard in the group \( \mathbb{G} \).

**Lemma 2.3.6** (Index Hiding). Assume that the \( \text{MatrixRank}(\mathbb{G}, 2, n, 1, 2) \) problem is hard. Then the distributions \( \text{crsGen}(1\(^\kappa\)) \) and \( \text{bindingCrsGen}(1\(^\kappa\), \( i \)) \) are computationally indistinguishable, for every \( i \in [n] \).

**Proof.** Assume there exists a PPT distinguisher \( \mathcal{D} \) that distinguishes the distributions \( \text{crsGen}(1\(^\kappa\)) \) and \( \text{bindingCrsGen}(1\(^\kappa\), \( i \)) \) with non-negligible advantage \( \varepsilon \). We will construct a PPT distinguisher \( \mathcal{D}' \) that distinguishes \( \text{MatrixRank}(\mathbb{G}, 2, n, 1, 2) \) with non-negligible advantage.

The distinguisher \( \mathcal{D}' \) does the following on input \( \hat{M} \in \mathbb{G}^{2 \times n} \). It computes \( \hat{H} \in \mathbb{G}^{2 \times n} \) as element-wise multiplication of \( \hat{M} \) and \( g^T_i \) and runs \( \mathcal{D} \) on \( \hat{H} \). If \( \mathcal{D} \) outputs \( \text{crsGen} \), then \( \mathcal{D}' \) outputs rank 2, otherwise \( \mathcal{D}' \) outputs rank 1.

We now show that \( \mathcal{D}' \) also has non-negligible advantage. Write \( \mathcal{D}' \)'s input as \( \hat{M} = g^M \). If \( M \) is chosen uniformly random with rank 2, then \( M \) is uniform in \( \mathbb{Z}_p^{2 \times n} \) with overwhelming probability. Hence with overwhelming probability, \( M + T_i \) is also distributed uniformly random and it follows that \( \hat{H} = g^{M + T_i} \) is uniformly random in \( \mathbb{G}^{2 \times n} \) which is identical to the distribution generated by \( \text{crsGen}(1\(^\kappa\)) \). On the other hand, if \( M \) is chosen uniformly random with rank 1, then there exists a vector \( w \in \mathbb{Z}_p^n \) such that each column of \( M \) can be written as \( a_i \cdot w \). We can assume that the first element \( w_1 \) of \( w \) is 1, since the case
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$w_1 = 0$ happens only with probability $1/p = \text{negl}(\kappa)$ and if $w_1 \neq 0$ we can replace all $a_i$ by $a_i' = a_i \cdot w_1$ and replace $w_i$ by $w_i' = w_i / w_1$. Thus, we can write $M$ as $M = w \cdot a^\top$ and consequently $\hat{H}$ as $\hat{H} = g^{w \cdot a^\top + T_i}$. Notice that $a$ is uniformly distributed, hence $\hat{H}$ is identical to the distribution generated by $\text{bindingCrsGen}(1^\kappa, i)$. Since $\mathcal{D}$ can distinguish the distributions $\text{crsGen}(1^\kappa)$ and $\text{bindingCrsGen}(1^\kappa, i)$ with non-negligible advantage $\varepsilon$, $\mathcal{D}'$ can distinguish $\text{MatrixRank}(G, 2, n, 1, 2)$ with advantage $\varepsilon - \text{negl}(\kappa)$, which contradicts the hardness of $\text{MatrixRank}(G, 2, n, 1, 2)$. □

A corollary of Lemma 2.3.6 is that for all $i, j \in [n]$ the distributions $\text{bindingCrsGen}(1^\kappa, i)$ and $\text{bindingCrsGen}(1^\kappa, j)$ are computationally indistinguishable, stated as follows.

Corollary 2.3.7. Assume the $\text{MatrixRank}(G, 2, n, 1, 2)$ problem is computationally hard. Then it holds for all $i, j \in [n]$ that $\text{bindingCrsGen}(1^\kappa, i)$ and $\text{bindingCrsGen}(1^\kappa, j)$ are computationally indistinguishable.

We next show that if the common reference string $\text{crs} = \hat{H}$ is generated by $\text{bindingCrsGen}(1^\kappa, i)$, then the hash value $\text{Hash}(\text{crs}, x)$ is statistically binded to $x_i$.

Lemma 2.3.8 (Statistically Binding at Position $i$). For every $i \in [n]$, every $x \in \mathbb{Z}_p^n$, and all choices of $\text{crs}$ in the support of $\text{bindingCrsGen}(1^\kappa, i)$ we have that for every $x' \in \mathbb{Z}_p^n$ such that $x'_i \neq x_i$, $\text{Hash}(\text{crs}, x) \neq \text{Hash}(\text{crs}, x')$.

Proof. We first write $\text{crs}$ as $\hat{H} = g^H = g^{w \cdot a^\top + T_i}$ and $\text{Hash}(\text{crs}, x)$ as $\text{Hash}(\hat{H}, x) = g^y = g^{H \cdot x}$. Thus, by taking the discrete logarithm with basis $g$ our task is to demonstrate that there exists a unique $x_i$ from $H = w \cdot a^\top + T_i$ and $y = H \cdot x$. Observe that

$$y = H \cdot x = (w \cdot a^\top + T_i) \cdot x = w \cdot \langle a, x \rangle + T_i \cdot x = \left( \begin{array}{c} 1 \\ w_2 \end{array} \right) \cdot \langle a, x \rangle + \left( \begin{array}{c} 0 \\ 1 \end{array} \right) \cdot x_i,$$

where $\langle a, x \rangle$ is the inner product of $a$ and $x$. If $a \neq 0$, then we can use any non-zero element of $a$ to compute $w_2$ from $H$, and recover $x_i$ by computing $x_i = y_2 - w_2 \cdot y_1$; otherwise $a = 0$, so $x_i = y_2$. □

2.3.3 A Hash Proof System for Knowledge of Preimage Bits

In this section, we give our desired hash proof systems. In particular, we need a hash proof system for membership in a subspace of a vector space.

Construction. Fix a matrix $\hat{H} \in \mathbb{G}^{2 \times n}$ and an index $i \in [n]$. We will construct a hash proof system $\text{HPS} = (\text{KeyGen}, \text{H}_{\text{public}}, \text{H}_{\text{secret}})$ for the following language $\mathcal{L}_{\hat{H}, i}$:

$$\mathcal{L}_{\hat{H}, i} = \{ (\hat{y}, b) \in \mathbb{G}^2 \times \{0, 1\} \mid \exists x \in \mathbb{Z}_p^n \text{ s.t. } \hat{y} = \hat{H}^x \text{ and } x_i = b \}.$$
Note that in our hash proof system we only enforce that a single specified bit is $b$, where $b \in \{0, 1\}$. However, our hash proof system does not place any requirement on the value used at any of the other locations. In fact the values used at the other locations may actually be from the full domain $\mathcal{D}$ (i.e., $\mathbb{Z}_p$). Observe that the formal definition of the language $\mathcal{L}_{H,i}$ above incorporates this difference in how the honest computation of the hash function is performed and what the hash proof system is supposed to prove.

For ease of exposition, it will be convenient to work with a matrix $\hat{H}' \in \mathbb{G}_p^{3 \times n}$:

$$\hat{H}' = \begin{pmatrix} \hat{H} \\ e_i^\top \end{pmatrix},$$

where $e_i \in \mathbb{Z}_p^n$ is the $i$-th unit vector, with all elements equal to zero except the $i^{th}$ one which is equal to one.

- **KeyGen**$(1^\kappa, (\hat{H}, i))$: Choose $r \overset{\$}{\leftarrow} \mathbb{Z}_p^3$ uniformly at random. Compute $\hat{h} = (\hat{H}')^r$. Set $pk = \hat{h}$ and $sk = r$. Output $(pk, sk)$.
- **H_{public}**$(pk, (\hat{y}, b), x)$: Parse $pk$ as $\hat{h}$. Compute $\hat{z} = (\hat{h})^x$ and output $\hat{z}$.
- **H_{secret}**$(sk, (\hat{y}, b))$: Parse $sk$ as $r$ and set $\hat{y}' = (\hat{y}g_b)^r$. Compute $\hat{z} = ((\hat{y}')^r)$ and output $\hat{z}$.

**Security Proof.** In our proof we need the following technical lemma.

**Lemma 2.3.9.** Let $M \in \mathbb{Z}_p^{m \times n}$ be a matrix. Let $\text{colsp}(M) = \{M \cdot x \mid x \in \mathbb{Z}_p^n\}$ be its column space, and $\text{rowsp}(M) = \{x^\top \cdot M \mid x \in \mathbb{Z}_p^m\}$ be its row space. Assume that $y \in \mathbb{Z}_p^m$ and $y \notin \text{colsp}(M)$. Let $r \overset{\$}{\leftarrow} \mathbb{Z}_p^m$ be chosen uniformly at random. Then it holds that

$$(M, y, r^\top M, r^\top y) \equiv (M, y, r^\top M, u),$$

where $u \overset{\$}{\leftarrow} \mathbb{Z}_p$ is distributed uniformly and independently of $r$. Here, $\equiv$ denotes distributional equivalence.

**Proof.** For any $t \in \text{rowsp}(M)$ and $s \in \mathbb{Z}_p$, consider following linear equation system

$$\begin{cases} r^\top M = t \\ r^\top y = s \end{cases}.$$ 

Let $\mathcal{N}$ be the left null space of $M$. We know that $y \notin \text{colsp}(M)$, hence $M$ has rank $\leq m - 1$, therefore $\mathcal{N}$ has dimension $\geq 1$. Let $r_0$ be an arbitrary solution for $r^\top M = t$, and let $n$ be a vector in $\mathcal{N}$ such that $n^\top y \neq 0$ (there must be such a vector since $y \notin \text{colsp}(M)$). Then there exists a solution $r$ for the above linear equation system, that is,

$$r = r_0 + (n^\top y)^{-1} \cdot (s - r_0^\top y) \cdot n,$$
where \((n^\top y)^{-1}\) is the multiplicative inverse of \(n^\top y\) in \(\mathbb{Z}_p\). Then two cases arise: (i) column vectors of \((M y)\) are full-rank, or (ii) not. In this first case, there is a unique solution for \(r\). In the second case the solution space has the same size as the left null space of \((M y)\). Therefore, in both cases, the number of solutions for \(r\) is \(\frac{2^n}{n}\).

As \(r\) is chosen uniformly at random, all pairs \((t, s) \in \text{rowsp}(M) \times \mathbb{Z}_p\) have the same probability of occurrence and the claim follows.

Now we are ready to prove security.

**Theorem 2.3.10.** For every matrix \(H \in \mathbb{G}^{2 \times n}\) and every \(i \in [n]\), HPS is a hash proof system for the language \(L_{H,i}^f\).

**Proof.** Let \(H = g^H, \quad r = (r^*, r_3)\) where \(r^* \in \mathbb{Z}_p^2\). Let \(y' := \log_g y', \quad y := \log_g \hat{y}, \quad H' := \log_g \hat{H}, \quad h := \log_g \hat{h}\).

For perfect correctness, we need to show that for every \(i \in [n]\), every \(H \in \mathbb{G}^{2 \times n}\), and every \((pk, sk)\) in the support of \(\text{KeyGen}(1^n, (\hat{H}, i))\), if \((\hat{y}, b) \in L_{H,i}^f\) and \(x\) is a witness for membership (i.e., \(\hat{y} = H^x\) and \(x_i = b\)), then it holds that \(H_{\text{public}}(pk, (\hat{y}, b), x) = H_{\text{secret}}(sk, (\hat{y}, b))\).

To simplify the argument, we again consider the statement under the discrete logarithm with basis \(g\). Then it holds that

\[
\log_g \left( \text{H}_{\text{secret}}(sk, (\hat{y}, b)) \right) = \log_g \left( \langle (y')^\top \rangle \right) = \langle y', r \rangle = \langle y, r^* + b \cdot r_3 \rangle
\]

\[
= \langle H \cdot x, r^* \rangle + x_i \cdot r_3 = \langle H'x, r \rangle = \langle (H')^\top r, x \rangle
\]

\[
= \langle h, x \rangle = \log_g \left( (\hat{H})^\top x \right)
\]

\[
= \log_g \left( \text{H}_{\text{public}}(pk, (\hat{y}, b), x) \right).
\]

For perfect soundness, let \((pk, sk) \leftarrow \text{KeyGen}(1^n, (\hat{H'}, i))\). We will show that if \((\hat{y}, b) \notin L_{H,i}^f\), then \(H_{\text{secret}}(sk, (\hat{y}, b))\) is distributed uniformly random in the range of \(H_{\text{secret}}\), even given \(H, i,\) and \(pk\). Again under the discrete logarithm, this is equivalent to showing that \(\langle y', r \rangle\) is distributed uniformly random given \(H'\) and \(h = (H')^\top r\).

Note that we can re-write the language \(L_{H,i}^f = \{(\hat{y}, b) \in \mathbb{G}^2 \times \mathbb{Z}_p \mid \exists x \in \mathbb{Z}_p^n \text{ s.t. } H'x = y'\}\). It follows that if \((\hat{y}, b) \notin L_{H,i}^f\), then \(y' \notin \text{span}(H')\). Now it follows directly from Lemma 2.3.9 that

\[
\begin{align*}
r^\top y' & \equiv u \\
given H' \text{ and } r^\top H', \text{ where } u \text{ is distributed uniformly random. This concludes the proof.}
\end{align*}
\]

**Remark 2.3.11.** While proving the security of our applications based on the above hash-proof system, we would generate \(\hat{H}\) to be the output of \(\text{bindingCrsGen}(1^n, i)\) and use the property that if \((\hat{y}, b) \in L_{H,i}^f\), then \((\hat{y}, (1 - b)) \notin L_{H,i}^f\). This follows directly from Lemma 2.3.8 (that is, \(\hat{H}\) and \(\hat{y}\) uniquely fixes \(x_i\)).
2.3.4 Construction of Laconic OT with Factor-2 Compression

We are now ready to put the pieces together and provide our ℓOT scheme with factor-2 compression.

Let \( \mathbb{SSBH} = (\mathbb{SSBH} . \text{crsGen}, \mathbb{SSBH} . \text{bindingCrsGen}, \mathbb{SSBH} . \text{Hash}) \) be the HPS-friendly SSB hash function constructed in Section 2.3.2 with domain \( \mathcal{D} = \mathbb{Z}_p \). Notice that we achieve factor-2 compression (namely, compressing \( 2\kappa \) bits into \( \kappa \) bits) by restricting the domain from \( \mathcal{D}^n \) to \( \{0, 1\}^n \) in our laconic OT scheme. Also, abstractly let the associated hash proof system be \( \mathbb{HPS} = (\mathbb{HPS} . \text{KeyGen}, \mathbb{HPS} . \text{H}_{\text{public}}, \mathbb{HPS} . \text{H}_{\text{secret}}) \) for the language

\[
\mathcal{L}_{\text{crs}, i} = \{(\text{digest}, b) \in \{0, 1\}^\kappa \times \{0, 1\} \mid \exists D \in \mathcal{D}^{2\kappa} : \text{SSBH}.\text{Hash}(\text{crs}, D) = \text{digest} \text{ and } D[i] = b\}.
\]

Recall that the bit-representation size of a group element of \( \mathbb{G} \) is \( \kappa^2 \), hence the language defined above is the same as the one defined in Section 2.3.3.

Now we construct the laconic OT scheme \( \ellOT = (\text{crsGen}, \text{Hash}, \text{Send}, \text{Receive}) \) as follows.

- **crsGen\( (1^\kappa) \):** Compute \( \text{crs} \leftarrow \mathbb{SSBH} . \text{crsGen}(1^\kappa) \) and output \( \text{crs} \).

- **Hash\( (\text{crs}, D \in \{0, 1\}^{2\kappa}) \):**
  \[
  \text{digest} \leftarrow \mathbb{SSBH} . \text{Hash}(\text{crs}, D),
  \hat{D} \leftarrow (D, \text{digest}),
  \text{Output } (\text{digest}, \hat{D})
  \]

- **Send\( (\text{crs}, \text{digest}, L, m_0, m_1) \):**
  Let \( \mathbb{HPS} \) be the hash-proof system for the language \( \mathcal{L}_{\text{crs}, L} \)
  \[
  (\text{pk}, \text{sk}) \leftarrow \mathbb{HPS} . \text{KeyGen}(1^\kappa, (\text{crs}, L)),
  c_0 \leftarrow m_0 \oplus \mathbb{HPS} . \text{H}_{\text{secret}}(\text{sk}, (\text{digest}, 0)),
  c_1 \leftarrow m_1 \oplus \mathbb{HPS} . \text{H}_{\text{secret}}(\text{sk}, (\text{digest}, 1)),
  \text{Output } e = (\text{pk}, c_0, c_1)
  \]

- **Receive\( ^D (\text{crs}, e, L) \):**
  \[
  \text{Parse } e = (\text{pk}, c_0, c_1),
  \text{Parse } \hat{D} = (D, \text{digest}), \text{ and set } b \leftarrow D[L],
  m \leftarrow c_b \oplus \mathbb{HPS} . \text{H}_{\text{public}}(\text{pk}, (\text{digest}, b), D),
  \text{Output } m
  \]

2.3.5 Security Proof

We now prove that \( \ellOT \) is a laconic OT protocol with factor-2 compression, i.e., it has compression factor 2, and satisfies the correctness and sender privacy requirements. First notice that \( \mathbb{SSBH}.\text{Hash} \) is factor-2 compressing, so \( \text{Hash} \) also has compression factor 2. We next argue correctness and sender privacy in Lemmas 2.3.12 and 2.3.13, respectively.
Lemma 2.3.12. Given that HPS satisfies the correctness property, the \(\ell\)OT scheme also satisfies the correctness property.

Proof. Fix a common reference string \(\text{crs}\) in the support of \(\text{crsGen}(1^\kappa)\), a database string \(D \in \{0,1\}^{2\kappa}\) and an index \(L \in [2\kappa]\). For any \(\text{crs}, D, L\) such that \(D[L] = b\), let \(\text{digest} = \text{Hash}(\text{crs}, D)\). Then it clearly holds that \((\text{digest}, b) \in \mathcal{L}_{\text{crs},L}\). Thus, by the correctness property of the hash proof system HPS it holds that

\[
\text{HPS}.H_{\text{secret}}(\text{sk}, (\text{digest}, b)) = \text{HPS}.H_{\text{public}}(\text{pk}, (\text{digest}, b), D).
\]

By the construction of \(\text{Send}(\text{crs}, \text{digest}, L, m_0, m_1)\), \(c_b = m_b \oplus \text{HPS}.H_{\text{secret}}(\text{sk}, (\text{digest}, b))\).

Hence the output \(m\) of \(\text{Receive}^D(\text{crs}, e, L)\) is

\[
m = c_b \oplus \text{HPS}.H_{\text{public}}(\text{pk}, (\text{digest}, b), D)
\]

\[
= m_b \oplus \text{HPS}.H_{\text{secret}}(\text{sk}, (\text{digest}, b)) \oplus \text{HPS}.H_{\text{public}}(\text{pk}, (\text{digest}, b), D)
\]

\[
= m_b.
\]

Lemma 2.3.13. Given that SSBH is index-hiding and has the statistically binding property and that HPS is sound, then the \(\ell\)OT scheme satisfies sender privacy against semi-honest receiver.

Proof. We first construct the simulator \(\ell\text{OTSIm}\).

\(\ell\text{OTSIm}(\text{crs}, D, L, m_{D[L]}):\)

\[
\text{digest} \leftarrow \text{SSBH}.\text{Hash}(\text{crs}, D)
\]

Let HPS be the hash-proof system for the language \(\mathcal{L}_{\text{crs},L}\)

\[
(\text{pk}, \text{sk}) \leftarrow \text{HPS}.\text{KeyGen}(1^\kappa, (\text{crs}, L))
\]

\[c_0 \leftarrow m_{D[L]} \oplus \text{HPS}.H_{\text{secret}}(\text{sk}, (\text{digest}, 0))\]

\[c_1 \leftarrow m_{D[L]} \oplus \text{HPS}.H_{\text{secret}}(\text{sk}, (\text{digest}, 1))\]

Output \((\text{pk}, c_0, c_1)\)

For any database \(D\) of size at most \(M = \text{poly}(\kappa)\) for any polynomial function \(\text{poly}(\cdot)\), any memory location \(L \in [M]\), and any pair of messages \((m_0, m_1) \in \{0,1\}^\kappa \times \{0,1\}^\kappa\), let \(\text{crs} \leftarrow \text{crsGen}(1^\kappa)\) and \(\text{digest} \leftarrow \text{Hash}(\text{crs}, D)\). Then we will prove that the two distributions \((\text{crs}, \text{Send}(\text{crs}, \text{digest}, L, m_0, m_1))\) and \((\text{crs}, \ell\text{OTSIm}(\text{crs}, D, L, m_{D[L]}))\) are computationally indistinguishable. Consider the following hybrids.

- Hybrid 0: This is the real experiment, namely \((\text{crs}, \text{Send}(\text{crs}, \text{digest}, L, m_0, m_1))\).
- Hybrid 1: Same as hybrid 0, except that \(\text{crs}\) is computed by \(\text{crs} \leftarrow \text{SSBH}.\text{bindingCrsGen}(1^\kappa, L)\).
- Hybrid 2: Same as hybrid 1, except that \(c_1 - D[L]\) is computed by \(c_1 - D[L] \leftarrow m_{D[L]} \oplus \text{HPS}.H_{\text{secret}}(\text{sk}, (\text{digest}, 1 - D[L]))\). That is, both \(c_0\) and \(c_1\) encrypt the same message \(m_{D[L]}\).
• Hybrid 3: Same as hybrid 2, except that crs is computed by \( \text{crs} \leftarrow \text{SSBH.crsGen}(1^\kappa) \). This is the simulated experiment, namely \((\text{crs}, \ell\text{OTSim}((\text{crs}, D, L, m_D[L])))\).

Indistinguishability of hybrid 0 and hybrid 1 follows directly from Lemma 2.3.6, as we replace the distribution of crs from \( \text{SSBH.crsGen}(1^\kappa) \) to \( \text{SSBH.bindingCrsGen}(1^\kappa, i) \). Indistinguishability of hybrids 2 and 3 also follows from Lemma 2.3.6, as we replace the distribution of crs from \( \text{SSBH.bindingCrsGen}(1^\kappa, L) \) back to \( \text{SSBH.crsGen}(1^\kappa) \).

We now show that hybrids 1 and 2 are identically distributed. Since crs is in the support of \( \text{SSBH.bindingCrsGen}(1^\kappa, i) \) and digest = \( \text{SSBH.Hash}(\text{crs}, D) \), by Lemma 2.3.8 it holds that \((\text{digest}, 1 - D[L]) \notin \mathcal{L}_{\text{crs}, L}\). By the soundness property of the hash-proof system HPS, it holds that

\[
(\text{crs}, L, \text{pk}, \text{HPS.H_secret}(\text{sk}, (\text{digest}, 1 - D[L]))) \equiv (\text{crs}, L, \text{pk}, u),
\]

for a uniformly random \( u \). Furthermore, \( c_D[L] \) can be computed by \( m_D[L] \oplus \text{HPS.H_public}(\text{pk}, (\text{digest}, D[L]), D) \). Hence

\[
(\text{crs}, L, \text{pk}, m_D[L] \oplus \text{HPS.H_secret}(\text{sk}, (\text{digest}, 1 - D[L])), c_{D[L]}) \\
\equiv (\text{crs}, L, \text{pk}, u, c_{D[L]}) \\
\equiv (\text{crs}, L, \text{pk}, m_{1-D[L]} \oplus \text{HPS.H_secret}(\text{sk}, (\text{digest}, 1 - D[L])), c_{D[L]}).
\]

This concludes the proof. \( \square \)

2.4 Laconic OT with Arbitrary Compression

In this section, we construct an updatable laconic OT that supports a hash function that allows for compression from an input (database) of size an arbitrary polynomial in \( \kappa \) to \( \kappa \) bits. As every updatable laconic OT protocol is also a (standard) laconic OT protocol, we will only construct the former. Our main technique in this construction, is the use of garbled circuits to bootstrap a laconic OT with factor-2 compression into one with an arbitrary compression factor.

Below in Section 2.4.1 we describe some background on the primitives needed for realizing our laconic OT construction. Then we give the construction of laconic OT along with its correctness and security proofs in Sections 2.4.2 and 2.4.3, respectively.

2.4.1 Background

In this section we recall the needed background of garbled circuits and Merkle trees.

2.4.1.1 Garbled Circuits

Garbled circuits were first introduced by Yao [Yao82; Yao86] (see Lindell and Pinkas [LP09] and Bellare et al. [BHR12] for a detailed proof and further discussion). A circuit garbling...
scheme GC is a tuple of PPT algorithms (GCircuit, GEval). Very roughly GCircuit is the circuit garbling procedure and GEval the corresponding evaluation procedure. Looking ahead, each individual wire \( w \) of the circuit being garbled will be associated with two labels, namely \( \text{lab}_{w,0}, \text{lab}_{w,1} \).

- \( \tilde{C} \leftarrow \text{GCircuit} \left( 1^\kappa, C, \{\text{lab}_{w,b}\}_{w \in \text{inp}(C), b \in \{0,1\}} \right) \): GCircuit takes as input a security parameter \( \kappa \), a circuit \( C \), and a set of labels \( \text{lab}_{w,b} \) for all the input wires \( w \in \text{inp}(C) \) and \( b \in \{0,1\} \). This procedure outputs a garbled circuit \( \tilde{C} \).

- \( y \leftarrow \text{GEval} \left( \tilde{C}, \{\text{lab}_{w,x_w}\}_{w \in \text{inp}(C)} \right) \): Given a garbled circuit \( \tilde{C} \) and a garbled input represented as a sequence of input labels \( \{\text{lab}_{w,x_w}\}_{w \in \text{inp}(C)} \), GEval outputs \( y \).

**Correctness.** For correctness, we require that for any circuit \( C \) and input \( x \in \{0,1\}^m \) (here \( m \) is the input length to \( C \)) we have that:

\[
\Pr \left[ C(x) = \text{GEval} \left( \tilde{C}, \{\text{lab}_{w,x_w}\}_{w \in \text{inp}(C)} \right) \right] = 1
\]

where \( \tilde{C} \leftarrow \text{GCircuit} \left( 1^\kappa, C, \{\text{lab}_{w,b}\}_{w \in \text{inp}(C), b \in \{0,1\}} \right) \).

**Security.** For security, we require that there is a PPT simulator \( \text{CircSim} \) such that for any \( C, x, \) and uniformly random keys \( \{\text{lab}_{w,b}\}_{w \in \text{inp}(C), b \in \{0,1\}} \), we have that

\[
\left( \tilde{C}, \{\text{lab}_{w,x_w}\}_{w \in \text{inp}(C)} \right) \overset{\varepsilon}{\approx} \text{CircSim} \left( 1^\kappa, C, y \right)
\]

where \( \tilde{C} \leftarrow \text{GCircuit} \left( 1^\kappa, C, \{\text{lab}_{w,b}\}_{w \in \text{inp}(C), b \in \{0,1\}} \right) \) and \( y = C(x) \).

**Terminology of Keys and Labels.** We use the notation \text{Keys} to refer to both the secret values sampled for wires and the notation \text{Labels} to refer to exactly one of them. In other words, generation of garbled circuit involves \text{Keys} while computation itself depends just on \text{Labels}. Let \( \text{Keys} = ((\text{lab}_{1,0}, \text{lab}_{1,1}), \ldots, (\text{lab}_{n,0}, \text{lab}_{n,1})) \) be a list of \( n \) key-pairs, we denote \( \text{Keys}_x \) for a string \( x \in \{0,1\}^n \) to be a list of labels \((\text{lab}_{1,x_1}, \ldots, \text{lab}_{n,x_n})\).

### 2.4.1.2 Merkle Tree

In this section we briefly review Merkle trees. A Merkle tree is a hash based data structure that generically extend the domain of a hash function. The following description will be tailored to the hash function of the laconic OT scheme that we will present in Section 2.4.2. Given a two-to-one hash function \( \text{Hash} : \{0,1\}^{2\kappa} \to \{0,1\}^\kappa \), we can use a Merkle tree to construct a hash function that compresses a database of an arbitrary (a priori unbounded polynomial in \( \kappa \)) size to a \( \kappa \)-bit string. Now we briefly illustrate how to compress a database \( D \in \{0,1\}^M \) (assume for ease of exposition that \( M = 2^d \cdot \kappa \)). First, we partition \( D \) into
strings of length $2\kappa$; we call each string a leaf. Then we use Hash to compress each leaf into a new string of length $\kappa$; we call each string a node. Next, we bundle the new nodes in pairs of two and call these pairs siblings, i.e., each pair of siblings is a string of length $2\kappa$. We then use Hash again to compress each pair of siblings into a new node of size $\kappa$. We continue the process till a single node of size $\kappa$ is obtained. This process forms a binary tree structure, which we refer to as a Merkle tree. Looking ahead, the hash function of the laconic OT scheme has output $(\hat{D}, \text{digest})$, where $\hat{D}$ is the entire Merkle tree, and digest is the root of the tree.

A Merkle tree has the following property. In order to verify that a database $D$ with hash root digest has a certain value $b$ at a location $L$ (namely, $D[L] = b$), there is no need to provide the entire Merkle tree. Instead, it is sufficient to provide a path of siblings from the Merkle tree root to the leaf that contains location $L$. It can then be easily verified if the hash values from the leaf to the root are correct.

Moreover, a Merkle tree can be updated in the same fashion when the value at a certain location of the database is updated. Instead of recomputing the entire tree, we only need to recompute the nodes on the path from the updated leaf to the root. This can be done given the path of siblings from the root to the leaf.

### 2.4.2 Construction of Updatable Laconic OT with Arbitrary Compression

We now present our construction to bootstrap an $\ell$OT scheme with factor-2 compression into an updatable $\ell$OT scheme with an arbitrary compression factor, which can compress a database of an arbitrary (a priori unbounded polynomial in $\kappa$) size.

**Overview.** We first give an overview of the construction. Consider a database $D \in \{0, 1\}^M$ such that $M = 2^d \cdot \kappa$. Given a laconic OT scheme with factor-2 compression (denoted as $\ell$OT$_{\text{const}}$), we will first use a Merkle tree to obtain a hash function with arbitrary (polynomial) compression factor. As described in Section 2.4.1.2, the Hash function of the updatable $\ell$OT scheme will have an output $(\hat{D}, \text{digest})$, where $\hat{D}$ is the entire Merkle tree, and digest is the root of the tree.

In the Send algorithm, suppose we want to send a message depending on a bit $D[L]$, we will follow the natural approach of traversing the Merkle tree layer by layer until reaching the leaf containing $L$. In particular, $L$ can be represented as $L = (b_1, \ldots, b_{d-1}, t)$, where $b_1, \ldots, b_{d-1}$ are bits representing the path from the root to the leaf containing location $L$, and $t \in [2\kappa]$ is the position within the leaf. The Send algorithm first takes as input the root digest of the Merkle tree, and it will generate a chain of garbled circuits, which would enable the receiver to traverse the Merkle tree from the root to the leaf. And upon reaching the leaf, the receiver will be able to evaluate the last garbled circuit and retrieve the message corresponding to the $t$-th bit of the leaf.
We briefly explain the chain of garbled circuits as follows. The chain consists of $d - 1$ traversing circuits along with a reading circuit. Every traversing circuit takes as input a pair of siblings $sbl = (sbl_0, sbl_1)$ at a certain layer of the Merkle tree, chooses $sbl_b$ which is the node in the path from root to leaf, and generates a laconic OT ciphertext (using $\ell_{OT_{\text{const}}, \text{Send}}$) which encrypts the input keys of the next traversing garbled circuit and uses $sbl_b$ as the hash value. Looking ahead, when the receiver evaluates the traversing circuit and obtains the laconic OT ciphertext, he can then use the siblings at the next layer to decrypt the ciphertext (by $\ell_{OT_{\text{const}}, \text{Receive}}$) and obtain the corresponding input labels for the next traversing garbled circuit. Using the chain of traversing garbled circuits the receiver can therefore traverse from the first layer to the leaf of the Merkle tree. Furthermore, the correct keys for the first traversing circuit are sent via the $\ell_{OT_{\text{const}}}$ with digest (i.e., root of the tree) as the hash value.

Finally, the last traversing circuit will transfer keys for the last reading circuit to the receiver in a similar fashion as above. The reading circuit takes the leaf as input and outputs $m_{\text{leaf}[t]}$, i.e., the message corresponding to the $t$-th bit of the leaf. Hence, when evaluating the reading circuit, the receiver can obtain the message $m_{\text{leaf}[t]}$.

$\text{SendWrite}$ and $\text{ReceiveWrite}$ are similar as $\text{Send}$ and $\text{Receive}$, except that (a) $\text{ReceiveWrite}$ updates the Merkle tree from the leaf to the root, and (b) the last writing circuit recomputes the root of the Merkle tree and outputs messages corresponding to the new root. To enable (b), the writing circuit will take as input the whole path of siblings from the root to the leaf. The input keys for the writing circuit corresponding to the siblings at the $(i+1)$-th layer are transferred via the $i$-th traversing circuit. That is, the $i$-th traversing circuit transfers the keys for the $(i+1)$-th transferring circuit as well as partial keys for the writing circuit. In the actual construction, both the reading circuit and writing circuit take as input the entire path of siblings (for the purpose of symmetry).

**Construction.** Let $\text{GC} = (\text{GCircuit}, \text{GEval})$ be a circuit garbling scheme. Let $\ell_{OT_{\text{const}}} = (\ell_{OT_{\text{const}}}\cdot \text{crsGen}, \ell_{OT_{\text{const}}}\cdot \text{Hash}, \ell_{OT_{\text{const}}}\cdot \text{Send}, \ell_{OT_{\text{const}}}\cdot \text{Receive})$ be a laconic OT protocol with factor-2 compression. Without loss of generality, let $D \in \{0, 1\}^M$ be a database such that $|M| = 2^d \cdot \kappa$. A location $L \in [M]$ can be represented as $(b_1, b_2, \ldots, b_{d-1}, t) \in \{0, 1\}^{d-1} \times [2\kappa]$, where the bits $b_i$'s define the path from the root to a leaf in the Merkle tree, and $t \in [2\kappa]$ defines a position in that leaf.

Before delving into the construction, we first describe three gadget circuits: the traversing circuit $\text{C}_{\text{trav}}$, the reading circuit $\text{C}_{\text{read}}$, and the writing circuit $\text{C}_{\text{write}}$. These circuits are defined formally in Figures 2.2, 2.3, and 2.4, respectively.

The traversing circuit has hardwired inside it a common reference string $\text{crs}$, a bit $b$ and two vectors of input keys $\text{Keys, Keys}$, each containing $2\kappa$ key-pairs (a key-pair is a pair of $\kappa$-bit strings). It takes as input a pair of siblings $sbl = (sbl_0, sbl_1)$, each of length $\kappa$, and generates two laconic OT $\text{Send}$ messages with $sbl_b$ as the digest and $\text{Keys, Keys}$ as message vectors respectively. Further, it also has the randomness needed for $\ell_{OT_{\text{const}}, \text{Send}}$ hardwired inside it.
CHAPTER 2. LACONIC OBLIVIOUS TRANSFER

The reading circuit $C_{\text{read}}$ has a location $t \in [2\kappa]$, and messages $m_0, m_1 \in \{0, 1\}^\kappa$ hardwired inside it. It takes as input a path of siblings from the root to a leaf, reads the $t$-th bit of the leaf, and outputs either $m_0$ or $m_1$ depending on that bit.

The writing circuit $C_{\text{write}}$ has hardwired inside it a common reference string $\text{crs}$, a location $L \in [M]$, a bit $b$ and a vector of messages $\text{Keys}$ consisting of $\kappa$ key-pairs. It takes as input a path of siblings from the root to a leaf, changes the $t$-th bit of the leaf to $b$ (where $L$ corresponds to $t$-th location in the leaf), recomputes the Merkle tree root along the path, and outputs the corresponding labels for the new root/digest.

\begin{figure}[h]
\centering
\begin{tabular}{|l|}
\hline
\textbf{Circuit $C_{\text{trav}}$} \\
\hline
\textbf{Hardwired Values:} $\text{crs}, b, \text{Keys, } \tilde{\text{Keys}}, r, \tilde{r}$ \\
\hline
\textbf{Input:} $\text{sbl}$ \\
\hline
Parse $\text{sbl}$ as $(\text{sbl}_0, \text{sbl}_1)$ \\
$e \leftarrow \ell\text{OT}_{\text{const}}.\text{Send}(\text{crs, sbl}_0, \text{Keys}; r)$ \\
$\tilde{e} \leftarrow \ell\text{OT}_{\text{const}}.\text{Send}(\text{crs, sbl}_0, \tilde{\text{Keys}}; \tilde{r})$ \\
Output $(e, \tilde{e})$ \\
\hline
\end{tabular}
\caption{Description of the traversing circuit $C_{\text{trav}}[\text{crs, b, Keys, } \tilde{\text{Keys}}, r, \tilde{r}]$.}
\end{figure}

\begin{figure}[h]
\centering
\begin{tabular}{|l|}
\hline
\textbf{Circuit $C_{\text{read}}$} \\
\hline
\textbf{Hardwired Values:} $t, m_0, m_1$ \\
\hline
\textbf{Input:} path \\
\hline
Parse $\text{path} = (\text{sbl}_1, \ldots, \text{sbl}_{d-1}, \text{leaf})$ \\
Output $m_{\text{leaf}[t]}$ \\
\hline
\end{tabular}
\caption{Description of the reading circuit $C_{\text{read}}[t, m_0, m_1]$.}
\end{figure}

\begin{figure}[h]
\centering
\begin{tabular}{|l|}
\hline
\textbf{Circuit $C_{\text{write}}$} \\
\hline
\textbf{Hardwired Values:} $\text{crs, L, b, Keys}$ \\
\hline
\textbf{Input:} path \\
\hline
Parse $L = (b_1, b_2, \ldots, b_{d-1}, t)$ \\
Parse $\text{path} = (\text{sbl}_1, \ldots, \text{sbl}_{d-1}, \text{leaf})$, and parse $\text{sbl}_i = (\text{sbl}_i^0, \text{sbl}_i^1)$ for $i \in [d - 1]$ \\
$\text{leaf}[t] \leftarrow b$ \\
$\text{sbl}_d \leftarrow \text{leaf}$ \\
For $i = d - 1$ downto 1: \\
$\text{sbl}_i^b \leftarrow \ell\text{OT}_{\text{const}}.\text{Hash}(\text{crs, sbl}_{i+1})$ \\
$\text{digest}^* \leftarrow \ell\text{OT}_{\text{const}}.\text{Hash}(\text{crs, sbl}_1)$. \\
Output $\text{Keys}_{\text{digest}}^*$ \\
\hline
\end{tabular}
\caption{Description of the writing circuit $C_{\text{write}}[\text{crs, L, b, Keys}]$.}
\end{figure}
Now we construct the updatable $\ellOT$, namely $(\text{crsGen}, \text{Hash}, \text{Send}, \text{Receive}, \text{SendWrite}, \text{ReceiveWrite})$ as follows.

- $\text{crsGen}(1^k)$: Sample $\text{crs} \leftarrow \ellOT_{\text{const}}.\text{crsGen}(1^k)$ and output $\text{crs}$.

- $\text{Hash}(\text{crs}, D \in \{0, 1\}^M)$:
  
  Build a Merkle tree $\hat{D}$ of $D$ using $\ellOT_{\text{const}}.\text{Hash}(\text{crs}, \cdot)$, as in Section 2.4.1.2.
  
  Let $\text{digest}$ be the root of $\hat{D}$.
  Output $(\text{digest}, \hat{D})$.

- $\text{Send}(\text{crs}, \text{digest}, L, m_0, m_1)$:
  
  Parse $L = (b_1, b_2, \ldots, b_{d-1}, t)$.
  Pick $(\text{Keys}_1, \ldots, \text{Keys}_d)$ as input keys for $C^{\text{read}}$,
  where $\text{Keys}_i$ corresponds to the input keys of $sbl^i$ for $i \in [d-1]$,
  and $\text{Keys}_d$ corresponds to the input keys of $\text{leaf}$.
  $\tilde{C}^{\text{read}} \leftarrow \text{GCircuit}\left(1^k, C^{\text{read}}[t, m_0, m_1], \left(\text{Keys}_1, \ldots, \text{Keys}_d\right)\right)$
  Let $\text{Keys}_d$ be 0$^*$
  For $i = d - 1$ downto 1:
    Pick $\text{Keys}_i^i$ as input keys for $C^{\text{trav}}$
    Pick $\tilde{r}_i, \tilde{r}_i$ as random coins for $\ellOT_{\text{const}}.\text{Send}$
    $\tilde{C}_i \leftarrow \text{GCircuit}\left(1^k, C^{\text{trav}}[\text{crs}, b_i, \text{Keys}^i_{i+1}, \text{Keys}^{i+1}, \tilde{r}_i, \tilde{r}_i], \text{Keys}_i\right)$
  $e_0 \leftarrow \ellOT_{\text{const}}.\text{Send}(\text{crs}, \text{digest}, \text{Keys}_1)$
  $\tilde{e}_0 \leftarrow \ellOT_{\text{const}}.\text{Send}(\text{crs}, \text{digest}, \tilde{C}^{\text{read}})$
  Output $e = (e_0, \tilde{e}_0, \tilde{C}_1, \ldots, \tilde{C}_{d-1}, \tilde{C}^{\text{read}})$

- $\text{Receive}^d(\text{crs}, L, e)$:
  
  Parse $e = (e_0, \tilde{e}_0, \tilde{C}_1, \ldots, \tilde{C}_{d-1}, \tilde{C}^{\text{read}})$
  Parse $L = (b_1, b_2, \ldots, b_{d-1}, t)$
  Parse $\hat{D}$ as a Merkle tree.
  Denote the end node of path $b_1b_2\ldots b_i$ by $\hat{D}_{b_1b_2\ldots b_i}$.
  For $i = 1$ to $d - 1$:
    $\text{sbl}_i^i \leftarrow (\hat{D}_{b_1\ldots b_{i-1}0}, \hat{D}_{b_1\ldots b_{i-1}1})$
    $\text{Labels}_i^i \leftarrow \ellOT_{\text{const}}.\text{Receive}(\text{crs}, e_{i-1}, \text{sbl}_i^i)$
    $\text{Labels} \leftarrow \ellOT_{\text{const}}.\text{Receive}(\text{crs}, \tilde{e}_{i-1}, \text{sbl}_i^i)$
    $(e_i, \tilde{e}_i) \leftarrow \text{GEval}(\tilde{C}_i, \text{Labels}^i)$
    $\text{leaf} \leftarrow (\hat{D}_{b_1\ldots b_{d-1}0}, \hat{D}_{b_1\ldots b_{d-1}1})$
    $\text{Labels} \leftarrow \ellOT_{\text{const}}.\text{Receive}(\text{crs}, \tilde{e}_{d-1}, \text{leaf})$
    $m \leftarrow \text{GEval}\left(\tilde{C}^{\text{read}}, \left(\text{Labels}^{d-1}, \text{Labels}^d\right)\right)$
Output $m$

- **SendWrite(crs, digest, $L$, $b$, $\{m_{j,0}, m_{j,1}\}_{j=1}^\kappa$):**
  
  Parse $L = (b_1, b_2, \ldots, b_{d-1}, t)$.
  
  Pick $(\tilde{\text{Keys}}^1, \ldots, \tilde{\text{Keys}}^d)$ as input keys for $\text{C}_{\text{write}}$,
  
  where $\tilde{\text{Keys}}^i$ corresponds to the input keys of $sbl^i$ for $i \in [d-1]$, and $\tilde{\text{Keys}}^d$ corresponds to the input keys of leaf.
  
  $\tilde{\text{C}}_{\text{write}} \leftarrow \text{GCircuit}(1^\kappa, \text{C}_{\text{write}}[\text{crs}, L, b, \{m_{j,0}, m_{j,1}\}_{j=1}^\kappa], (\tilde{\text{Keys}}^1, \ldots, \tilde{\text{Keys}}^d))$
  
  Let $\text{Keys}^d$ be $0^*$
  
  For $i = d - 1$ downto 1:
  
  Pick $\text{Keys}^i$ as input keys for $\text{C}_{\text{trav}}$
  
  Pick $r_i, \tilde{r}_i$ as random coins for $\ell \text{OT}_{\text{const}}$-Send
  
  $\tilde{C}_i \leftarrow \text{GCircuit}(1^\kappa, \text{C}_{\text{trav}}[\text{crs}, b_i, \text{Keys}^{i+1}, \text{Keys}^{d-i+1}, r_i, \tilde{r}_i, \text{Keys}^i])$
  
  $e_0 \leftarrow \ell \text{OT}_{\text{const}}$-Send($\text{crs}, \text{digest}, \text{Keys}^1$)
  
  $\tilde{e}_0 \leftarrow \ell \text{OT}_{\text{const}}$-Send($\text{crs}, \text{digest}, \tilde{\text{Keys}}^1$)
  
  Output $e_w = (e_0, \tilde{e}_0, \tilde{C}_1, \ldots, \tilde{C}_{d-1}, \tilde{\text{C}}_{\text{write}})$

- **ReceiveWrite$(\hat{D}, \text{L}, \text{L}, e_w)$:**
  
  Parse $e_w = (e_0, \tilde{e}_0, \tilde{C}_1, \ldots, \tilde{C}_{d-1}, \tilde{\text{C}}_{\text{write}})$
  
  Parse $L = (b_1, b_2, \ldots, b_{d-1}, t)$
  
  Parse $\hat{D}$ as a Merkle tree.
  
  Denote the end node of path $b_1 b_2 \ldots b_i$ by $\hat{D}_{b_1 b_2 \ldots b_i}$.

**Computing messages corresponding to the new digest:**

For $i = 1$ to $d - 1$:

  $sbl^i \leftarrow (\hat{D}_{b_1 \ldots b_{i-1} 0}, \hat{D}_{b_1 \ldots b_{i-1} 1})$
  
  $\text{Labels}^i \leftarrow \ell \text{OT}_{\text{const}}$-Receive($\text{crs}, e_{i-1}, sbl^i$)
  
  $\tilde{\text{Labels}} \leftarrow \ell \text{OT}_{\text{const}}$-Receive($\text{crs}, \tilde{e}_{i-1}, sbl^i$)
  
  $(e_i, \tilde{e}_i) \leftarrow \text{GEval}(\tilde{C}_i, \text{Labels}^i)$
  
  $\text{leaf} \leftarrow (\hat{D}_{b_1 \ldots b_{d-1} 0}, \hat{D}_{b_1 \ldots b_{d-1} 1})$
  
  $\tilde{\text{Labels}} \leftarrow \ell \text{OT}_{\text{const}}$-Receive($\text{crs}, \tilde{e}_{d-1}, \text{leaf}$)
  
  $\{m_j\}_{j=1}^\kappa \leftarrow \text{GEval}(\tilde{\text{C}}_{\text{write}}, (\tilde{\text{Labels}}^1, \ldots, \tilde{\text{Labels}}^d))$

**Updating the Merkle tree:**

$(\hat{D}_{b_1 \ldots b_{d-1} 0} || \hat{D}_{b_1 \ldots b_{d-1} 1})[t] \leftarrow b$

For $i = d - 1$ downto 0:

$\hat{D}_{b_1 \ldots b_i} \leftarrow \ell \text{OT}_{\text{const}}$-Hash($\text{crs}, \hat{D}_{b_1 \ldots b_i 0} || \hat{D}_{b_1 \ldots b_i 1}$)

Update digest with the new root of $\hat{D}$
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Output \( \{m_j\}_{j=1}^\kappa \)

**Efficiency.** It can be seen from the scheme that the length of digest is \( \kappa \). The algorithm Hash runs in time \( |D| \cdot \text{poly}(\log |D|, \kappa) \). Furthermore, Send, Receive, SendWrite, SendWrite all run in time \( \text{poly}(\log |D|, \kappa) \).

**Correctness.** We briefly argue (perfect) correctness of the updatable laconic OT scheme. Given a ciphertext \( e = (e_0, \tilde{e}_0, \tilde{C}_1, \ldots, \tilde{C}_{d-1}, \tilde{c}_\text{read}) \) computed by Send, correctness of \( \ell \text{OT}_{\text{const}} \) ensures that Labels\(^1\) \( \leftarrow \ell \text{OT}_{\text{const}} \cdot \text{Receive}(\text{crs}, e_0, \text{sbl}^1) \) outputs the correct labels for \( \tilde{C}_1 \) and that Labels \( \leftarrow \ell \text{OT}_{\text{const}} \cdot \text{Receive}(\text{crs}, \tilde{e}_0, \text{sbl}^1) \) outputs the correct labels for \( \tilde{c}_\text{read} \), namely Labels\(^1\) = Keys\(_{\text{sbl}^1}\) and Labels = Keys\(_{\text{sbl}^1}\). In turn, correctness of the garbling scheme guarantees that \( \tilde{C}_1 \) outputs the correct \( (e_1, \tilde{e}_1) \), namely \( e_1 = \ell \text{OT}_{\text{const}} \cdot \text{Send}(\text{crs}, \text{sbl}^1, \text{Keys}^2; r_1) \) and \( \tilde{e}_1 = \ell \text{OT}_{\text{const}} \cdot \text{Send}(\text{crs}, \text{sbl}^1, \text{Keys}^2; \tilde{r}_1) \). It follows inductively that for every \( i = 1, 2, \ldots, d-1 \), Labels\(^i\) = Keys\(_{\text{sbl}^i}\), Labels = Keys\(_{\text{sbl}^i}\), \( e_i = \ell \text{OT}_{\text{const}} \cdot \text{Send}(\text{crs}, \text{sbl}^i, \text{Keys}^{i+1}; r_i) \), and \( \tilde{e}_i = \ell \text{OT}_{\text{const}} \cdot \text{Send}(\text{crs}, \text{sbl}^i, \text{Keys}^{i+1}; \tilde{r}_i) \). Again by using the correctness of \( \ell \text{OT}_{\text{const}} \), Labels \( \leftarrow \ell \text{OT}_{\text{const}} \cdot \text{Receive}(\text{crs}, \tilde{e}_{d-1}, \text{leaf}) \) gives Labels\(^d\) = Keys\(_{\text{leaf}}\). Then by using correctness of the garbling scheme it follows that evaluating \( \tilde{c}_\text{read} \) gives the correct output \( m_{D[L]} \). Correctness with regard to writes can be argued analogously.

**2.4.3 Security Proof**

In this section, we prove the security of the above updatable laconic OT scheme.

**Theorem 2.4.1** (Sender Privacy against Semi-honest Receivers). *Given that \( \ell \text{OT}_{\text{const}} \) has sender privacy and that the garbled circuit scheme \( \text{GCircuit} \) is secure, the updatable laconic OT scheme \( \ell \text{OT} \) has sender privacy.*

*Proof.* Let \( \ell \text{OTSim}^\text{const} \) be the simulator for \( \ell \text{OT}_{\text{const}} \) and \( \text{CircSim} \) be the simulator for the garbling scheme \( \text{GCircuit} \). Below, we provide the two simulators \( \ell \text{OTSim} \) for a read and \( \ell \text{OTSimWrite} \) for the write.

- \( \ell \text{OTSim}(\text{crs}, D, L, m) \):
  \[(\text{digest}, \hat{D}) \leftarrow \text{Hash(\text{crs}, D)} \]
  \[\text{Parse } L = (b_1, b_2, \ldots, b_{d-1}, t)\]
  \[\left( \tilde{C}_\text{read}, \left( \tilde{\text{Labels}}^1, \ldots, \tilde{\text{Labels}}^d \right) \right) \leftarrow \text{CircSim}(1^\kappa, \tilde{c}_\text{read}, m)\]
  \[\text{leaf} \leftarrow (\hat{D}_{b_1 \ldots b_{d-1} 0}, \hat{D}_{b_1 \ldots b_{d-1} 1})\]
  \[e_{d-1} \leftarrow \ell \text{OTSim}^\text{const}(\text{crs}, \text{leaf}, 0^* )\]
\[ \tilde{e}_{d-1} \leftarrow \ell \text{OTSim}_{\text{const}} \left( \text{crs}, \text{leaf}, \text{Labels}^d \right) \]

For \( i = d - 1 \) downto 1:

\[ (\tilde{C}_i, \text{Labels}^i) \leftarrow \text{CircSim}(1^\kappa, \text{C}^{\text{trav}}, (e_i, \tilde{e}_i)) \]

\[ \text{sbl}^i \leftarrow (\tilde{D}_{b_i \ldots b_{i+1}}, \tilde{D}_{b_i \ldots b_{i+1}}) \]

\[ e_{i-1} \leftarrow \ell \text{OTSim}_{\text{const}} \left( \text{crs}, \text{sbl}^i, \text{Labels}^i \right) \]

\[ \tilde{e}_{i-1} \leftarrow \ell \text{OTSim}_{\text{const}} \left( \text{crs}, \text{sbl}^i, \text{Labels}^i \right) \]

Output \( e = (e_0, \tilde{e}_0, \tilde{C}_1, \ldots, \tilde{C}_{d-1}, \tilde{\text{c}}_{\text{read}}) \)

- \( \ell \text{OTSimWrite}(\text{crs}, D, L, b, \{mj\}_{j=1}^\kappa) \):
  
  \[ (\text{digest}, \tilde{D}) \leftarrow \text{Hash}(\text{crs}, D) \]
  
  Parse \( L = (b_1, b_2, \ldots, b_{d-1}, t) \)

  \[ \left( \tilde{\text{write}}^\text{write}, \left( \text{Labels}^1, \ldots, \text{Labels}^d \right) \right) \leftarrow \text{CircSim}(1^\kappa, \text{C}^{\text{write}}, \{mj\}_{j=1}^\kappa) \]

  \[ \text{leaf} \leftarrow (\tilde{D}_{b_1 \ldots b_{d-1}}, \tilde{D}_{b_1 \ldots b_{d-1}}) \]

  \[ e_{d-1} \leftarrow \ell \text{OTSim}_{\text{const}} \left( \text{crs}, \text{leaf}, 0^* \right) \]

  \[ \tilde{e}_{d-1} \leftarrow \ell \text{OTSim}_{\text{const}} \left( \text{crs}, \text{leaf}, \text{Labels}^d \right) \]

For \( i = d - 1 \) downto 1:

\[ (\tilde{C}_i, \text{Labels}^i) \leftarrow \text{CircSim}(1^\kappa, \text{C}^{\text{trav}}, (e_i, \tilde{e}_i)) \]

\[ \text{sbl}^i \leftarrow (\tilde{D}_{b_i \ldots b_{i+1}}, \tilde{D}_{b_i \ldots b_{i+1}}) \]

\[ e_{i-1} \leftarrow \ell \text{OTSim}_{\text{const}} \left( \text{crs}, \text{sbl}^i, \text{Labels}^i \right) \]

\[ \tilde{e}_{i-1} \leftarrow \ell \text{OTSim}_{\text{const}} \left( \text{crs}, \text{sbl}^i, \text{Labels}^i \right) \]

Output \( e_w = (e_0, \tilde{e}_0, \tilde{C}_1, \ldots, \tilde{C}_{d-1}, \tilde{\text{c}}_{\text{write}}) \)

In the following we only prove sender security with regard to reads. Since \((\text{Send}, \ell \text{OTSim}) \) and \((\text{SendWrite}, \ell \text{OTSimWrite}) \) are very similar, sender security with regard to writes can be argued analogously.

We prove security via a hybrid argument. In the first hybrid, we replace the ciphertexts \( e_0 \) and \( \tilde{e}_0 \) computed by \( \ell \text{OTSim}_{\text{const}} \cdot \text{Send} \) with ciphertexts computed by \( \ell \text{OTSim}_{\text{const}} \).

Afterwards, we can use security of the garbling scheme to replace the honestly generated \( \tilde{C}_1 \) with a simulated one, and run \( \ell \text{OTSim}_{\text{const}} \) using the simulated input labels of \( \tilde{C}_1 \). As the output of \( \tilde{C}_1 \) is again a pair of ciphertexts \( (e_1, \tilde{e}_1) \), we will simulate it using \( \ell \text{OTSim}_{\text{const}} \) in the next hybrid. We continue alternating between simulating the garbled circuits and simulating the ciphertexts, until reaching the reading circuit. Once we reach the reading circuit, it holds that all \( \text{Labels}^i \) are information theoretically fixed to the path from the root to the leaf containing \( L \). We will then invoke the garbled circuit security of the reading circuit, and concludes the hybrid argument.

The formal proof is as follows. For every PPT machine \( A \), let \( \text{crs} \leftarrow \text{crsGen}(1^\kappa) \), and let \((D, L, m_0, m_1) \leftarrow A(\text{crs}) \). Further let \( \text{digest} \leftarrow \text{Hash}(\text{crs}, D) \). Then we will prove that
the two distributions \((\text{crs, Send}(\text{crs, digest, }L, m_0, m_1))\) and \((\text{crs, } \ell \text{OTSim}(\text{crs, } D, L, m_D[L]))\) are computationally indistinguishable. Consider the following hybrids.

- **Hybrid 0**: This is the real experiment, i.e., \((\text{crs, Send}(\text{crs, digest, }L, m_0, m_1))\).

- **Hybrid 1**: Same as hybrid 0, except that \(e_0\) and \(\tilde{e}_0\) are computed as follows.

\[
(digest, \hat{D}) \leftarrow \text{Hash}(\text{crs, } D) \\
\text{Parse } L = (b_1, b_2, \ldots, b_{d-1}, t). \\
\text{Pick } (\text{Keys}_1, \ldots, \text{Keys}_d) \text{ as input keys for } C_{\text{read}} \\
\tilde{C}_{\text{read}} \leftarrow \text{GCircuit}(1^k, C_{\text{read}}[t, m_0, m_1], (\text{Keys}_1, \ldots, \text{Keys}_d)) \\
\text{Let } \text{Keys}_d \text{ be } 0^* \\
\text{For } i = d-1 \text{ downto } 1: \\
\quad \text{Pick } \text{Keys}_i \text{ as input keys for } C_{\text{trav}} \\
\quad \text{Pick } r_i, \tilde{r}_i \text{ as random coins for } \ell \text{OT}_{\text{const}}_\text{Send} \\
\quad \tilde{C}_i \leftarrow \text{GCircuit}(1^k, C_{\text{trav}}[\text{crs, } b_i, \text{Keys}_i, r_i, \tilde{r}_i, \text{Keys}_i]) \\
\text{sbl}^1 \leftarrow (\hat{D}_0, \hat{D}_1) \\
\text{Labels}^1 \leftarrow \text{Keys}_{\text{sbl}^1} \\
\tilde{\text{Labels}}^1 \leftarrow \tilde{\text{Keys}}_{\text{sbl}^1} \\
\text{e}_0 \leftarrow \ell \text{OTSim}_{\text{const}}(\text{crs, sbl}^1, \text{Labels}^1) \\
\tilde{\text{e}}_0 \leftarrow \ell \text{OTSim}_{\text{const}}(\text{crs, sbl}^1, \tilde{\text{Labels}}^1) \\
\text{Output } e = (\text{e}_0, \tilde{\text{e}}_0, C_1, \ldots, C_{d-1}, \tilde{C}_{\text{read}})
\]

The differences between hybrid 0 and hybrid 1 have been marked with boxes. Indistinguishability between hybrid 0 and hybrid 1 can be argued from the multi-execution sender security of \(\ell \text{OT}_{\text{const}}\) via the following reduction. Given \text{crs} by the experiment and the adversarial input \(D\), compute hybrid 1 until \(e_0\) and \(\tilde{e}_0\) are computed. In particular, compute \(\hat{D}, \text{Keys}^1, \tilde{\text{Keys}}^1, \text{sbl}^1, \text{Labels}^1 = \text{Keys}_{\text{sbl}^1}, \tilde{\text{Labels}}^1 = \tilde{\text{Keys}}_{\text{sbl}^1}\). Then choose \(\text{sbl}^1\) as the database and \((\text{Keys}^1, \tilde{\text{Keys}}^1)\) as the messages for \(\ell \text{OT}_{\text{const}}\), and obtain the challenge \((e_0^*, \tilde{e}_0^*)\), which is from one of the following two distributions:

\[
\left(\ell \text{OT}_{\text{const}}.\text{Send}(\text{crs, sbl}^1, \text{Keys}^1), \ell \text{OT}_{\text{const}}.\text{Send}(\text{crs, sbl}^1, \tilde{\text{Keys}}^1)\right); \\
\left(\ell \text{OTSim}_{\text{const}}(\text{crs, sbl}^1, \text{Labels}^1), \ell \text{OTSim}_{\text{const}}(\text{crs, sbl}^1, \tilde{\text{Labels}}^1)\right).
\]

If \((e_0^*, \tilde{e}_0^*)\) is from the first distribution, then it results in hybrid 0; otherwise it results in hybrid 1. Hence the indistinguishability of the two distributions implies indistinguishability of the two hybrids.
• **Hybrid** $2k$ ($k = 1, 2, \ldots, d - 1$): Same as hybrid $2k - 1$, except that $\tilde{C}_k$ is computed as follows.

$$(\text{digest}, \hat{D}) \leftarrow \text{Hash}(\text{crs}, D)$$

Parse $L = (b_1, b_2, \ldots, b_{d-1}, t)$.

Pick $(\tilde{\text{Keys}}^1, \ldots, \tilde{\text{Keys}}^d)$ as input keys for $C^{\text{read}}$

$\tilde{C}^{\text{read}} \leftarrow \text{GCircuit}\left(1^k, C^{\text{read}}[t, m_0, m_1], (\tilde{\text{Keys}}^1, \ldots, \tilde{\text{Keys}}^d)\right)$

Let $\text{Keys}^d$ be $0^*$

For $i = d - 1$ downto $k + 1$:

Pick $\text{Keys}^i$ as input keys for $C^{\text{trav}}$

Pick $r_i, \tilde{r}_i$ as random coins for $\ell OT^{\text{const. Send}}$

$\tilde{C}_i \leftarrow \text{GCircuit}\left(1^k, C^{\text{trav}}[\text{crs}, b_i, \text{Keys}^{i+1}, \tilde{\text{Keys}}^{i+1}, r_i, \tilde{r}_i], \text{Keys}^i\right)$

$sbl^k \leftarrow (\hat{D}_{b_1 \ldots b_{k-1} 0}, \hat{D}_{b_1 \ldots b_{k-1} 1})$

$e_k \leftarrow \ell OT^{\text{const. Send}}(\text{crs}, sbl^K_{b_k}, \text{Keys}^{k+1})$

$\tilde{e}_k \leftarrow \ell OT^{\text{const. Send}}(\text{crs}, sbl^K_{b_k}, \tilde{\text{Keys}}^{k+1})$

For $i = k$ downto 1:

$$(\tilde{C}_i, \text{Labels}^i) \leftarrow \text{CircSim}(1^k, C^{\text{trav}}, (e_i, \tilde{e}_i))$$

$sbl^i \leftarrow (\hat{D}_{b_1 \ldots b_{i-1} 0}, \hat{D}_{b_1 \ldots b_{i-1} 1})$

Labels $\leftarrow \text{Keys}_{sbl^i}$

$e_{i-1} \leftarrow \ell OT^{\text{Sim Const}}(\text{crs}, sbl^i, \text{Labels}^i)$

$\tilde{e}_{i-1} \leftarrow \ell OT^{\text{Sim Const}}(\text{crs}, sbl^i, \tilde{\text{Labels}}^i)$

Output $e = (e_0, \tilde{e}_0, \tilde{C}_1, \ldots, \tilde{C}_{d-1}, \tilde{C}^{\text{read}})$

• **Hybrid** $2k + 1$ ($k = 1, 2, \ldots, d - 1$): Same as hybrid $2k$, except that $e_k$ and $\tilde{e}_k$ are computed as follows.

$$(\text{digest}, \hat{D}) \leftarrow \text{Hash}(\text{crs}, D)$$

Parse $L = (b_1, b_2, \ldots, b_{d-1}, t)$.

Pick $(\tilde{\text{Keys}}^1, \ldots, \tilde{\text{Keys}}^d)$ as input keys for $C^{\text{read}}$

$\tilde{C}^{\text{read}} \leftarrow \text{GCircuit}\left(1^k, C^{\text{read}}[t, m_0, m_1], (\tilde{\text{Keys}}^1, \ldots, \tilde{\text{Keys}}^d)\right)$

Let $\text{Keys}^d$ be $0^*$

For $i = d - 1$ downto $k + 1$:

Pick $\text{Keys}^i$ as input keys for $C^{\text{trav}}$

Pick $r_i, \tilde{r}_i$ as random coins for $\ell OT^{\text{const. Send}}$

$\tilde{C}_i \leftarrow \text{GCircuit}\left(1^k, C^{\text{trav}}[\text{crs}, b_i, \text{Keys}^{i+1}, \tilde{\text{Keys}}^{i+1}, r_i, \tilde{r}_i], \text{Keys}^i\right)$

$sbl^k \leftarrow (\hat{D}_{b_1 \ldots b_{k-1} 0}, \hat{D}_{b_1 \ldots b_{k-1} 1})$

$e_k \leftarrow \ell OT^{\text{const. Send}}(\text{crs}, sbl^K_{b_k}, \text{Keys}^{k+1})$

$\tilde{e}_k \leftarrow \ell OT^{\text{const. Send}}(\text{crs}, sbl^K_{b_k}, \tilde{\text{Keys}}^{k+1})$
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We first show that hybrids $2^k$ are indistinguishable via a reduction to the security of the garbling scheme $GCircuit$. Notice that the only difference between hybrids $2^k - 1$ and $2^k$ is $(\tilde{C}_k, e_{k-1})$. Consider the following two distributions:

$$ egin{align*}
(\tilde{C}_k, \text{Labels}^k) &\xleftarrow{} \text{CircSim}(1^\kappa, C_{\text{trav}}, (e_i, \tilde{e}_i)) \\
\text{sb}^i &\xleftarrow{} (D_{b_1...b_{i-1}}0, D_{b_1...b_{i-1}}1) \\
\text{Labels} &\xleftarrow{} \text{Keys}_{\text{sb}^i} \\
e_{i-1} &\xleftarrow{} \ell\text{OTSim}_{\text{const}}(\text{crs}, \text{sb}^i, \text{Labels}^i) \\
\tilde{e}_{i-1} &\xleftarrow{} \ell\text{OTSim}_{\text{const}}(\text{crs}, \text{sb}^i, \text{Labels}) \\
\text{Output } e &= (e_0, \tilde{e}_0, \tilde{C}_1, \ldots, \tilde{C}_{d-1}, \tilde{C}_{\text{read}})
\end{align*} $$

Indistinguishability of hybrids $2k$ and $2k + 1$ follows again from sender security of $\ell\text{OT}_{\text{const}}$ in the same fashion as the indistinguishability between hybrids 0 and 1.

- **Hybrid $2d$:** This is the simulated experiment, namely $(\text{crs}, \ell\text{OTSIm}(\text{crs}, D, L, m_{D[L]}))$.

The difference between hybrids $2d - 1$ and $2d$ is $(\tilde{C}_{\text{read}}, \tilde{e}_0, \ldots, \tilde{e}_{d-1})$. The indistinguishability would follow from the security of garbled circuit scheme, similarly as when we argue indistinguishability hybrids $2k - 1$ and $2k$. 

\[ \square \]
Chapter 3

Non-Interactive Secure Computation

In this chapter, we apply laconic OT to reduce communication complexity in non-interactive secure computation on large inputs in the circuit setting as well as RAM setting. Our construction for the circuit setting is presented in Section 3.1 and for the RAM setting is presented in Section 3.2. Finally we discuss how to enhance the security guarantee from semi-honest security to malicious security in Section 3.3.

3.1 Circuit Setting

In the circuit setting, consider a receiver $R$, holding a large database $D$, publishes a short encoding of it such that any sender $S$, with private input $x$, can send a single message to reveal $C(x, D)$ to $R$. Here, $C$ is the circuit being evaluated. For security, we want the receiver’s encoding to hide $D$ and the sender’s message to hide $x$. For communication complexity, we restrict the receiver’s published encoding to be independent of the size of her database.

This is a straightforward application of laconic OT. Recall the garbled circuit based approach to non-interactive secure computation, where $R$ can publish the first message of a two-message oblivious transfer (OT) for his input $D$, and the sender responds with a garbled circuit for $C[x, \cdot]$ (with hardcoded input $x$) and sends the input labels corresponding to $D$ via the second OT message. The downside of this protocol is that $R$’s public message grows with the size of $D$, which could be substantially large.

We resolve this issue via our new primitive laconic OT. In our protocol, $R$’s first message is the digest $\text{digest}$ of his large database $D$. Next, the sender generates the garbled circuit for $C[x, \cdot]$ as before. It also transfers the labels for each location of $D$ via laconic OT $\text{Send}$ messages. Hence, by efficiency requirements of laconic OT, the length of $R$’s public message is independent of the size of $D$. Moreover, sender privacy against a semi-honest receiver follows directly from the sender privacy of laconic OT and security of garbled circuits. To achieve receiver privacy, we can enhance the laconic OT with receiver privacy (discussed in Section 2.2.1).
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3.2 RAM Setting

In this section, we consider the application of non-interactive secure computation in the RAM (random access machine) setting. It is the RAM version of the above application where $S$ holds a RAM program $P$ and $R$ holds a large database $D$. As before, we want that (1) the length of $R$’s first message is independent of $|D|$, (2) $R$’s first message can be published and used by multiple senders, (3) the database is persistent for a sequence of programs for every sender, and (4) the computational complexity of both $S$ and $R$ per program execution grows only with running time of the corresponding program. For this application, we only achieve unprotected memory access (UMA) security against a corrupt receiver, i.e., the memory access pattern in the execution of $P^D(x)$ is leaked to the receiver. We achieve full security against a corrupt sender.

Organization. In Section 3.2.1 we give a technical overview of this application. In Section 3.2.2 we describe necessary background needed for our construction. Then we formalize our model in Section 3.2.3 and present our construction for a single sender executing a single program with the receiver in Sections 3.2.4. Correctness and security proofs are provided in Sections 3.2.5 and 3.2.6, respectively. Finally we discuss how to extend our construction to executing multiple programs on a persistent database in Section 3.2.7.

3.2.1 Technical Overview

For simplicity, consider a read-only program such that each CPU step outputs the next location to be read based on the value read from last location. At a high level, since we want the sender’s complexity to grow only with the running time $t$ of the program, we cannot create a garbled circuit that takes $D$ as input. Instead, we would go via the garbled RAM based approaches where we have a sequence of $t$ garbled circuits where each circuit executes one CPU step. A CPU step circuit takes the current CPU state and the last bit read from the database $D$ as input and outputs an updated state and a new location to be read. The new location would be read from the database and fed into the next CPU step. The most non-trivial part in all garbled RAM constructions is being able to compute the correct labels for the next circuit based on the value of $D[L]$, where $L$ is the location being read. Since we are working with garbled circuits, it is crucial for security that the receiver does not learn two labels for any input wire. We solve this issue via laconic OT as follows.

For the simpler case of sender security, $R$ publishes the short digest of $D$, which is fed into the first garbled circuit and this digest is passed along the sequence of garbled circuits. When a circuit wants to read a location $L$, it outputs the laconic OT ciphertexts which encrypt the input keys for the next circuit and use digest of $D$ as the hash value.\footnote{We note that the above idea of using laconic OT also gives a conceptually very simple solution for UMA secure garbled RAM scheme [LO13]. Moreover, there is a general transformation [GHL+14] that converts any UMA secure garbled RAM into one with full security via the usage of symmetric key encryption and oblivious} Security
against a corrupt receiver follows from the sender security of laconic OT and security of garbled circuits. To achieve security against a corrupt sender, $R$ does not publish digest in the clear. Instead, the labels for digest for the first circuit are transferred to $R$ via regular OT.

Note that the garbling time of the sender as well as execution time of the receiver will grow only with the running time of the program. This follows from the efficiency requirements of laconic OT.

Above, we did not describe how we deal with general programs that also write to the database or memory. We achieve this via updatable laconic OT (for definition see Section 2.2.2). This allows for transferring the labels for updated digest (corresponding to the updated database) to the next circuit. For a formal description of our scheme for general RAM programs, see Section 3.2.4.

Other Related Work. Prior works consider secure computation which hides the input size of one [MRK03; IP07; ADT11; LNO13] or both parties [LNO13]. Our notion only requires the receiver’s communication cost to be independent of the its input size, and is therefore weaker. However, these results are largely restricted to special functionalities, such as zero-knowledge sets and computing certain branching programs (which imply input-size hiding private set intersection). The general result of [LNO13] uses FHE and as mentioned earlier needs more rounds of interaction.\footnote{In an orthogonal work of Hubacek and Wichs [HW15] obtain constructions where the communication cost is independent of the length of the output of the computation using indistinguishability obfuscation [GGH+13].}

\section{Background}

We recall the needed background of RAM computation model and two-message oblivious transfer in this section. We also use garbled circuits (see Section 2.4.1.1) as building blocks.

\subsection{Random Access Machine (RAM) Model of Computation}

Now we define the RAM model of computation. Parts of this subsection have been taken verbatim from [GLO15].

\textbf{Notations.} The RAM model consists of a CPU and a memory storage of size $M$. The CPU executes a program that can access the memory by using read/write operations. In particular, for a program $P$ with memory of size $M$ we denote the initial contents of the memory data by $D \in \{0, 1\}^M$. Additionally, the program gets a “short” input $x \in \{0, 1\}^m$, which we alternatively think of as the initial state of the program. We use the notation $P^D(x)$ to denote the execution of program $P$ with initial memory contents $D$ and input $x$.\footnote{This would give a simplified construction of fully secure garbled RAM under DDH assumption.}
The program $P$ can read from and write to various locations in memory $D$ throughout its execution.\footnote{In general, the distinction between what to include in the program $P$, the memory data $D$ and the short input $x$ can be somewhat arbitrary. However as motivated by our applications we will typically be interested in a setting where the data $D$ is large while the size of the program $|P|$ and input length $m$ is small.}

We will also consider the case where several different programs are executed sequentially and the memory persists between executions. We denote this process as $(y_1, \ldots, y_\ell) = (P_1(x_1), \ldots, P_\ell(x_\ell))$ to indicate that first $P_1^D(x_1)$ is executed, resulting in some memory contents $D_1$ and output $y_1$, then $P_2^D(x_2)$ is executed resulting in some memory contents $D_2$ and output $y_2$ etc. As an example, imagine that $D$ is a huge database and the programs $P_i$ are database queries that can read and possibly write to the database and are parameterized by some values $x_i$.

**CPU-Step Circuit.** Consider an execution of a RAM program which involves at most $t$ CPU steps. We represent a RAM program $P$ via $t$ small CPU-Step Circuits each of which executes one CPU step. In this work we will denote one CPU step by:

$$C_{CPU}^P(state, rData) = (state', R/W, L, wData)$$

This circuit takes as input the current CPU state $state$ and a bit $rData$. Looking ahead the bit $rData$ will be read from the memory location that was requested by the previous CPU step. The circuit outputs an updated state $state'$, a read or write bit $R/W$, the next location to read/write from $L \in [M]$, and a bit $wData$ to write into that location ($wData = \perp$ when reading). The sequence of locations and read/write values collectively form what is known as the access pattern, namely $MemAccess = \{(R/W_\tau, L_\tau, wData_\tau) : \tau = 1, \ldots, t\}$.

Note that in the description above without loss of generality we have made some simplifying assumptions. We assume that each CPU-step circuit always reads from or write some location in memory. This is easy to implement via a dummy read and write step. Moreover, we assume that the instructions of the program itself are hardwired into the CPU-step circuits.

**Representing RAM Computation by CPU-Step Circuits.** The computation $P^D(x)$ starts with the initial state set as $state_1 = x$. In each step $\tau \in \{1, \ldots, t\}$, the computation proceeds as follows: If $\tau = 1$ or $R/W^{\tau-1} = write$, then $rData^\tau := 0$; otherwise $rData^\tau := D[L^{\tau-1}]$. Next it executes the CPU-Step Circuit $C_{CPU}^P(state^\tau, rData^\tau) = (state^{\tau+1}, R/W^\tau, L^\tau, wData^\tau)$. If $R/W^\tau = write$, then set $D[L^\tau] = wData^\tau$. Finally, when $\tau = t$, then $state^{\tau+1}$ is the output of the program.

### 3.2.2.2 Oblivious Transfer

[AIR01; NP01; HK12] gave two-message oblivious transfer (OT) protocols. We describe the definition below and refer the reader to [AIR01; NP01; HK12] for details.
Definition 3.2.1 (Two-Message Oblivious Transfer). A two-message oblivious transfer protocol \( \text{OT} = (\text{OT}_1, \text{OT}_2, \text{OT}_3) \) is a protocol between a sender \( S \) and a receiver \( R \) where \( S \) gets as input two strings \( s_1, s_2 \) of equal length and \( R \) gets as input a choice bit \( x \in \{0, 1\} \). The algorithms have the following syntax:

- \( (m_1, \text{secret}) \leftarrow \text{OT}_1(1^\kappa, x) \): It takes as input the security parameter \( 1^\kappa \) and receiver’s choice bit \( x \in \{0, 1\} \) and outputs the first OT message \( m_1 \) (sent by the receiver) and receiver’s secret state \( \text{secret} \).
- \( m_2 \leftarrow \text{OT}_2(m_1, s_0, s_1) \): It takes as input the first OT message and the sender’s input \( (s_0, s_1) \), and outputs the second OT message \( m_2 \) (sent back to the receiver).
- \( s \leftarrow \text{OT}_3(m_2, \text{secret}) \): It takes \( m_2 \) and \( \text{secret} \) as input, and outputs a string \( s \).

The following conditions are satisfied:

- **Perfect Correctness.** For all security parameter \( \kappa \), sender input strings \( (s_1, s_2) \) of equal length, and receiver’s choice bit \( x \), let \( (m_1, \text{secret}) \leftarrow \text{OT}_1(1^\kappa, x) \), \( m_2 \leftarrow \text{OT}_2(m_1, s_0, s_1) \), and \( s \leftarrow \text{OT}_3(m_2, \text{secret}) \), then it holds that

\[
\Pr [s = s_x] = 1.
\]

- **Receiver Security.** The following two distributions are computationally indistinguishable:

\[
\text{OT}_1(1^\kappa, 0) \approx \text{OT}_1(1^\kappa, 1).
\]

- **Sender Security.** There exists a PPT simulator \( \text{OTSim} \) such that for all sender input strings \( (s_1, s_2) \) of equal length and receiver’s choice bit \( x \), and any first message \( m_1 \) in the support of \( \text{OT}_1(1^\kappa, x) \), the following two distributions are statistically close:

\[
\text{OT}_2(m_1, s_0, s_1) \approx \text{OTSim}(1^\kappa, x, s_x, m_1).
\]

We described the above definition with respect to one OT, but the same formalism naturally extends to support multiple parallel executions of OT. We use the following short-hand notations (generalizing the above notions) to run multiple parallel executions. Let \( \text{Keys} = ((\text{Key}_{1,0}, \text{Key}_{1,1}), \ldots, (\text{Key}_{n,0}, \text{Key}_{n,1})) \) be a list of \( n \) string-pairs, and \( x \in \{0, 1\}^n \) be an \( n \)-bit choice string. Then we define

- \( (m_1, \text{secret}) \leftarrow \text{OT}_1(1^\kappa, x) = (\text{OT}_1(1^\kappa, x_1), \ldots, \text{OT}_1(1^\kappa, x_n)) \).
- \( m_2 \leftarrow \text{OT}_2(m_1, \text{Keys}) = (\text{OT}_2(m_{1,1}, \text{Key}_{1,0}, \text{Key}_{1,1}), \ldots, \text{OT}_2(m_{1,n}, \text{Key}_{n,0}, \text{Key}_{n,1})) \).
- \( \text{Labels} \leftarrow \text{OT}_3(m_2, \text{secret}) = (\text{OT}_3(m_{2,1}, \text{secret}_1), \ldots, \text{OT}_3(m_{2,n}, \text{secret}_n)) \).
In the above $m_1 = (m_{1,1}, \ldots, m_{1,n})$, $m_2 = (m_{2,1}, \ldots, m_{2,n})$, secret = (secret_1, \ldots, secret_n). \nabla
Correctness guarantees that Labels = Keys = (Key_{1,x}, \ldots, Key_{n,x}). \nabla
Moreover, we will use two important properties of the oblivious transfer [NP01] for our applications: (1) Security holds for multiple second OT messages with regard to the same first OT message. This will be crucial for extending NISC for RAM to support multiple senders with the same receiver. (2) The second OT message is re-randomizable. This will be crucial for the application of multi-hop homomorphic encryption for RAM.

3.2.3 Our Model

Suppose the receiver owns a large confidential database $D \in \{0, 1\}^M$. It first publishes a short message, denoted by $m_1$, which hides $D$. Afterwards, if a sender wants to run a RAM program $P$ (with input $x$) on $D$, it can send a single message $m_2$ to the receiver. For security we require that $m_2$ only reveals the output $P^D(x)$ and the memory access pattern MemAccess of the execution to the receiver. We require that once $m_1$ is published, the computational cost of both the sender (in computing $m_2$) and the receiver (in evaluation), as well as the size of $m_2$, should grow only with the running time of the RAM computation and the size of $m_1$, and is independent of the size of $D$.

Moreover, the sender can run a sequence of programs on a persistent database by sending one message per program to the receiver. Finally, the receiver can run the protocol in parallel with multiple senders, where the same $m_1$ is used. For ease of exposition, below we will describe the setting of one single sender executing one program with the receiver. We provide details on above extensions in Section 3.2.7.

Definition 3.2.2 (Non-Interactive Secure RAM Computation). A non-interactive secure RAM computation scheme NISC-RAM = (Setup, EncData, EncProg, Dec) has the following syntax. It is a two-party protocol between a receiver holding a large secret database $D$ and a sender holding secret program $P$ of running time $t$ and a short input $x$.

- **Setup.** $\text{crs} \leftarrow \text{Setup}(1^\kappa)$.
  On input the security parameter $1^\kappa$, it outputs a common reference string.

- **Database Encryption.** $(m_1, \tilde{D}) \leftarrow \text{EncData}(\text{crs}, D)$.
  On input the common reference string $\text{crs}$ and a database $D \in \{0, 1\}^M$, it outputs a message $m_1$ and a secret state $\tilde{D}$. The receiver publishes $m_1$ as the short message corresponding to $D$.

- **Program Encryption.** $m_2 \leftarrow \text{EncProg}(\text{crs}, m_1, (P, x, t))$.
  It takes as input the $\text{crs}$, a message $m_1$, a RAM program $P$ with input $x$ and maximum run-time $t$. It then outputs another message $m_2$. The sender sends the message $m_2$.

- **Decryption.** $y \leftarrow \text{Dec}^{\tilde{D}}(\text{crs}, m_2)$.
  The procedure $\text{Dec}$ is modeled as a RAM program that can read and write to arbitrary
locations of its database initially containing $\tilde{D}$. This procedure is run by the receiver. On input the $crs$ and $m_2$, it outputs $y$.

The following conditions are satisfied:

- **Correctness.** For every database $D \in \{0,1\}^M$ where $M = \text{poly}(\kappa)$ for any polynomial function $\text{poly}(\cdot)$, for every RAM program $(P, x, t)$, it holds that
  \[ \Pr \left[ \text{Dec}^{\tilde{D}}(crs, m_2) = P^D(x) \right] = 1, \]
  where $crs \leftarrow \text{Setup}(1^\kappa)$, $(m_1, \tilde{D}) \leftarrow \text{EncData}(crs, D)$, $m_2 \leftarrow \text{EncProg}(crs, m_1, (P, x, t))$.

- **Receiver Privacy.** For every pair of databases $D_0 \in \{0,1\}^M, D_1 \in \{0,1\}^M$ where $M$ is polynomial in $\kappa$, for every $crs$ in the support of $\text{Setup}(1^\kappa)$, let $(m_0, \tilde{D}_0) \leftarrow \text{EncData}(crs, D_0), (m_1, \tilde{D}_1) \leftarrow \text{EncData}(crs, D_1)$. Then it holds that
  \[ (crs, m_0) \approx (crs, m_1). \]

- **Sender Privacy.** There exists a PPT simulator $niscSim$ such that for every database $D \in \{0,1\}^M$ where $M = \text{poly}(\kappa)$ for any polynomial function $\text{poly}(\cdot)$, and for every RAM program $(P, x, t)$, let $y = P^D(x)$ be the output of the program, and $\text{MemAccess}$ be the memory access pattern, then it holds that
  \[ (crs, D, (m_1, \tilde{D}), m_2) \approx niscSim(1^\kappa, D, (y, \text{MemAccess})) \]
  where $crs \leftarrow \text{Setup}(1^\kappa)$, $(m_1, \tilde{D}) \leftarrow \text{EncData}(crs, D)$ and $m_2 \leftarrow \text{EncProg}(crs, m_1, (P, x, t))$.

- **Efficiency.** The length of $m_1$ is a fixed polynomial in $\kappa$ independent of the size of the database. Moreover, the algorithm $\text{EncData}$ runs in time $M \cdot \text{poly}(\kappa, \log M)$, $\text{EncProg}$ and $\text{Dec}$ run in time $t \cdot \text{poly}(\kappa, \log M)$.

### 3.2.4 Construction of Non-Interactive Secure RAM Computation

**Overview.** We first give an overview of the construction. For ease of exposition, consider a read-only program where each CPU step outputs the next location to be read based on the value read from last location.

We first describe the $\text{EncProg}$ procedure. As already mentioned in technical overview (see Section 3.2.1), our construction is based on high level ideas of garbled RAM (introduced by Lu and Ostrovsky [LO13]) to make sender and receiver complexity grow only with the running time of the program. In particular, the sender would generate a garbled RAM program consisting of a sequence of $t$ garbled step circuits. Similar to the RAM computation model described in Section 3.2.2.1, every step circuit takes as input the current CPU state...
and the last read bit and outputs the updated state and the next read location, say $L$. Note that the next step circuit would take the new value read from database as input.

The main challenge in program garbling is revealing the correct labels for the next circuit based on the value of $D[L]$. Moreover, it is crucial for garble circuit security that the receiver does not learn the label corresponding to $1 - D[L]$. Prior works [LO13; GHL+14; GLOS15; GLO15] proposed several different solutions to the above problem. Here we present a new and arguably simpler solution for achieving this using laconic oblivious transfer.

Let $\text{digest}$ be the hash value of $D$ that would be fed into the first step circuit and passed along the sequence of circuits. That is, each circuit would take this digest as input and also output the correct input labels corresponding to the digest for the next circuit. Now, to transfer the correct label corresponding to the value in the database, a step circuit would output a laconic OT ciphertext (using algorithm $\text{Send}$) that encrypts the input keys of the next step circuit and uses $\text{digest}$ as the hash value. Looking ahead, when the receiver evaluates the step circuit which outputs the laconic OT ciphertext, he can use $D$ to decrypt it to obtain the correct labels (using the procedure $\text{Receive}$ of laconic OT).

We show that the sender privacy follows from the sender privacy of laconic OT and security of circuit garbling. In order to achieve receiver privacy, the receiver does not publish $\text{digest}$ in the clear, but instead, the labels for $\text{digest}$ of the first step circuit are transferred from the sender to the receiver via a two-message OT. In particular, the $\text{EncData}$ procedure outputs the first OT message of $\text{digest}$, and $\text{EncProg}$ will output the garbled step circuits along with the second OT message for $\text{digest}$'s labels.

Finally, note that a general program can also write to the database, in which case we need to update the database as well as the step circuits need to know the updated digest for the correctness of laconic OT and future reads/writes. This is achieved via the updatability property of the laconic OT which allows a sender to generate a ciphertext that allows the receiver to learn messages corresponding to the updated digest. In our case, the messages encrypted would be the input digest keys of the next step circuit.

Next, we give a more formal construction of our scheme.

**Construction.** Let $tOT = (\text{crsGen, Hash, Send, Receive, SendWrite, ReceiveWrite})$ be an updatable laconic OT protocol as per Definition 2.2.2. Let $\text{OT} = (\text{OT}_1, \text{OT}_2, \text{OT}_3)$ be a two-message secure oblivious transfer, and let $\text{GC} = (\text{GCircuit, GEval})$ be a circuit garbling scheme. The non-interactive secure RAM computation scheme $\text{NISC-RAM} = (\text{Setup, EncData, EncProg, Dec})$ is constructed as follows.

- **Setup.** $\text{crs} \leftarrow \text{Setup}(1^\kappa)$.
  The set up algorithm is described in Figure 3.1. It generates the common reference string for the updatable laconic OT scheme.

- **Database Encryption.** $(m_1, \tilde{D}) \leftarrow \text{EncData}(\text{crs, D})$.
  The algorithm is formally described in Figure 3.2. It hashes the database $D$ using
CHAPTER 3. NON-INTERACTIVE SECURE COMPUTATION

Setup. \( \text{crs} \leftarrow \text{Setup}(1^\kappa) \).
1. \( \text{crs} \leftarrow \text{crsGen}(1^\kappa) \).
2. Output \( \text{crs} \).

Figure 3.1: Setup procedure of NISC-RAM.

laconic OT Hash function and obtains \textit{digest}. Then \textit{digest} is encrypted using the OT\(_1\) procedure of two-message OT protocol.

Database Encryption. \( (m_1, \tilde{D}) \leftarrow \text{EncData}(\text{crs}, D) \).
1. \((\text{digest}, \hat{D}) \leftarrow \text{Hash}(\text{crs}, D)\).
2. \((m_1, \text{secret}) \leftarrow \text{OT}\(_1\)(1\(^\kappa\), \text{digest})\).
3. Output \( (m_1, \tilde{D} = (\text{digest}, \hat{D}, \text{secret})) \).

Figure 3.2: Database encryption procedure of NISC-RAM.

- Program Encryption. \( m_2 \leftarrow \text{EncProg}(\text{crs}, m_1, (P, x, t)) \).
The program encryption procedure is formally described in Figure 3.3. As mentioned above, it generates \( t \) garbled \textit{step circuits} \( \{\tilde{C}_{\text{step}}^\tau\}_{\tau=1}^t \), where every step circuit implements the functionality of a CPU-step circuit. We describe the structure of a step circuit \( C_{\text{step}} \) below. The program encryption also consists of the second OT message corresponding to the short message \( m_1 \) of the receiver (for \textit{digest}) where the sender’s messages consist of the input keys for the first garbled circuit. Finally, it also outputs the keys for decrypting the output of the last step circuit.

Program Encryption. \( m_2 \leftarrow \text{EncProg}(\text{crs}, m_1, (P, x, t)) \).
1. \textbf{Generate the garbled program for} \( P \): Generate garbled circuits \( \{\tilde{C}_{\text{step}}^\tau\}_{\tau=1}^t \).
   a) Sample \textit{stateKeys}^\tau, \textit{dataKeys}^\tau, \textit{digestKeys}^\tau for each \( \tau \in \{1, \ldots, t+1\} \).
   b) For each \( \tau \in \{1, \ldots, t\} \)
      \[ \tilde{C}_{\text{step}}^\tau \leftarrow \text{GCircuit}(1^\kappa, C_{\text{step}}^{\tau}[\text{crs}, P, \text{Keys}^{\tau+1}], \text{Keys}^\tau) , \]
      where \( \text{Keys}^\tau = (\text{stateKeys}^\tau, \text{dataKeys}^\tau, \text{digestKeys}^\tau) \).
   c) For \( \tau = 1 \), embed labels \textit{dataKeys}^1_0 and \textit{stateKeys}^1_2 in \( \tilde{C}_{\text{step}}^1 \).
2. Compute \( L \leftarrow \text{OT}\(_2\)(m_1, \text{digestKeys}^1_0) \).
3. Output \( m_2 = (L, \{\tilde{C}_{\text{step}}^\tau\}_{\tau=1}^t, \text{stateKeys}^{t+1}) \).

Figure 3.3: Program encryption procedure of NISC-RAM.

Now we elaborate on the logic of a step circuit. The pseudocode of a step circuit \( C_{\text{step}} \) is formally described in Figure 3.4, and the structure is illustrated in Figure 3.5.
CHAPTER 3. NON-INTERACTIVE SECURE COMPUTATION

Hardwired Parameters: \[\text{crs}, P, \text{nextKeys} = (\text{stateKeys}, \text{dataKeys}, \text{digestKeys})\].

Input: (\text{state}, \text{rData}, \text{digest}).

\[(\text{state}', \text{R/W}, L, \text{wData}) := C_{\text{CPU}}^P(\text{state}, \text{rData}).\]

if \(\text{R/W} = \text{read}\) then

\[e_{\text{data}} \leftarrow \text{Send}(\text{crs}, \text{digest}, L, \text{dataKeys}).\]

return \((\text{stateKeys}_{\text{state}'}, e_{\text{data}}, \text{digestKeys}_{\text{digest}}), \text{R/W}, L\).

else

\[e_{\text{digest}} \leftarrow \text{SendWrite}(\text{crs}, \text{digest}, L, \text{wData}, \text{digestKeys}).\]

return \((\text{stateKeys}_{\text{state}'}, \text{dataKeys}_0, e_{\text{digest}}, \text{wData}), \text{R/W}, L\).

Figure 3.4: Pseudocode of a step circuit \(C^{\text{step}}[\text{crs}, P, \text{nextKeys}]\).

The input of a step circuit can be partitioned into \((\text{state}, \text{rData}, \text{digest})\), where \text{state} is the current CPU state, \text{rData} is the bit read from the database, and \text{digest} is the up-to-date digest of the database. If the previous step is a write, then \text{rData} = 0. The program encryption outputs garbled circuits for these step circuits, hence, the first step of \text{EncProg} is to pick the input keys for all the circuits. The \(\tau\)-th step circuit \(C^{\text{step}}_\tau\) has hardwired in it the input keys \text{nextKeys} = (\text{stateKeys}, \text{dataKeys}, \text{digestKeys}) for the next step circuit \(C^{\text{step}}_{\tau+1}\).

The logic of the step circuit is as follows: It first computes the new \((\text{state}', \text{R/W}, L, \text{wData})\). Then, in the case of a “read” it outputs \text{stateKeys} corresponding to \text{state}', labels for \text{rData} via laconic OT procedure \text{Send}(\cdot), and \text{digestKeys} corresponding to \text{digest}. The case of a write is similar, but now the labels of new updated digest are transferred via laconic OT procedure \text{SendWrite}(\cdot).
• **Decryption:** \( y \leftarrow \text{Dec}^\tilde{D}(\text{crs}, m_2) \).

The decryption procedure is described in Figure 3.6. At a high level the receiver evaluates the garbled step circuits one by one from \( \tilde{C}_1^{\text{step}} \) to \( \tilde{C}_t^{\text{step}} \), and uses the database to decrypt \( t \text{OT} \) ciphertexts between two consecutive circuits. The output of the last step circuit can be decrypted using \( \text{stateKeys}^{t+1} \) and hence \( y \) is obtained.

<table>
<thead>
<tr>
<th>Decryption. ( y \leftarrow \text{Dec}^\tilde{D}(\text{crs}, m_2) ).</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. Parse ( \tilde{D} = (\text{digest}, \hat{\tilde{D}}, \text{secret}) ).</td>
</tr>
<tr>
<td>2. Parse ( m_2 = (L, {\tilde{C}<em>\tau^{\text{step}}}</em>{\tau=1}^t, \text{stateKeys}^{t+1}) ).</td>
</tr>
<tr>
<td>3. Compute ( \text{digestLabels}^{1} \leftarrow \text{OT}_3(L, \text{secret}) ).</td>
</tr>
<tr>
<td>4. Parse ( \tilde{C}_1^{\text{step}} = (\tilde{C}_1^{\text{step}}, \text{dataLabels}^{1}, \text{stateLabels}^{1}) ).</td>
</tr>
<tr>
<td>5. For ( \tau = 1 ) to ( t ) do the following:</td>
</tr>
<tr>
<td>( (X, R/W, L) := \text{GEval} \left( \tilde{C}_\tau^{\text{step}}, (\text{stateLabels}^\tau, \text{dataLabels}^\tau, \text{digestLabels}^\tau) \right) ).</td>
</tr>
<tr>
<td>if ( R/W = \text{read} ) then</td>
</tr>
<tr>
<td>Parse ( X = (\text{stateLabels}^{\tau+1}, e_{\text{data}}, \text{digestLabels}^{\tau+1}) )</td>
</tr>
<tr>
<td>dataLabels^{\tau+1} = \text{Receive}^\hat{\tilde{D}}(\text{crs}, e_{\text{data}}, L)</td>
</tr>
<tr>
<td>else</td>
</tr>
<tr>
<td>Parse ( X = (\text{stateLabels}^{\tau+1}, \text{dataLabels}^{\tau+1}, e_{\text{digest}}, \text{wData}) )</td>
</tr>
<tr>
<td>digestLabels^{\tau+1} = \text{ReceiveWrite}^\hat{\tilde{D}}(\text{crs}, L, \text{wData}, e_{\text{digest}})</td>
</tr>
<tr>
<td>6. Use ( \text{stateKeys}^{t+1} ) to decode ( \text{stateLabels}^{t+1} ) and obtain ( y ).</td>
</tr>
</tbody>
</table>

Figure 3.6: Decryption procedure of NISC-RAM.

More precisely, the receiver first obtains the \( \text{digestLabels} \) for the first step circuit by running \( \text{OT}_3 \). Note that the first garbled step circuit already has labels for the \( r \text{Data} \) and \( \text{state} \) embedded. Hence the receiver can obtain all the labels for the first step circuit and evaluate it. Then the receiver executes the circuits \( \{\tilde{C}_\tau^{\text{step}}\}_{\tau=1}^t \) one by one, and learns the labels for the next circuit by running the receiver algorithms of laconic OT on its database.

### 3.2.5 Correctness

For correctness, we require that for every database \( D \in \{0, 1\}^M \), for every RAM program \((P, x, t)\), it holds that

\[
\Pr \left[ \text{Dec}^\tilde{D}(\text{crs}, m_2) = P^D(x) \right] = 1,
\]

where \( \text{crs} \leftarrow \text{Setup}(1^\kappa), (m_1, \tilde{D}) \leftarrow \text{EncData}(\text{crs}, D), m_2 \leftarrow \text{EncProg}(\text{crs}, m_1, (P, x, t)) \). Correctness follows from Lemma 3.2.4 that we will prove below.
**Claim 3.2.3.** The first garbled step circuit \( \widetilde{C}^{\text{step}}_1 \) gets evaluated on \((x, 0, \text{digest})\), where \((\text{digest}, \hat{D}) = \text{Hash}(\text{crs}, D)\).

**Proof.** Since \((m_1, \text{secret}) \leftarrow \text{OT}_1(1^\kappa, \text{digest}), L \leftarrow \text{OT}_2(m_1, \text{digestKeys}^{\ell})\), and \(\text{digestLabels}^{\ell} \leftarrow \text{OT}_3(L, \text{secret})\), by correctness of \(\text{OT}\), \(\text{digestLabels}^{\ell} = \text{digestKeys}_{\text{digest}}^{\ell}\). Moreover, \(\widetilde{C}^{\text{step}}_1\) already has labels \(\text{stateKeys}^1_x\) and \(\text{dataKeys}^1_0\) embedded in it, by correctness of the circuit garbling scheme, \(\widetilde{C}^{\text{step}}_1\) gets evaluated on \((x, 0, \text{digest})\).

**Lemma 3.2.4.** Consider the execution of \(PD^\tau(x)\). Let \((\text{state}^\tau, \text{rData}^\tau)\) be the input to the \(\tau\)-th CPU step. Let \(D^\tau\) be the database at the beginning of step \(\tau\), and let \((\text{digest}^\tau, \hat{D}^\tau) = \text{Hash}(\text{crs}, D^\tau)\). During the Dec procedure, for every \(\tau \in [t]\), \(\widetilde{C}^{\text{step}}_\tau\) is evaluated on inputs \((\text{state}^\tau, \text{rData}^\tau, \text{digest}^\tau)\). Moreover, the state of the database held by the receiver at the beginning of evaluating \(\widetilde{C}^{\text{step}}_\tau\) is \(\hat{D}^\tau\).

**Proof.** We will prove this lemma by induction on \(\tau\). The base case follows from Claim 3.2.3. Assume that the lemma holds for \(\tau = \rho\), then we prove that the lemma holds for \(\tau + 1\) in the following. We know that \((\hat{D}^\rho, \text{digest}^\rho) = \text{Hash}(\text{crs}, D^\rho)\), and that \(\widetilde{C}^{\text{step}}_\rho\) is executed on \((\text{state}^\rho, \text{rData}^\rho, \text{digest}^\rho)\). By correctness of GC, \(\widetilde{C}^{\text{step}}_\rho\) implements its code of a CPU step, namely \((\text{state}^\rho, R/W, L, wData) = C^\rho_{\text{CPU}}(\text{state}^\rho, \text{rData}^\rho)\). Also notice that \(\text{nextKeys} = (\text{stateKeys}, \text{dataKeys}, \text{digestKeys})\) hardwired in \(\widetilde{C}^{\text{step}}_\rho\) are the input keys for \(\widetilde{C}^{\text{step}}_{\rho + 1}\). There are two cases:

- **R/W = read:** In this case, it follows directly from the Dec procedure that \(\text{stateLabels}^{\rho + 1} = \text{stateKeys}^\rho_{\text{state}}\) and \(\text{digestLabels}^{\rho + 1} = \text{digestKeys}^\rho_{\text{digest}}\). Since \(e_{\text{data}} \leftarrow \text{Send}(\text{crs}, \text{digest}^\rho, L, \text{dataKeys})\) and \(\text{dataLabels}^{\rho + 1} = \text{Receive}^{\rho}_{\text{crs}}(e_{\text{data}}, L)\), by correctness of the \(\ell\text{OT}\) scheme, \(\text{dataLabels}^{\rho + 1} = \text{dataKeys}^{\rho + 1}_{D^\rho[L]}\). Hence \(\widetilde{C}^{\text{step}}_{\rho + 1}\) is evaluated on inputs \((\text{state}^\rho, D^\rho[L], \text{digest})\), which is exactly \((\text{state}^\rho, \text{rData}^{\rho + 1}, \text{digest}^{\rho + 1})\). And \((\hat{D}^\rho, \text{digest}^\rho)\) remains unchanged.

- **R/W = write:** In this case, it follows from the Dec procedure that \(\text{stateLabels}^{\rho + 1} = \text{stateKeys}^\rho_{\text{state}}\) and \(\text{dataLabels}^{\rho + 1} = \text{dataKeys}^\rho_{\text{digest}}\). Since \(e_{\text{digest}} \leftarrow \text{SendWrite}(\text{crs}, \text{digest}^\rho, L, \text{wData}, \text{digestKeys})\) and \(\text{digestLabels}^{\rho + 1} = \text{ReceiveWrite}^{\rho}_{\text{crs}}(L, \text{wData}, e_{\text{digest}})\), by correctness of the \(\ell\text{OT}\) scheme, \(\text{digestLabels}^{\rho + 1} = \text{digestKeys}^{\rho + 1}_{\text{digest}}\) where \((\hat{D}', \text{digest}') = \text{Hash(}\text{crs}, D')\) for an updated database \(D'\) (\(D'\) is identical to \(D^\rho\) except that \(D'[L] = wData\)). Hence \(\widetilde{C}^{\text{step}}_{\rho + 1}\) is evaluated on inputs \((\text{state}^\rho, 0, \text{digest}')\), which is exactly \((\text{state}^{\rho + 1}, \text{rData}^{\rho + 1}, \text{digest}^{\rho + 1})\). In addition, \((\hat{D}^\rho, \text{digest}^\rho)\) gets updated to \((\hat{D}', \text{digest}')\), which is exactly \((\hat{D}^{\rho + 1}, \text{digest}^{\rho + 1})\).
3.2.6 Security Proof

In this section we prove sender privacy and receiver privacy as defined in Section 3.2.3 under the decisional Diffie-Hellman (DDH) assumption. The receiver privacy follows directly from the receiver security of $\text{OT}$. Below we prove sender privacy by describing a PPT simulator $\text{niscSim}$ such that for every database $D \in \{0, 1\}^M$ where $M$ is polynomial in $\kappa$, and for every RAM program $(P, x, t)$, let $y = P^D(x)$ be the output of the program, and $\text{MemAccess}$ be the memory access pattern, then it holds that

$$\left( \text{crs}, (m_1, \tilde{D}), \text{EncProg}(\text{crs}, m_1, (P, x, t)) \right) \approx \left( \text{crs}, (m_1, \tilde{D}), \text{niscSim}(\text{crs}, m_1, D, y, \text{MemAccess}) \right),$$

where $\text{crs} \leftarrow \text{Setup}(1^\kappa)$, $(m_1, \tilde{D}) \leftarrow \text{EncData}(\text{crs}, D)$. Notice that this definition is slightly different from the definition in Section 3.2.3, but in the semi-honest case it implies a simulator as defined in Section 3.2.3

1. Sample input keys $(\text{stateKeys}^{t+1}, \text{dataKeys}^{t+1}, \text{digestKeys}^{t+1})$ for $C^{\text{step}}$.
2. Parse $\text{MemAccess}$ as $\{(R/W^\tau, L^\tau, wData^\tau) : \tau \in [t]\}$, where $(R/W^\tau, L^\tau, wData^\tau)$ is partial output of the $\tau$-th CPU step circuit. Compute $(rData^\tau, D^\tau, digest^\tau)$ at the beginning of step $\tau$ for every $\tau \in [t+1]$.
3. Compute $(\text{stateLabels}^{t+1}, \text{dataLabels}^{t+1}, \text{digestLabels}^{t+1})$:
   - $\text{stateLabels}^{t+1} \leftarrow \text{stateKeys}^{t+1}$
   - $\text{digestLabels}^{t+1} \leftarrow \text{digestKeys}^{t+1}$
   - $\text{dataLabels}^{t+1} \leftarrow \text{dataKeys}^{t+1}$
4. For $\tau = t$ downto 1, proceed as follows:
   - if $R/W^\tau = \text{read}$ then
     $$e_{\text{data}} \leftarrow \ell \text{OTSim} \left( \text{crs}, D^\tau, L^\tau, \text{dataLabels}^{\tau+1} \right).$$
     $$X \leftarrow (\text{stateLabels}^{\tau+1}, e_{\text{data}}, \text{digestLabels}^{\tau+1}).$$
   - else
     $$e_{\text{digest}} \leftarrow \ell \text{OTSimWrite} \left( \text{crs}, D^\tau, L^\tau, wData^\tau, \text{digestLabels}^{\tau+1} \right).$$
     $$X \leftarrow (\text{stateLabels}^{\tau+1}, \text{dataLabels}^{\tau+1}, e_{\text{digest}}, wData^\tau).$$
   $$\left( \mathcal{C}^{\text{step}}_\tau, \text{stateLabels}^\tau, \text{dataLabels}^\tau, \text{digestLabels}^\tau \right) \leftarrow \text{CircSim}(1^\kappa, C^{\text{step}}, (X, R/W^\tau, L^\tau)).$$
5. $L \leftarrow \text{OT}_2 \left( m_1, (\text{digestLabels}^1, \text{digestLabels}^1) \right)$.
6. Output $\left( L, \{\mathcal{C}^{\text{step}}_\tau\}_{\tau=1}^t, \text{stateKeys}^{t+1} \right)$.

We show that the above simulation is indistinguishable from the real execution through a sequence of hybrids where the first hybrid outputs the real execution and the last hybrid outputs the simulated one.
CHAPTER 3. NON-INTERACTIVE SECURE COMPUTATION

- $H_{2i}$ for $i \in \{0, 1, \ldots, t\}$: Notice that in the output, there are $t$ garbled step circuits $\{\tilde{C}_i^{\text{step}}\}_{i=1}^{t}$. In hybrid $H_{2i}$, the garbled step circuits from 1 to $i$ are simulated while the remaining step circuits ($i+1$ to $t$) are generated honestly. Given the program, all the intermediate outputs of every step circuit can all be computed. Given the correct output of circuit $C_i^{\text{step}}$, the step circuits from 1 to $i$ can be simulated one by one from the $i$-th to the first similarly as niscSim. More formally, it proceeds as follows.

1. Execute $P^{D}(x)$ to obtain $(R/W^{i}, L^{i}, GW^{i})$ for every $i \in [t]$ and state$^{i+1} = y$.
   Compute $(rData^{i}, D^{i}, digest^{i})$ at the beginning of step $i$ for every $i \in [t+1]$.
2. Generate the garble circuits $\{\tilde{C}_i^{\text{step}}\}_{i=1}^{t}$ honestly (same as Step 1 in EncProg).
3. Let $(\text{stateKeys}_{i+1}^{i+1}, \text{dataKeys}_{i+1}^{i+1}, \text{digestKeys}_{i+1}^{i+1})$ be the input keys of $\tilde{C}_i^{\text{step}}$.
4. Compute $(\text{stateLabels}_{i+1}^{i+1}, \text{dataLabels}_{i+1}^{i+1}, \text{digestLabels}_{i+1}^{i+1})$:
   - $\text{stateLabels}_{i+1}^{i+1} \leftarrow \text{stateKeys}_{i+1}^{i+1}$.
   - $\text{digestLabels}_{i+1}^{i+1} \leftarrow \text{digestKeys}_{i+1}^{i+1}$.
   - $\text{dataLabels}_{i+1}^{i+1} \leftarrow \text{dataKeys}_{i+1}^{i+1}$.
5. For $\tau = i$ downto 1, proceed as in Step 4 of the simulator niscSim.
6. L $\leftarrow$ OT$_2$ $(m_1, (\text{digestLabels}_i^{i+1}, \text{digestLabels}_i^{i+1}))$.
7. Output $\left(L, \{\tilde{C}_i^{\text{step}}\}_{i=1}^{t}, \text{stateKeys}_{i+1}^{i+1}\}$.

- $H_{2i+1}$ for $i \in \{0, \ldots, t-1\}$: Hybrid $H_{2i+1}$ is identical to $H_{2i}$ except that $H_{2i+1}$ simulates $\tilde{C}_i^{\text{step}}$ based on the real output of $C_i^{\text{step}}$. In particular, $H_{2i+1}$ is the same as $H_{2i}$ except that Steps 2, 3, 4 proceed as follows:

2. Generate the garble circuits $\{\tilde{C}_i^{\text{step}}\}_{i=1}^{t}$ honestly (same as Step 1 in EncProg).
3. Let $(\text{stateKeys}_{i+2}^{i+2}, \text{dataKeys}_{i+2}^{i+2}, \text{digestKeys}_{i+2}^{i+2})$ be the input keys of $\tilde{C}_i^{\text{step}}$.
4. if $R/W^{i+1} = \text{read}$ then
   - $e_{\text{data}} \leftarrow \text{Send(crs, digest}_{i+1}^{i+1}, L^{i+1}, \text{dataKeys}_{i+1}^{i+1})$.
   - $X \leftarrow (\text{stateKeys}_{i+2}^{i+2}, e_{\text{data}}, \text{digestKeys}_{i+2}^{i+2})$.
else
   - $e_{\text{digest}} \leftarrow \text{SendWrite(crs, digest}_{i+1}^{i+1}, L^{i+1}, \text{data}_{i+1}^{i+1}, \text{digestKeys}_{i+1}^{i+1})$.
   - $X \leftarrow (\text{stateKeys}_{i+1}^{i+1}, \text{dataKeys}_{i+1}^{i+1}, e_{\text{digest}}, \text{data}_{i+1}^{i+1})$.
   - $\left(\tilde{C}_i^{\text{step}}, \text{stateLabels}_{i+1}^{i+1}, \text{dataLabels}_{i+1}^{i+1}, \text{digestLabels}_{i+1}^{i+1}\right) \leftarrow \text{CircSim}(1^\kappa, C_{\text{step}}^{\text{step}}, (X, R/W^{i+1}, L^{i+1}))$.

It is easy to see that $H_0$ is the output of the real execution, and $H_2$ is the simulated output. Now we prove that the consecutive hybrids are computationally indistinguishable. Below we prove that $H_{2i} \approx H_{2i+1} \approx H_{2(i+1)}$ for every $i \in \{0, \ldots, t-1\}$. Since hybrid $H_{2i+1}$ simulates $\tilde{C}_i^{\text{step}}$ based on the real output of $C_i^{\text{step}}$, the output of $\tilde{C}_i^{\text{step}}$ is identical for hybrids
\(H_2\) and \(H_{2i+1}\). That said, indistinguishibility of hybrids \(H_2\) and \(H_{2i+1}\) follows from the garbled circuit security. Next, indistinguishability between \(H_{2i+1}\) and \(H_{2(i+1)}\) follows from the sender’s privacy property of the updatable laconic OT since the laconic OT responses are simulated in \(H_{2(i+1)}\). This concludes the proof.

### 3.2.7 Extension

For simplicity of exposition, the protocol we described so far is for a single sender executing a single program with the receiver. It can be extended to the setting where a sender can execute a sequence of programs on a persistent database. Moreover, the message \(m_1\) published by the receiver can be used by multiple senders, in which case the receiver maintains a different copy of the database for every sender.

**Executing Multiple Programs on a Persistent Database.** After receiving the first message \(m_1\) from the receiver, a sender can run multiple programs on a persistent database (with initial content \(D\)) by sending one message per program to the receiver. For security we require only the output and the memory access pattern of every program execution are revealed to the receiver. We also require that once \(m_1\) is published, the computational cost of both the sender and the receiver for every program should grow only with the running time of the RAM computation, and is independent of the size of \(D\). The NISC-RAM scheme we constructed in Section 3.2.4 can be naturally extended to the multi-program setting. We explain the extension by describing the changes of \(\text{EncProg}\) and \(\text{Dec}\) procedures for the second program. Encryption and evaluation of more programs would follow analogously.

- **EncProg:** When encrypting the first program, the sender should store locally \(\text{digestKeys}^* = \text{digestKeys}^{t+1}\). Then, when encrypting the second program, there are two changes in \(\text{EncProg}\) compared to encrypting the first program: (1) \(\text{digestKeys}^*\) is used as the digest keys of the first step circuit, (2) \(L\) is not generated.

- **Dec:** When evaluating the first program, the receiver should store locally \(\text{digestLabels}^* = \text{digestLabels}^{{t+1}}\). Then, when evaluating the second program, the sender should use \(\text{digestLabels}^*\) as the digest labels for the first step circuit.

**Multiple Senders with a Single Receiver.** The above protocol also works for multiple parallel senders. That is, after the receiver publishes the first message \(m_1\), every sender \(S\) can send a message \(m_S\) to the receiver enabling the execution of \(P^D_S(x_S)\), where \(D\) is the initial database of the receiver, and \((P_S, x_S)\) is the program of \(S\). Security follows from the security of OT which supports multiple second OT messages with the same first OT message. Moreover, every sender can execute a sequence of programs on a persistent database. In this case, the receiver keeps a different copy of her initial database for every sender.
3.3 Security Against Malicious Adversaries

The aforementioned results are obtained in the semi-honest setting. We can upgrade to security against a malicious sender by use of (i) non-interactive zero knowledge proofs (NIZKs) [FLS90] at the cost of additionally assuming doubly enhanced trapdoor permutations or bilinear maps [CHK04; GOS06], (ii) the techniques of Ishai et al. [IKO+11] while obtaining slightly weaker security,\(^4\) or (iii) interactive zero-knowledge proofs but at the cost of additional interaction.

Upgrading to security against a malicious receiver is tricky. This is because the receiver’s public encoding is short and hence, it is not possible to recover the receiver’s entire database just given the encoding. Standard simulation-based security can be obtained by using (i) universal arguments as done by [CV12; COV15] at the cost of additional interaction, or (ii) using SNARKs at the cost of making extractability assumptions [BCCT12; BCG+13].\(^5\)

\(^4\)The receiver is required to keep the output of the computation private.

\(^5\)We finally note that relaxing to the weaker notion of indistinguishability-based security we can expect to obtain the best of both worlds, i.e. a non-interactive solution while making only a black-box use of the adversary (a.k.a. avoiding the use of extractability assumptions). We leave this open for future work.
Chapter 4

Homomorphic Encryption for RAM

In this chapter, we present another application of laconic OT, that is multi-hop homomorphic encryption for RAM programs. In Section 4.1 we give a technical overview of this application and then we formalize our model in Section 4.2. We present our construction with unprotected memory access (UMA) security in Section 4.3 and enhance it to obtain full security in Section 4.4.

4.1 Technical Overview

Our Model. We consider a scenario where a server $S$, holding an input $x$, publishes a public key $pk$ and an encryption $ct$ of $x$ under $pk$. Now this ciphertext is passed on to a client $Q$ that will compute a (possibly private) program $P$ accessing memory $D$ on the value encrypted in $ct$, obtaining another ciphertext $ct'$. Finally, we want that the server can use its secret key to recover $P^D(x)$ from the ciphertext $ct'$ and $\tilde{D}$, where $\tilde{D}$ is an encrypted form of $D$ that has been previously provided to $S$ in a one-time setup phase. More generally, the computation could be performed by multiple clients $Q_1, \ldots, Q_n$. In this case, each client is required to place a pre-processed version of its database $\tilde{D}_i$ with the server during setup. The computation itself could be performed in different sequences of the clients (for different extensions of the model, see Section 4.2). Examples of two such computation paths are shown in Figure 1.1.

For security, we want IND-CPA security for server’s input $x$. For honest clients, we want program-privacy as well as data-privacy, i.e., the evaluation does not leak anything beyond the output of the computation even when the adversary corrupts the server and any subset of the clients. We note that data-privacy is rather easy to achieve via encryption and ORAM. Hence we focus on the challenges of achieving UMA security for honest clients, i.e., the adversary is allowed to learn the database $D$ as well as memory access pattern of $P$ on $D$.

UMA Secure Multi-Hop Scheme. We first build on the ideas from non-interactive secure computation for RAM programs. Every client first passes its database to the server.
Then in every round, the server sends an OT message for input $x$. We assume for simplicity that every client has an up-to-date digest of its own database. Next, the first client $Q_1$ generates a garbled program for $P_1$, say $ct_1$ and sends it to $Q_2$. Here, the garbled program consists of $t_1$ ($t_1$ is the running time of $P_1$) garbled circuits accessing $D_1$ via laconic OT as described in the previous application. Now, $Q_2$ appends its garbled program for $P_2$ to the end of $ct_1$ and generates $ct_2$ consisting of $ct_1$ and new garbled program. Note that $P_2$ takes the output of $P_1$ as input and hence, the output keys of the last garbled circuit of $P_1$ have to be compatible with the input keys of the first garbled circuit of $P_2$ and so on. If we continue this procedure, after the last client $Q_n$, we get a sequence of garbled circuits where the first $t_1$ circuits access $D_1$, the next set accesses from $D_2$ and so on. Finally, the server $S$ can evaluate the sequence of garbled circuits given $D_1, \ldots, D_n$. It is easy to see that correctness holds. But we have no security for clients.

The issue is similar to the issue pointed out by [GHV10] for the case of multi-hop garbled circuits. If the client $Q_{i-1}$ colludes with the server, then they can learn both input labels for the garbled program of $Q_i$. To resolve this issue it is crucial that $Q_i$ re-randomizes the garbled circuits provided by $Q_{i-1}$. For this we rely on re-randomizable garbled circuits provided by [GHV10], where given a garbled circuit anyone can re-garble it such that functionality of the original circuit is preserved while the re-randomized garbled circuit is unrecognizable even to the party who generated it. In our protocol we use re-randomizable garbled circuits but we stumble upon the following issue.

Recall that in the RAM application above, a garbled circuit outputs the laconic OT ciphertexts corresponding to the input keys of the next circuit. Hence, the input keys of the $(\tau + 1)$-th circuit have to be hardwired inside the $\tau$-th circuit. Since all of these circuits will be re-randomized for security, for correctness we require that we transform the hardwired keys in a manner consistent with the future re-randomization. But for security, $Q_{i-1}$ does not know the randomness that will be used by $Q_i$.

Our first idea to resolve this issue is as follows: The circuits generated by $Q_{i-1}$ will take additional inputs $s_1, \ldots, s_n$ which are the randomness used by future parties for their re-randomization procedure. Since we are in the non-interactive setting, we cannot run an OT protocol between clients $Q_{i-1}$ and later clients. We resolve this issue by putting the first message of OT for $s_j$ in the public key of client $Q_j$ and client $Q_{i-1}$ will send the OT second messages along with $ct_{i-1}$. We do not want the clients’ public keys to grow with the running time of the programs, hence, we think of $s_j$ as PRF keys and each circuit re-randomization will invoke the PRF on a unique input.

The above approach causes a subtle issue in the security proof. Suppose, for simplicity, that client $Q_i$ is the only honest client. When arguing security, we want to simulate all the garbled circuits in $ct_i$. To rely on the security of re-randomization, we need to replace the output of the PRF with key $s_i$ with uniform random values but this key is fed as input to the circuits of the previous clients. We note that this is not a circularity issue but makes arguing security hard. We solve this issue as follows: Instead of feeding in PRF keys directly to the garbled circuits, we feed in corresponding outputs of the PRF. We generate the PRF output via a bunch of PRF circuits that take the PRF keys as input (see Figure 4.1). Now
Figure 4.1: One step circuit for $P_i$ along with the attached PRF circuits generated by $Q_i$.

during simulation, we first simulate these PRF circuits, followed by the simulation of the main circuits.

4.2 Our Model

Consider a server $S$ and a collection of clients $Q_1, Q_2, \ldots$ with private databases $D_1, D_2, \ldots$, respectively. The clients ship their encrypted databases to $S$ to be computed on later in multiple executions in a persistent manner. At the beginning of any execution, the server $S$ encrypts his private input $x$ as $ct_0$, chooses a subset of clients $Q_{i_1}, \ldots, Q_{i_n}$ and sends the $ct_0$ to client $Q_{i_1}$. Next, for all $j \in [n]$, client $Q_{i_j}$ homomorphically evaluates an arbitrary program $P_j$ of his choice on $ct_{j-1}$ to obtain $ct_j$. Finally, client $Q_{i_n}$ sends $ct_n$ to the server $S$. The server decrypts this ciphertext using his secret key of encryption as well as encrypted databases sent earlier to learn $P^{D_{i_n}}_n (\ldots P^{D_{i_1}}_1 (x) \ldots)$. During this execution, the databases get updated and future execution of any client happens on respective updated databases.

We require that the size of the ciphertext only grows with the cumulative running time of all programs in an execution and is independent of the size of the databases. For security, we require program and data privacy for all honest clients against an adversary that corrupts
CHAPTER 4. HOMOMORPHIC ENCRYPTION FOR RAM

the server and any subset of the clients. Next, we describe the model formally.

Syntax. We say that an ordered sequence of RAM programs $P_1, \ldots, P_n$ are compatible if the output length of $P_i$ is the same as the input length of $P_{i+1}$ for every $i \in [n - 1]$. A multi-hop RAM homomorphic encryption scheme $\text{mhop-RAM} = (\text{Setup, KeyGen, InpEnc, EncData, Eval, Dec})$ has the following syntax. We define the algorithms with regard to clients $Q_1, \ldots, Q_n$.

- **Setup.** $\text{crs} \leftarrow \text{Setup}(1^\kappa)$.
  On input the security parameter $1^\kappa$, it outputs a common reference string.

- **Key Generation.** $(pk, sk) \leftarrow \text{KeyGen}(1^\kappa)$.
  On input the security parameter $1^\kappa$, it outputs a public/secret key pair $(pk, sk)$.

- **Database Encryption.** $(\tilde{D} = (\hat{D}, \text{digest})) \leftarrow \text{EncData}(\text{crs}, D)$.
  On input the common reference string $\text{crs}$ and database $D \in \{0, 1\}^M$, it outputs an encrypted database $\tilde{D} = (\hat{D}, \text{digest})$, where $\text{digest}$ is a short digest of the database.

- **Input Encryption.** $(ct, x_{\text{secret}}) \leftarrow \text{InpEnc}(x)$.
  On input $S$’s input $x$, it outputs a ciphertext $ct$ and secret state for $S$ denoted by $x_{\text{secret}}$.

- **Homomorphic Evaluation.** $ct' \leftarrow \text{Eval}(\text{crs}, i, \{pk_j\}_{j=i+1}^n, ct, sk, (P, t), \text{digest})$.
  It takes as input the $\text{crs}$, the client number $i$, the public keys of the clients later in the sequence, i.e., $Q_{i+1}, \ldots, Q_n$, a ciphertext from the previous client, $Q$’s secret key $sk$, $Q$’s RAM program $P$ with maximum run-time $t$ and the digest $\text{digest}$ of the database $D$ of $Q$. It then outputs a new ciphertext $ct'$.

- **Decryption.** $y = \text{Dec}^{\tilde{D}_1, \ldots, \tilde{D}_n}(\text{crs}, x_{\text{secret}}, ct)$.
  On input the $\text{crs}$, server’s state $x_{\text{secret}}$, the final ciphertext $ct$ from client $Q_n$, and RAM access to encrypted databases $\tilde{D}_1, \ldots, \tilde{D}_n$, it outputs $y$. The procedure $\text{Dec}$ is itself modeled as a RAM program that can read and write to arbitrary locations of its database initially containing $\tilde{D}_1, \ldots, \tilde{D}_n$.

Next, we describe how these algorithms are used in a real execution.

Real Scenario. In our multi-hop scheme for RAM programs, after the initialization phase that generates the common-random string $\text{crs}$, each client runs key generation to generate the public key and the secret key, followed by the database encryption. The encrypted database is sent to the server, and the client stores the digest of the database locally. After this initialization phase, the server $S$ can initiate various executions of RAM computations with different subsets of the clients. After each execution, the database of a client gets updated by the server during the decryption phase. It is ensured that the server also learns
the updated digest of the database that is communicated to the clients during the start of the next execution.

At the onset of any execution, the server $S$ encrypts his input and sends the ciphertext $ct_0$ to the first client $Q_1$ and maintains $x_{\text{secret}}$ to be used later. The client $Q_1$ generates the ciphertext $ct_1$ using his program $P_1$ and digest $\text{digest}_1$ and sends it to $Q_2$. Similarly, when a client $Q_i$ receives $ct_{i-1}$ from $Q_{i-1}$, it uses program $P_i$ and $\text{digest}_i$ to generate $ct_i$ and sends it to $Q_{i+1}$. This continues and finally, $Q_n$ sends $ct_n$ back to the server $S$. Then, the server runs the decryption procedure on $ct_n$ using all the encrypted databases and secret state $x_{\text{secret}}$ to obtain output $y$.

For the case of multiple executions, each of the above procedures take the session identifier $\text{sid}$ as additional input. We denote by $\tilde{D}_1^{\text{(sid)}}$, $\ldots$, $\tilde{D}_n^{\text{(sid)}}$ the encrypted databases before the execution with session identifier $\text{sid}$. Initially $\tilde{D}^{\text{(1)}} = \tilde{D}_1$, $\ldots$, $\tilde{D}_n^{\text{(1)}} = \tilde{D}_n$.

We require the algorithms above to satisfy the correctness, sender privacy, client privacy and efficiency properties described below.

**Correctness.** We require that in a sequence of executions, each output of homomorphic evaluation equals the output of the corresponding computation in the clear. We formalize this as follows: For every set of keys $\{(pk_i, sk_i)\}_{i=1}^n$ in the support of KeyGen, and any collection of initial databases $D_1, \ldots, D_n$, for any unbounded polynomial $N$ number of executions the following holds: For $\text{sid} \in \mathbb{N}$, let $P_1^{\text{(sid)}}, \ldots, P_n^{\text{(sid)}}$ be the sequence of programs, $x^{\text{(sid)}}$ be the server’s input and $D_i^{\text{(sid)}}$ be the resulting database after executing the session $\text{sid}$-1 in the clear, then

$$\Pr\left[\text{Dec}^{\text{(sid)}}(\text{crs}, x_{\text{secret}}^{\text{(sid)}}, ct_n^{\text{(sid)}}) = P_n^{\text{(sid)}D_n^{\text{(sid)}}}\left(\ldots\left(P_1^{\text{(sid)}D_1^{\text{(sid)}}}(x^{\text{(sid)}})\right)\ldots\right]\right] = 1,$$

where $\tilde{D}_i^{\text{(sid)}}$ is the resulting garbled database after executing $\text{sid}$-1 homomorphic evaluations, $(ct_0^{\text{(sid)}}, x_{\text{secret}}^{\text{(sid)}}) \leftarrow \text{InpEnc}(x^{\text{(sid)}}), ct_i^{\text{(sid)}} \leftarrow \text{Eval}(\text{crs}, i, \{pk_j\}_{j=i+1}^n, ct_{i-1}^{\text{(sid)}}, sk, P_i^{\text{(sid)}}, t_i^{\text{(sid)}}, \text{digest}_i^{\text{(sid)}})$.

**Server Privacy (Semantic Security).** For server privacy, we require that for every pair of inputs $(x_0, x_1)$, let $(\text{CT}_b, x_{\text{secret}}^b) \leftarrow \text{InpEnc}(x_b)$ for $b \in \{0, 1\}$, then

$$\text{CT}_0 \approx \text{CT}_1.$$

**Client Privacy (Program Privacy) with Unprotected Memory Access (UMA).** We define client privacy against a semi-honest adversary that corrupts the server $S$ as well as an arbitrary subset of clients $\mathcal{I} \subseteq [n]$. Intuitively, we require program-privacy for the honest clients such that the adversary cannot learn anything beyond the output of the honest client’s program on one input. We formalize this as follows:

There exists a PPT simulator $\text{ihopSim}$ such that the following holds. Let $\text{crs} \leftarrow \text{Setup}(1^\kappa)$, for every set of keys $\{(pk_i, sk_i)\}_{i=1}^n$ in the support of KeyGen, and any collection of initial
databases $D_1, \ldots, D_n$, for any unbounded polynomial $N$ number of executions: For $\text{id} \in \mathbb{N}$, let $P_1^{(\text{id})}, \ldots, P_n^{(\text{id})}$ be the sequence of programs, $x^{(\text{id})}$ be the server’s input, then

$$(\text{crs}, (\bar{D}_1, \ldots, \bar{D}_n), \left\{ \text{ct}_0^{(\text{id})}, \text{ct}_1^{(\text{id})}, \ldots, \text{ct}_n^{(\text{id})} \right\}_{\text{id} \in [N]}),$$

$$(\text{crs}, \text{ihopSim} \left( \text{crs}, \left\{ \left\{ \text{pk}_i, \text{sk}_i \right\}_{i \in [n]}, \left\{ D_j, P_j^{(\text{id})} \right\}_{j \in \mathcal{I}, x^{(\text{id})}}, \left\{ D_j, \text{MemAccess}^{(\text{sid})}_j, y_j^{(\text{sid})} \right\}_{j \in [n] \setminus \mathcal{I}} \right\}_{\text{id} \in [N]} \right) \approx$$

where $\bar{D}_i, \text{ct}_i^{(\text{id})}$ corresponds to outputs in the real execution given all the programs and databases and $y_j^{(\text{id})} = P_j^{(\text{sid})} D_j^{(\text{id})} \left( \cdots \left( P_1^{(\text{sid})} D_1^{(\text{id})} (x^{(\text{id})}) \right) \right) \cdots$.

Remark 4.2.1. We note that the above definition also captures the security against a semi-malicious adversary who may choose his randomness for $\text{KeyGen}$ maliciously but behaves honestly in the protocol.

Client Privacy (Program Privacy) with Full Security. For full client privacy, the simulator does not get the database or access pattern of the honest clients. That is, the simulator $\text{ihopSim}$ takes as input

$$\left\{ \left\{ \text{pk}_i, \text{sk}_i \right\}_{i \in [n]}, \left\{ D_j, P_j^{(\text{id})} \right\}_{j \in \mathcal{I}, x^{(\text{id})}}, \left\{ 1_{M_j}, 1_j^{(\text{id})}, y_j^{(\text{id})} \right\}_{j \in [n] \setminus \mathcal{I}} \right\}_{\text{id} \in [N]}$$

where $M_j$ is the size of $D_j$ and $t_j^{(\text{id})}$ is the running time of $P_j^{(\text{id})}$.

Efficiency. We require the following efficiency guarantees from $\text{mhop-RAM}$. Let $M_i = |D_i|$.

- $|\bar{D}_i| = M_i \cdot \text{poly}(\kappa, \log M_i)$ for all $i \in [n]$.
- $|\text{ct}_0| = |x| \cdot \text{poly}(\kappa)$, where $\text{ct}_0$ is the output of $\text{InpEnc}(x)$.
- $|\text{ct}_i| = \sum_{j=1}^i n \cdot t_j \cdot \text{poly}(\kappa, \log M_j, \log t_j)$ for all $i \in [n]$.

Extension. This definition (and our construction) can be extended to the setting where in each execution all the clients do not necessarily join the homomorphic evaluation. We allow for different set of clients to participate in different executions. In particular, before the first execution, the initial database of every client is encrypted. Later before each execution, a sequence of distinct clients $\langle i_1, \ldots, i_m \rangle$ can be specified.

The input encryption is the same as above, while the homomorphic evaluation is executed in the specified order (as specified by the server) as $\text{ct}_j \leftarrow \text{Eval}(\text{crs}, j, \{\text{pk}_{i_u}\}_{u=j+1}^n, \text{sk}_{i_j}, \text{ct}_{j-1}, P_{i_j}, t_{i_j}, \text{digest}_{i_j})$ for every $j \in [m]$. And the decryption is executed as $y = \text{Dec}^{\bar{D}_1, \ldots, \bar{D}_m} (\text{crs}, x_{\text{secret}}, x_{m})$, where $\bar{D}_1, \ldots, \bar{D}_m$ are the up-to-date garbled databases of clients $Q_{i_1}, \ldots, Q_{i_m}$. The correctness and privacy properties can be naturally extended to this setting.
4.3 UMA-Secure Construction

In this section, we first describe the UMA-secure scheme for a single execution in Section 4.3.2 and then explain how this scheme can be extended naturally for multiple executions in Section 4.3.5. Also, as we shall see our scheme can easily be extended to the setting where different subset of parties participate in each session. The correctness and security proofs are presented in Sections 4.3.3 and 4.3.4, respectively. Necessary background for our construction is given in Section 4.3.1.

4.3.1 Background

In this section we introduce building blocks needed in our construction. The two-message secure function evaluation (SFE) and re-randomizable secure function evaluation that we consider are both based on garbled circuits (see Section 2.4.1.1 for the definition of garbled circuits). In addition to these building blocks, we will also need RAM computation model (see Section 3.2.2.1) and two-message oblivious transfer (see Section 3.2.2.2). We use $\mathbb{Z}_n$ to denote the set $\{1, \ldots, n\}$.

4.3.1.1 Two-Message Secure Function Evaluation

A two-message secure function evaluation (SFE) based on garbled circuits is as follows: Let $U(\cdot, \cdot)$ be a particular “universal circuit evaluator” that takes as input the description of a circuit $C$ and an argument $x$, and returns $U(C, x)$. Let Alice be the client with private input $x$ and Bob have private input a circuit $C$. The protocol is as follows:

1. $(m_1, x_{\text{secret}}) \leftarrow \text{SFE}_1(x)$: Alice computes $(m_1, x_{\text{secret}}) \leftarrow \text{OT}_1(x)$ and sends $m_1$ to Bob.

2. $m_2 \leftarrow \text{SFE}_2(C, m_1)$: Bob computes $\tilde{C} \leftarrow \text{GCircuit}(C, \{\text{lab}_{w}^{x} \}_{w \in \text{inp}(C), b \in \{0, 1\}})$ and $L \leftarrow \text{OT}_2(m_1, \{\text{lab}_{w}^{x} \}_{w \in \text{inp}(C), b \in \{0, 1\}})$. Sends $m_2 := (\tilde{C}, L)$.

3. $y = \text{SFE}_\text{out}(x_{\text{secret}}, m_2)$: Alice locally computes the output: $\{\text{lab}_{x}^{w} \}_{w \in \text{inp}(C)} \leftarrow \text{OT}_3(L, s_{\text{secret}})$, and $y = \text{GEval}((\tilde{C}, \{\text{lab}_{x}^{w} \}_{w \in \text{inp}(C)}))$.

The correctness of the above protocol follows from the correctness of Yao garbled circuits. It can be shown that the above protocol is a secure function evaluation protocol satisfying both semi-honest client privacy and semi-honest server privacy.

4.3.1.2 Re-Randomizable Secure Function Evaluation

[GHV10] defined the tool of “re-randomizable secure function evaluation” that was used to realize multi-hop homomorphic computation for circuits. This tool was constructed under the DDH assumption by instantiating Yao’s garbled circuits with a special encryption scheme (BHHO [BHHO08]) and using re-randomizable two-message oblivious transfer [NP01].
Definition 4.3.1. A secure function evaluation protocol is said to be re-randomizable if there exists an efficient procedure \( \text{Re-rand} \) such that for every input \( x \) and function \( f \) and every \((m_1, x, \text{secret}) \in \text{SFE}_1(x) \) and \( m_2 \in \text{SFE}_2(C, m_1), \) the distributions \( \{x, C, m_1, x, \text{secret}, m_2, \text{Re-rand}(m_1, m_2)\} \) and \( \{x, C, m_1, x, \text{secret}, m_2, \text{SFE}_2(C, m_1)\} \) are computationally indistinguishable.

[GHV10] proved the following:

Theorem 4.3.2 ([GHV10]). Under the DDH assumption, there exists a re-randomizable secure function evaluation protocol satisfying Definition 4.3.1.

Below, we abstract out the scheme of [GHV10] by stating some of the procedures implicitly provided by [GHV10] that will be needed for this paper.

Definition 4.3.3 (Re-randomizable Yao garbled circuits.). The scheme in [GHV10] provides the following algorithms (implicitly) for their re-randomizable Yao scheme.

1. \( \text{Keys} = \text{SampleKeys}(1^\kappa, W; r) \): Takes as input a set of input wires \( W \) as well randomness \( r \) and outputs the input-keys for set of wires \( W \) for re-randomizable Yao. Note that it is a deterministic function given the randomness \( r \). When clear from context, we will skip mentioning the inputs in the calls to this function.

2. \( \tilde{C} \leftarrow \text{ReGCircuit}(C, \text{InpKeys}) \): Takes as input a circuit \( C \) and \( \text{InpKeys} \) for the input wires of \( C \) and outputs a re-randomizable garbled circuit \( \tilde{C} \) where input wires have keys as \( \text{InpKeys} \).

3. \( \tilde{C}' \leftarrow \text{ReGCircuit}'(C, \text{InpKeys}, \text{OutKeys}) \): Takes as input a circuit \( C \), \( \text{InpKeys} \) for input wires of \( C \) and \( \text{OutKeys} \) for output wires of \( C \), and outputs a re-randomizable garbled circuit \( \tilde{C}' \) where input wires have keys as \( \text{InpKeys} \) and output wires have keys as \( \text{OutKeys} \).

4. \( \text{Keys}^\dagger = \text{Transform}(\text{Keys}, r) \): Takes as input \( \text{Keys} \) and randomness \( r \) and outputs randomized keys \( \text{Keys}^\dagger \). Also, we use \( \text{Transform}(\text{Keys}, \{r_1, \ldots, r_k\}) \) to denote

\[
\text{Transform}(\text{Transform}(\ldots (\text{Transform}(\text{Keys}, r_1), \ldots), r_{k-1}), r_k)
\]

5. \( (\tilde{C}', L') \leftarrow \text{Re-rand} \left((\tilde{C}, L), \{r_w\}_{w \in \text{Wires}(C)}\right) \): Takes as input a re-randomizable garbled circuit \( \tilde{C} \) and OT second messages \( L \) for the keys of input wires of \( C \) and randomness to re-randomize each wire of \( C \) and outputs a new functionally equivalent re-randomizable garbled circuit \( \tilde{C}' \) and consistent OT second messages \( L' \). This procedure satisfies the property of re-randomizable SFE. Moreover, the guarantee is that after re-randomization, for any wire \( w \), the new keys for \( w \) in \( \tilde{C}' \) are \( \text{Transform}(\text{lab}^w, r_w) \). Finally, re-randomization of OT messages only requires\(^1\) \( \{r_w\}_{w \in \text{inp}(C)} \).

\(^1\)In fact, each OT message for keys of a wire can be randomized consistently just given the randomness used for that wire.
For our multi-hop homomorphic scheme for RAM it will be useful to define \( \text{SampleKeys}(\cdot) \) as follows: \( \text{SampleKeys}(1^\kappa, W, r) = \text{Transform}(\text{SampleKeys}(1^\kappa, W, 0^r), r) \).

### 4.3.2 Construction For a Single Execution Involving All Parties

Let \( Q_1, \ldots, Q_n \) be the clients holding databases \( D_1, \ldots, D_n \), respectively, and \( S \) be the server. Let the server’s private input be \( x \) and secret programs of clients be \( P_1, \ldots, P_n \), respectively. Let \( t\text{OT} = (\text{crsGen, Commit, Send, Receive, SendWrite, ReceiveWrite}) \) be an updatable laconic OT scheme with sender privacy as defined in Definition 2.2.2. Let \( \text{Re-GC} = (\text{SampleKeys, ReGCircuit, ReGCircuit}', \text{Transform, Re-rand}) \) be a re-randomizable scheme for Yao’s garbled circuits given by [GHV10] (see Definition 4.3.3). Let \( \text{OT} = (\text{OT}_1, \text{OT}_2, \text{OT}_3) \) be a two-message oblivious transfer protocol as defined in Section 3.2.2.2.

The multi-hop RAM scheme \( \text{mhop-RAM} = (\text{Setup, KeyGen, EncData, InpEnc, Eval, Dec}) \) is as follows: The algorithms \( \text{Setup, KeyGen, EncData} \) are formally described in Figure 4.2.

**Setup.** \( \text{crs} \leftarrow \text{Setup}(1^\kappa) \)

Setup algorithm generates the common reference string for laconic OT.

**Key Generation.** \( (\text{pk}, \text{sk}) \leftarrow \text{KeyGen}(1^\kappa) \)

Each client runs this algorithm once to generate the secret-key \( \text{sk} \) and public-key \( \text{pk} \). A client \( Q \) picks a PRF seed \( s \) as the secret key. Next, it generates the public key as the first message of OT for \( s \) and secret-key as the secret state for OT as well as PRF key.

Looking ahead, the client \( Q \) will use the PRF key \( s \) to garble his own \( P \) and to randomize the garbled program generated by all previous clients in any execution.

**Database Encryption.** \( \tilde{D} \leftarrow \text{EncData}(\text{crs}, D) \)

Each client runs this algorithm at the beginning to garble the database and sends the garbled database to the server \( S \). The garbled database is generated by executing the Hash procedure of laconic OT. This outputs an encoded database \( \tilde{D} \) and a digest \( \text{digest} \), both of which are given to the server \( S \).

**Input Encryption.** \( (\text{ct}, x, \text{secret}) \leftarrow \text{InpEnc}(x) \)

In each execution, the server \( S \) encrypts its input \( x \) as follows: It computes the first message of OT as the ciphertext and stores the secret state of OT to be used for decryption of computation later. The ciphertext is sent the first client, w.l.o.g. \( Q_1 \). The algorithm \( \text{InpEnc} \) is described formally in Figure 4.3.

**Homomorphic Evaluation.** \( \text{ct}' \leftarrow \text{Eval}(\text{crs}, i, \{\text{pk}_j\}_{j=i+1}^n, \text{ct}, \text{sk} = (s, s, \text{secret}), (P, t), \text{digest}) \)

This algorithm is executed by client \( Q_i \) to generate the next ciphertext \( \text{ct}' \) given \( \text{ct} \) from client \( Q_{i-1} \), and is described formally in Figure 4.4. This is the most involved procedure in our construction, and hence, we first provide an informal description.
Setup. $\text{crs} \leftarrow \text{Setup}(1^\kappa)$.
   1. Sample $\text{crs} \leftarrow \text{crsGen}(1^\kappa)$.
   2. Output $\text{crs}$.

Key Generation. $(\text{pk}, \text{sk}) \leftarrow \text{KeyGen}(1^\kappa)$.
   1. Sample a PRF key $s \leftarrow \{0, 1\}^\kappa$ and generate $(\text{pk}, s_{\text{secret}}) \leftarrow \text{OT}_1(s)$.
   2. Output $(\text{pk}, \text{sk} := (s, s_{\text{secret}}))$.

Database Encryption. $\tilde{D} \leftarrow \text{EncData}(\text{crs}, D)$.
   1. $(\text{digest}, \hat{D}) \leftarrow \text{Hash}(\text{crs}, D)$.
   2. Output $\tilde{D} = (\text{digest}, \hat{D})$.

Figure 4.2: Description of the setup, key generation and database encryption algorithms.

Input Encryption. $(\text{ct}, x_{\text{secret}}) \leftarrow \text{InpEnc}(\text{crs}, x)$.
   1. $(\text{ct}, x_{\text{secret}}) \leftarrow \text{OT}_1(x)$.

Figure 4.3: Description of the input encryption algorithm.

as illustrated in Figure 4.5, the client $Q_i$ generate the garbled program for $P$ consisting of $t$ garbled circuits and also re-randomize all the circuits in $\text{ct}$. As mentioned before, this re-randomization step is crucial to get program privacy for this client. Moreover, the re-randomization has to be done carefully so that the previous $\text{ct}$ is consistent with the new garbled program.\footnote{We do this by keeping track of the randomness used in randomizing the input wires for each garbled circuit.}

This procedure consists of four main steps: Let $T$ be the number of step-circuits in $\text{ct}$.

Step 1 Garble the new program $P$: For each $\tau \in [T + 1, T + t]$, client does the following: It generates a “super-circuit” that is illustrated in Figure 4.6 consisting of a CPU step circuit $\mathcal{C}^\text{step}_\tau$ (see Figure 4.7) and PRF circuits $\mathcal{C}^\text{PRF}_{\tau,i+1}, \ldots, \mathcal{C}^\text{PRF}_{\tau,n}$ (see Figure 4.8). A step circuit, encodes the logic of a CPU step of a program $P$ and PRF circuits provide a part of the randomness used in re-randomization. We will elaborate on the functionality of PRF circuits later.

The garbled program will consists of garbled circuits corresponding to all the step circuits and PRF circuits. The first step is to pick the keys for the input wires of all of these circuits. Next, we begin by describing the step circuits.

Step Circuits $\mathcal{C}^\text{step}_\tau$ (Figure 4.7): The inputs of a step circuit (see Figure 4.6) can be partitioned into $((\text{state}, r\text{Data}, \text{digest}), Rd)$, where $\text{state}$ is the current CPU state, $r\text{Data}$ is the bit-read from database, and $\text{digest}$ is the up-to-date digest of the database. $Rd$ corresponds to the randomness given as input to the step-circuit computed from the
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Homomorphic Evaluation.

\[
\text{ct}' \leftarrow \text{Eval} \left(\text{crs}, i, \{\text{pk}_j\}_{j=1}^n, \text{ct} = \left(\text{L}_0, \left\{\tilde{C}_\tau^{\text{step}}, \{\tilde{C}_\tau^{PRF}, \text{L}_{\tau,j}\}_{j=i+1}^n\right\}_{\tau \in [T]}\right)\right), \text{sk} = (s, s\_secret), (P, t), \text{digest}. \]

1. Generate the “new” garbled program for \( P \): Generate garbled circuits \( \left\{\tilde{C}_\tau^{\text{step}}, \{\tilde{C}_\tau^{PRF}\}_{\tau,j}^{n}\right\}_{\tau=T+1}^{T+t} \).

   a) Set \( \text{stateKeys}^\tau, \text{dataKeys}^\tau, \text{digestKeys}^\tau, \text{RdKeys}^\tau, \text{PKeys}^\tau \) for each \( \tau \in [T+1, T+t] \) and \( j \in [i+1, n] \) as \( \text{SampleKeys}(F_s(\text{GC}_\star || \tau)) \) where \( F_s(\text{GC}_\star || \tau) \) is the randomness used and \( \star \in \{\text{STATE, DATA, DIGEST, RD}\} \), respectively. Set \( \text{stateKeys}^\tau, \text{dataKeys}^\tau, \text{digestKeys}^\tau \) to \( \text{SampleKeys}(0^\star) \) for \( \tau = T+t+1 \).

   b) \text{Garble} \: \tilde{C}_\tau^{\text{step}} \: \text{circuits}: \text{For each} \: \tau \in [T+1, T+t] \text{ compute} \:

\[
\tilde{\text{C}}_\tau^{\text{step}} \leftarrow \text{ReGCircuit} \left(\text{C}^{\text{step}}[i, \text{crs}, P, \text{Keys}^\tau, F_s(\text{PSI} || \tau)), (\text{Keys}^\tau, \{\text{RdKeys}^\tau\}_{j=i+1}^n)\right), \]

where \( \text{Keys}^\tau = (\text{stateKeys}^\tau, \text{dataKeys}^\tau, \text{digestKeys}^\tau) \).

   Embed labels \( \text{dataKeys}^T_{T+1} \) and \( \text{digestKeys}^{T+1}_{T+1} \) in \( \tilde{\text{C}}_\tau^{\text{step}} \).

   c) \text{Garble} \: \tilde{C}_\tau^{PRF} \: \text{circuits}: \text{For each} \: [T+1, T+t] \text{ and} \: j \in [i+1, n], \text{ compute} \:

\[
\tilde{\text{C}}_\tau^{PRF} \leftarrow \text{ReGCircuit'} \left(\text{C}^{PRF}[\tau], \text{PKeys}^\tau, \text{RdKeys}^\tau\right). \]

2. Generate the OT second messages for newly generated circuits: For all \( \tau \in [T+1, T+t] \) and \( j \in [i+1, n] \) compute \( \text{L}_{\tau,j} \leftarrow \text{OT}_2 \left(\text{pk}_j, \text{PKeys}^\tau\right). \)

3. Obtain partial labels for previous circuits:

   a) For every \( \tau \in [T] \), compute \( \text{M}_{\tau,i} = \text{OT}_3 \left(\text{L}_{\tau,i}, s\_secret\right) \) and \( \tilde{\text{C}}_{\tau,i}^{PRF} \) using input labels \( \text{M}_{\tau,i} \) and embed the labels in \( \tilde{\text{C}}_\tau^{\text{step}} \).

   b) If \( i = 1 \), then \( \text{L}_0 \leftarrow \text{OT}_2 \left(\text{L}_0, \text{stateKeys}^1\right). \)

4. Re-randomize previous garbled circuits: If \( i > 1 \), do the following:

   a) For each \( \tau \in [T] \), re-randomize the circuit \( \tilde{\text{C}}_\tau^{\text{step}} \) using \( \text{Re-rand}(\cdot) \) (see Definition 4.3.3) such that the input wire keys are randomized using \( F_s(\text{GC}_\star || \tau) \), where \( \star \in \{\text{STATE, DATA, DIGEST, RD}\} \) for different input wires appropriately.

   b) For each \( \tau \in [T] \), re-randomize the circuits \( \{\text{C}^{PRF}_{\tau,j}\}_{j=i+1}^n \) and \( \{\text{L}_{\tau,j}\}_{j=i+1}^n \) using \( \text{Re-rand}(\cdot) \) such that the input wires are randomized using \( F_s(\text{GC}_P || \tau) \) and output wires are randomized using \( F_s(\text{GC}_\text{RD}) || \tau \).

   c) Re-randomize \( \text{L}_0 \) using \( F_s(\text{GC}_\text{STATE}) || 1 \).

5. Output \( \text{ct}' = \left(\text{L}_0, \left\{\tilde{\text{C}}_\tau^{\text{step}}, \{\tilde{\text{C}}_\tau^{PRF}, \text{L}_{\tau,j}\}_{j=i+1}^n\right\}_{\tau=1}^{T+t}\right) \).

Figure 4.4: Description of the homomorphic evaluation algorithm.
Homomorphic Evaluation by $Q_i$: $Q_i$ contributes new circuits (denoted in white in the lower layer) and processes the input circuits as follows: (i) computes the yellow circuits, and (ii) re-randomizes all input circuits. The re-randomized circuits are shown in gray color.

PRF circuits. A step circuit executes one CPU step and passes on the updated state, new bit read, and new digest to the next step circuit. Note that we do not achieve this by passing the output wires of $\tau$ into input wires of $\tau + 1$. That is, the output wire of $\tau$ will not have same keys as input wires of $\tau + 1$ (Note that the two consecutive step circuits are not connected by solid lines in Figure 4.5.). Hence, the step circuit $\tau$ will have the keys of the next circuit hard-coded inside it.

Next, we explain the logic of a step-circuit. First, it computes the new $(\text{state}', R/W, L, w\text{Data})$. Next, it computes the transformed keys $\text{nextKeys}^\dagger$ of the next step-circuit using the hard-coded keys and the input randomness (this uses the transform functionality of re-randomizable Yao from Section 4.3.1.2). Then, in the case of a “read” it outputs $\text{stateKeys}^\dagger$ corresponding to new $\text{state}'$, labels for data via laconic OT procedure $\text{Send}(\cdot)$ for location $L$ where the sender’s inputs are $\text{dataKeys}^\dagger_0, \text{dataKeys}^\dagger_1$ and $\text{digestKeys}^\dagger$ corresponding to $\text{digest}$. The case of a write is similar, but now the labels of new updated digest are transferred via laconic OT procedure $\text{SendWrite}(\cdot)$. Note that it follows via
correctness of reads and writes of the laconic OT that the evaluator would be able to recover the correct labels for the read-data and the new digest.

The down-bend in output and input wires of step-circuits for data and digest in Figure 4.5 represents that these keys are not output in the clear, but are output using laconic OT. Correct labels will be learnt during execution using the encoded database \( \tilde{D} \) and laconic OT procedures.

**PRF circuits** \( C^{PRF}_{\tau,j} \) (Figure 4.8): This circuit takes as input a PRF key \( s_j \) of client \( Q_j \) and outputs the PRF value corresponding to time-step \( \tau \). The use of these circuits will be clear when we describe the re-randomization step below.

All these circuits are garbled such that the keys for output wires of PRF circuits are same as keys for Rd input keys of step circuits. In Figure 4.5, this is depicted by joining the output wires of PRF circuits with Rd input wires of step circuit with a solid line. The garbled program consists of garbled step circuits and garbled PRF circuits \( \{ C^{step}_\tau, \{ C^{PRF}_{\tau,j} \}_{j \in \{i+1, \ldots, n\}} \}_{\tau \in [T+1, T+t]} \). The client also embeds labels for \( rData = 0 \) and \( digest \) in the first step circuit.

**Step 2** Generate OT messages for \( C^{PRF}_{\tau,j} \): Recall that this circuit takes as input a PRF key \( s_j \) of
Hard-coded parameters: \([i, \text{crs}, P, \text{nextKeys} = (\text{stateKeys}, \text{dataKeys}, \text{digestKeys}), \psi]\).

Input: \(((\text{state}, r\text{Data}, \text{digest}), (\{\omega_j, \phi_j\}_{j>i})).

\((\text{state}', R/W, L, w\text{Data}) := C_{CPU}^P(\text{state}, r\text{Data}).\)

\(\text{nextKeys}^\dagger := \text{Transform}(\text{nextKeys}, \{\omega_j\}_{j>i}).\)

Parse \(\text{nextKeys}^\dagger\) as \((\text{stateKeys}^\dagger, \text{dataKeys}^\dagger, \text{digestKeys}^\dagger).\)

if \(R/W = \text{read}\) then
\(e_{\text{data}} \leftarrow \text{Send}(\text{crs}, \text{digest}, L, \text{dataKeys}^\dagger; \psi \oplus \bigoplus_{j>i} \phi_j).\)

return \(((\text{stateKeys}^\dagger_{\text{state}'}, e_{\text{data}}, \text{digestKeys}^\dagger_{\text{digest}}), R/W, L).\)

else
\(e_{\text{digest}} \leftarrow \text{SendWrite}(\text{crs}, \text{digest}, L, w\text{Data}, \text{digestKeys}^\dagger; \psi \oplus \bigoplus_{j>i} \phi_j).\)

return \(((\text{stateKeys}^\dagger_{\text{state}'}, \text{dataKeys}^\dagger_0, e_{\text{digest}}, \text{wData}), R/W, L).\)

Figure 4.7: Pseudocode of the step circuit \(C_{\text{step}}^P[i, \text{crs}, P, \text{nextKeys}, \psi].\)

Hard-coded parameters: \([\tau]\).

Input: \(s.\)

Output: \(((F_s(\text{GC}^\ast || \tau + 1)), \tau \in \{\text{STATE, DATA, DIGEST}\}, F_s(\text{LACONIC_OT}||\tau)).\)

Figure 4.8: Pseudocode of the PRF circuit \(C_{\text{PRF}}^P[\tau].\)

client \(Q_j\) whose OT first message is present in \(pk_j.\) Client \(Q_i\) generates the OT second message \(L_{\tau,j}\) for the input keys of \(\tilde{C}_{PRF}^j.\)

Step 3 Evaluating the PRF circuits for itself: Note that the ciphertext \(ct\) consists of a sequence of step circuits and PRF circuits for each step circuit corresponding to \(j \in \{i, \ldots, n\}.\) See Figure 4.5 where the PRF circuits for client \(Q_i\) are depicted in yellow. \(Q_i\) computes the input labels for \(\tilde{C}_{PRF}^j\) using the OT message \(L_{r,i}\) and embeds the output labels in to \(\tilde{C}_{\text{step}}^j\) for all \(\tau \in [T].\) In other words, \(Q_i\) consumes the first PRF circuits from each step of previous ciphertext \(ct.\)

Step 4 Re-randomize the previous circuits: After consuming the first PRF circuit from each step, \(Q_i\) randomizes all the remaining circuits using appropriate randomness. Note that the input keys of \(\tilde{C}_{\text{step}}^j\) are randomized using the exact randomness that was fed into \(C_{\text{step}}^j\) via the PRF circuit for \(Q_i.\) This makes sure that the hard-coded input keys of step \(\tau + 1\) are randomized consistently in the same way as how \(Q_i\) will randomize the circuit \(\tilde{C}_{\text{step}}^{\tau+1}.\)

Hence, to conclude, the PRF circuits are present to provide the randomness needed to randomize the hard-coded keys inside the step circuits \(^3\).

\(^3\)Note that randomization of garbled circuits preserves the functionality. Since the keys for the next
Homomorphic Decryption.  \( y = \text{Dec}^{ar{D}_1, \cdots, \bar{D}_n} \left( \text{crs}, x_{\text{secret}}, ct = \left( L_0, \left\{ \bar{C}_{\text{step}}^\tau \right\}_{\tau \in [T]} \right) \right) \).

The algorithm is described formally in Figure 4.9. It takes as input the secret state of the server \( x_{\text{secret}} \) and the final ciphertext \( ct_n \) consisting of OT message for \( x \) and sequence of \( T \) step-circuits, where \( T = \sum_{i \in [n]} t_i \). Note that all the PRF circuits have been evaluated already by correct parties and correct labels for \( \text{RdKeys} \) have been embedded into the step-circuits. The server does the following for decryption:

1. It obtains the stateLabels for the first circuit by running OT\(_3\). Note that the first circuit of program of any client has labels for data and digest already embedded. Hence, now the server knows all the labels for the first circuit.

2. For \( \tau \in [T] \), the server executes the circuit \( \bar{C}_{\text{step}}^\tau \), and learns the labels for the next circuit via running the receiver algorithms of laconic OT correctly.

### 4.3.3 Correctness

Here we prove correctness (as defined in Section 4.2) for a single execution. In fact, we prove something stronger that would help us extend the scheme to multiple executions in a straightforward manner. We prove the following two properties:

**Property 1.** For the above scheme, \( y = P_n^{D_n} (\ldots P_1^{D_1}(x) \ldots) \), where programs, databases and input \( x \) are as defined above.

**Property 2.** Let \( \hat{D}_i', \text{digest}'_i \) denote the updated encoded database and digest with the server after the execution. We show that these are equal to \( \text{Hash}(\text{crs}, D_i') \), where \( D' \) results after executing \( P_i^{D_i}(P_{i-1}^{D_{i-1}}(\ldots P_1^{D_1}(x) \ldots)) \).

Below we prove correctness via a sequence of facts and claims.

**Fact 4.3.4.** At any point in homomorphic evaluation, the circuit \( \bar{C}_{\text{PRF}}^{\tau,j} \) and the second OT message for its input keys \( L_{\tau,j} \) are consistent.

This follows from correctness of OT\(_2(\cdot, \cdot)\) procedure when it is generated and the fact that re-randomization happens consistently in Re-rand procedure of re-randomizable garbled circuits.

**Fact 4.3.5.** During the homomorphic evaluation of client \( Q_i \), in Step 3a, Figure 4.4 while obtaining partial labels, \( M_{\tau,i} = \text{PKeys}^i_{\tau,i} \), where \( s \) is the PRF key of \( Q_i \).

This follows from the correctness of OT protocol as well as Fact 4.3.4.
\begin{figure}[h]
\centering
\begin{minipage}{\textwidth}
\begin{align*}
\text{Decryption.}
\end{align*}
\end{minipage}
\end{figure}

\begin{align*}
y = \text{Dec}^{\tilde{D}_1, \ldots, \tilde{D}_n} \left( \text{crs}, x_{\text{secret}}, ct = \left( L_0, \left\{ \tilde{C}_{\tau}^{\text{step}} \right\}_{\tau \in [T]} \right) \right), \text{ where } T = \sum_{i \in [n]} t_i.
\end{align*}

1. For all \( i \in [n] \), parse \( \tilde{D}_i = \left( \text{digest}_i, \hat{D}_i \right) \).

2. Compute \( M_0 = \text{OT}_3 (L_0, x_{\text{secret}}) \).

3. Parse \( \tilde{C}_1^{\text{step}} = \left( \tilde{C}_1^{\text{step}}, \text{dataLabels}, \text{digestLabels} \right) \).

4. Define \( \text{Labels}^1 = \left( \text{stateLabels}^1 = M_0, \text{dataLabels}^1 = \text{dataLabels}, \text{digestLabels}^1 = \text{digestLabels} \right) \).

5. For \( \tau = 1 \) to \( T \) do the following:
   
   Define \( i \) s.t. \( \tau \in \left[ \sum_{j \in [i-1]} t_j + 1, \sum_{j \in [i]} t_j \right] \).
   
   \( (X, R/W, L) := \text{ReEval} \left( \tilde{C}_{\tau}^{\text{step}}, \text{Labels}^\tau \right) \).

   \begin{align*}
   \text{if } R/W = \text{read then} & \quad \text{Parse } X = \left( \text{stateLabels}^{\tau+1}, e_{\text{data}}, \text{digestLabels}^{\tau+1} \right). \\
   & \quad \text{dataLabels}^{\tau+1} := \text{Receive}^{\hat{D}_i} \left( \text{crs}, e_{\text{data}}, L \right).
   \end{align*}

   \begin{align*}
   \text{else} & \quad \text{Parse } X = \left( \text{stateLabels}^{\tau+1}, \text{dataLabels}^{\tau+1}, e_{\text{digest}}, \text{wData} \right). \\
   & \quad \text{digestLabels}^{\tau+1} := \text{ReceiveWrite}^{\hat{D}_i} \left( \text{crs}, L, \text{wData}, e_{\text{digest}} \right).
   \end{align*}

   \begin{align*}
   \text{if } \tau = \sum_{j \in [i]} t_j \text{ and } \tau < T \text{ then} & \quad \text{Parse } \tilde{C}_{\tau+1}^{\text{step}} = \left( \tilde{C}_{\tau+1}^{\text{step}}, \text{dataLabels}, \text{digestLabels} \right) \\
   & \quad \text{Set } \text{dataLabels}^{\tau+1} = \text{dataLabels} \text{ and } \text{digestLabels}^{\tau+1} = \text{digestLabels}. \\
   \end{align*}

   \begin{align*}
   \text{Labels}^{\tau+1} := \left( \text{stateLabels}^{\tau+1}, \text{dataLabels}^{\tau+1}, \text{digestLabels}^{\tau+1} \right).
   \end{align*}

6. Decode output \( y \) using \( \left( \text{stateLabels}^{T+1}, \text{SampleKeys}(0^*) \right) \).

\end{align*}

\begin{itemize}
\item **Fact 4.3.6.** During the homomorphic evaluation of client \( Q_i \), in Step 3a, Figure 4.4 the labels embedded in circuit \( C_{\tau}^{\text{step}} \) correspond to \( \text{RdKeys}^{\tau,i} \), where \( \phi_i = F_s(\text{LACONIC} \_ \text{OT} || \tau) \) and \( \omega_i = \{ F_s(\text{GC} \_ \star || \tau + 1) \}_{s \in \{\text{STATE, DATA, DIGEST}\}} \).
\end{itemize}

This is because the functionality of the \( C_{\tau}^{\text{PRF}} \) is preserved in randomization so far, Fact 4.3.5 and because the output keys of \( C_{\tau,i}^{\text{PRF}} \) and \( \text{RdKeys}^{\tau,i} \) are same when they are generated and are re-randomized using same randomness in Step 4b of Figure 4.4.

Recall that \( \text{ct}_n \) consists of garbled step-circuits of client \( Q_1 \) followed by \( Q_2 \) and so on.
We prove the following fact about garbled step circuits belonging to some client $Q_i$ in final ciphertext $ct_n$.

**Claim 4.3.7.** Consider circuits $\tilde{C}^\text{step}_\tau$ and $\tilde{C}^\text{step}_{\tau+1}$ such that both belong to program $P_i$ for some $i$. Since all the PRF circuits $\tilde{C}^\text{PRF}$ have been evaluated, the value nextKeys$^i$ in $\tilde{C}^\text{step}_\tau$ is well defined. Then, $\text{nextKeys}^i = \text{Keys}^\tau+1$ where $\text{Keys}^\tau+1$ corresponds to the input keys for $\tilde{C}^\text{step}_{\tau+1}$ in $ct_n$.

**Proof.** Initially, $Q_i$ picks $\text{Keys}^\tau+1$ as $\text{SampleKeys}(F_s(\text{GC} \_ \_ \_ || \tau + 1))$, where $\_ \_ \_ \in \{\text{STATE, DATA, DIGEST}\}$ and uses them in garbling of $\tilde{C}^\text{step}_{\tau+1}$ as well as as hardcoding inside $C^\text{step}_\tau$.

Then, $\tilde{C}^\text{step}_{\tau+1}$ is randomized by clients $Q_{i+1}, \ldots, Q_n$ such that the $\text{stateKeys, dataKeys, digest Keys}$ are randomized sequentially using $\omega_j = (F_s(\text{GC} \_ \_ \_ || \tau + 1), F_s(\text{GC} \_ \_ \_ || \tau + 1), F_s(\text{GC} \_ \_ \_ || \tau + 1))$. This is same as $\text{Transform}(\text{Keys}^\tau+1, \{\omega_j\}_{j>i})$ inside $\tilde{C}^\text{step}_\tau$. By Fact 4.3.6, $\omega_j$ is the value used for $\text{Transform}$ in $\tilde{C}^\text{step}_\tau$.

**Claim 4.3.8.** The above claim also holds for $\tilde{C}^\text{step}_\tau$ and $\tilde{C}^\text{step}_{\tau+1}$ when $\tau$ is the last circuit of a program for $Q_i$ and $\tau + 1$ is the first circuit for $Q_{i+1}$.

**Proof.** When the client $Q_i$ generates $\tilde{C}^\text{step}_\tau$, the keys hard-coded are $\text{SampleKeys}(0^\_ \_ \_)$. Then, this circuit is re-randomized by $Q_{i+1}$ resulting in keys $\text{SampleKeys}(F_s(\text{GC} \_ \_ \_ || \tau + 1))$ which same as the value used by $Q_{i+1}$ to generate the step-circuit $\tilde{C}^\text{step}_{\tau+1}$.

**Fact 4.3.9.** The first garbled step circuit $\tilde{C}_1^\text{step}$ gets evaluated on $(x, 0, \text{digest}_1)$.

This follows from correctness of OT and consistency of re-rerandomization of OT and garbled circuits similar to Fact 4.3.4.

Now, we will prove a lemma about the execution of circuits generated by client $Q_1$. Then, we will prove a claim about the inputs on which circuit of $Q_2$ is executed. Finally, the correctness of execution programs of all clients would follow in a similar manner.

**Lemma 4.3.10.** Consider the program $P_1$ and the database $D_1$ of the first client and the input $x$ of the server. Consider the execution $P_1^{D_1}(x)$ execution in the clear as $(\text{state}_{\tau}, r\text{Data}_{\tau})$ as the values on which $C^\text{step}$ is executed. Also, let $(\tilde{D}_1^\tau, \text{digest}_{\tau})$ denote the $\text{Hash(crs, } D_1^\tau)$, where $D_1^\tau$ is the database at beginning of step $\tau$. Then, while decryption, $C^\text{step}_\tau$ is executed on inputs $(\text{state}_{\tau}, r\text{Data}_{\tau}, \text{digest}_{\tau})$. Moreover, the encoded database held by the server before step $\tau$ is $D_1^\tau$.

**Proof.** We will prove this lemma by induction on $\tau$. The base case follows from Fact 4.3.9. Assume that the lemma holds for $\tau = \rho$, then we prove that the lemma holds for $\rho + 1$ as follows: So it holds that $\tilde{C}_\rho^\text{step}$ is executed on $(\text{state}_{\rho}, r\text{Data}_{\rho}, \text{digest}_{\rho})$. Moreover, $(\tilde{D}_1^\rho, \text{digest}_{\rho})$ denote the $\text{Hash(crs, } D_1^\rho)$, where $D_1^\rho$ correctly implements its code that includes one CPU step of $P_1$. Hence, $(\text{state}', r/W, L, w\text{Data}) = C^\text{CPU}_\rho(\text{state}_{\rho}, r\text{Data}_{\rho})$. Also, by Claim 4.3.7, nextKeys$^i$ in $\tilde{C}_\rho^\text{step}$ are correct input keys for $\tilde{C}_\rho^\text{step}_{\rho+1}$. There are following two cases:
• **R/W = read**: In this case, database and the digest are unchanged. New CPU state and digest are output correctly. Moreover, the labels for bit read from the memory will be learnt via **Receive** of updatable laconic OT. Correctness of these labels follows from correctness of read of laconic OT.

• **R/W = write**: Similar to above, in this case new state and data keys are correctly output. Moreover, the digest keys w.r.t. the new updated digest are output via laconic OT. The correctness of these labels follows from correctness of laconic OT write function **ReceiveWrite**. Finally, in this function, the encoded database is updated correctly.

**Lemma 4.3.11.** Let \( \tilde{C}_{\text{step}}^{\tau_1} \) be the last circuit of client \( Q_1 \) or program \( P_1 \). Then, during decryption, \( \tilde{C}_{\text{step}}^{\tau_1+1} \) is executed on \( (y_1, 0, \text{digest}_2) \), where \( y_1 = P_1^{D_1}(x) \) and \( \text{digest}_2 \) is the digest for \( D_2 \).

**Proof.** At the time of homomorphic evaluation, in Step 1b, Figure 4.4, labels \( \text{dataKeys}^{\tau_1+1} \) and \( \text{digestKeys}^{\tau_1+1} \) are embedded in \( \tilde{C}_{\text{step}}^{\tau_1+1} \). Also, by Claim 4.3.8, in the final ciphertext \( ct \), nextKeys\(^\dagger\) inside \( C_{\text{step}}^{\tau_1} \) are correct keys for \( \tilde{C}_{\text{step}}^{\tau_1+1} \). Hence, the lemma holds since \( \tilde{C}_{\text{step}}^{\tau_1} \) outputs stateKeys\(^\dagger\)\( \text{state'} \), where \( \text{state'} = y_1 \).

**Lemma 4.3.12.** Consider the program \( P_i \) and the database \( D_i \) of the client \( Q_i \) and the input \( x \) of the server. Consider the execution \( P_i^{D_i}(y_{i-1}) \) execution in the clear as \( (\text{state}_r, \text{rData}_r) \) as the values on which \( C_{\text{step}}^{\tau} \) is executed. Also, let \( (\hat{D}_i^\tau, \text{digest}_r) \) denote the \( \text{Hash}(\text{crs}, D_i^\tau) \), where \( D_i^\tau \) is the database at beginning of step \( \tau \). Then, while decryption, \( \tilde{C}_{\text{step}}^{\tau} \) is executed on inputs \( (\text{state}_r, \text{rData}_r, \text{digest}_r) \). Moreover, the encoded database held by the server before step \( \tau \) is \( \hat{D}_i^\tau \).

**Proof.** The lemma follows via induction on number of clients where the base case is proved in Lemma 4.3.10. The rest of the proof follows simply via induction similar to Lemma 4.3.10 where at the end of each program and beginning of a new program we prove Lemma 4.3.11. This proves both the properties 1 and 2.

### 4.3.4 Security Proof

Server privacy follows receiver privacy of oblivious transfer. For ease of exposition, we prove client UMA privacy for the setting of a single honest client \( Q_i \) for a single execution. At the end of this section we will show that the proof can be extended for the case of multiple honest clients and multiple executions as well.

In the following, we prove that there exists a PPT simulator \( \text{ihopSim} \) such that, for any set of databases \( \{D_j\}_{j \in [n]} \), any sequence of compatible programs \( P_1, \cdots, P_n \) running
time \(t_1, \ldots, t_n\) and input \(x\), the outputs of the following two experiments are computational indistinguishable:

**Real experiment**

- \((pk_j, sk_j) \leftarrow \text{KeyGen}(1^n)\) for \(\forall j \in [n]\).
- \(\tilde{D}_j = (\text{digest}_j, \tilde{D}_j) \leftarrow \text{EncData}(\text{crs}, D_j)\) for \(\forall j \in [n]\).
- \((ct_0, x, \text{secret}) \leftarrow \text{InpEnc}(\text{crs}, x)\).
- \(ct_j \leftarrow \text{Eval}(\text{crs}, j, \{pk_k\}_{k=j+1}, ct_{j-1}, sk_j, (P_j, t_j), \text{digest}_j)\) for \(\forall j \in [n]\).
- Output \(ct_0, \{\tilde{D}_j, ct_j\}_{j \in [n]}\).

**Simulated experiment**

- \((pk_j, sk_j) \leftarrow \text{ihopSim}(1^n, i)\).
- \((pk_j, sk_j) \leftarrow \text{KeyGen}(1^n; r_j)\) for \(\forall j \in [n] \setminus \{i\}\). Here, \(r_j\) are uniform random coins.
- \((ct_0, \{\tilde{D}_j, ct_j\}_{j \in [n]}\) \leftarrow \text{ihopSim}(\text{crs}, x, \{pk_j, sk_j, \tilde{D}_j, t_j\}_{j \in [n]}; \{P_j, r_j\}_{j \in [n] \setminus \{i\}}, \text{MemAccess}_i, y_i)\), where \(y_i = P_i^{D_i} \left( \cdots (P_1^{D_1}(x)) \cdots \right)\).
- Output \(ct_0, \{\tilde{D}_j, ct_j\}_{j \in [n]}\).

The above definition can be made semi-malicious by allowing the adversary to pick random coins \(r_j\) adversarially given the public key \(pk_i\) of honest client as follows: \(\{r_j\}_{j \in [n] \setminus \{i\}} \leftarrow \mathcal{A}(1^n, \text{crs}, pk_i)\) that will be used to define \((pk_j, sk_j)\) in Step 2. Our proof would also support this stronger setting as well.

**Construction of ihopSim:** We describe the two phases of \(\text{ihopSim}\). In the first phase, \(\text{ihopSim}\) generates the keys of honest client \(Q_i\) as \((pk_i, sk_i) \leftarrow \text{KeyGen}(1^n)\).

In the second phase, \(\text{ihopSim}\) is described in Figure 4.10. At a high level, \(\text{ihopSim}\) generates everything honestly except \(ct_i\). When generating \(ct_i\), it simulates the step circuits one by one from the last to the first using the output \(y_i\) and memory access \(\text{MemAccess}_i\). In particular, since \(\text{ihopSim}\) takes \(D_i\) and \(\text{MemAccess}_i\) as input, it can compute \(D_i\) and \(\text{digest}_i\) before every step circuit, and use that to compute the output of every step circuit. Security follows from security of re-randomization of SFE, namely re-randomized garbled circuits are indistinguishable from freshly generated ones and that freshly generated garbled circuits are indistinguishable from simulated ones.

Now we give a series of hybrids such that the first hybrid outputs \((ct_0, \{\tilde{D}_j, ct_j\}_{j \in [n]}\) in the real execution, and the last hybrid is the output of \(\text{ihopSim}\). Notice that the only difference between the real and ideal experiments is \(ct_i\), so all the hybrids generate everything in the same way except \(ct_i\).

- \(\tilde{H}_0\): Output in the real experiment.
\( \left( c_{t_0}, \{ \tilde{D}_j, ct_j \}_{j \in [n]} \right) \leftarrow \text{ihopSim} \left( \text{crs}, x, \{ \text{pk}_j, \text{sk}_j, D_j, t_j \}_{j \in [n]}, \{ P_j \}_{j \in [n] \setminus \{ i \}}, \text{MemAccess}_i, y_i \right) . \)

1. Compute \( \tilde{D}_j = (\text{digest}_j, \tilde{D}_j) \leftarrow \text{EncData}(\text{crs}, D_j) \) for \( \forall j \in [n] \).
   Compute \( (c_{t_0}, \text{x_secret}) \leftarrow \text{lnpEnc}(x) . \)
   Compute \( \text{ct}_j \leftarrow \text{Eval} \left( j, \{ \text{pk}_k \}_{k=j+1}^n, \text{ct}_{j-1}, \text{sk}_j, (P_j, t_j), \text{digest}_j \right) \) for every \( j \in [i - 1] \).
   Pick a random function \( F \) (in the following use random values for \( F(\cdot) \)).

2. Let \( T_j := \sum_{k \in [j]} t_k \). Generate \( \text{ct}_i \) as follows:

   a) Run the program \( P_{i-1} D_{\tau} \) to obtain \( (R/W^\tau, L^\tau, wData^\tau) \) for every CPU step \( \tau \in [T_i - 1] \). Obtain \( (R/W^\tau, L^\tau, wData^\tau) \) for \( \tau \in [T_i - 1, T_i] \) from \( \text{MemAccess}_i \).

   b) \( \left( \text{stateKeys}^{T, +1}, \text{dataKeys}^{T, +1}, \text{digestKeys}^{T, +1} \right) \leftarrow \text{SampleKeys}(0^*) \).
   Compute \( \text{stateLabels}^{T, +1} \) using \( y_i \); compute \( \text{dataLabels}^{T, +1}, \text{digestLabels}^{T, +1} \) using \( (D_i, \text{digest}_i, R/W, L, wData) \) of the last CPU step.

   c) For \( \tau = T_i \) downto 1 do the following:
      \( \langle R/W, L, wData \rangle := (R/W^\tau, L^\tau, wData^\tau) \).
      Define \( j \) s.t. \( \tau \in [T_{j-1} + 1, T_{j}] \).
      Let \( D \) be the database of \( Q_j \) before step \( \tau \).
      (stateLabels\(^{T, +1}\), dataLabels\(^{T, +1}\), digestLabels\(^{T, +1}\))
      \( \leftarrow \text{Transform} \left( \left( \text{stateLabels}^{T, +1}, \text{dataLabels}^{T, +1}, \text{digestLabels}^{T, +1} \right) \right) . \)
      \( \left\{ F_{s_{\tau, i}} \left( \text{GC}_\tau \| \tau + 1 \right) \right\}_{s \in \{ \text{STATE}, \text{DATA}, \text{DIGEST} \}} \left| \cdots \left| \left\{ F_{s_{\tau, n}} \left( \text{GC}_\tau \| \tau + 1 \right) \right\}_{s \in \{ \text{STATE}, \text{DATA}, \text{DIGEST} \}} \right. \).

      if \( R/W = \text{read} \) then
      \( e_{\text{data}} \leftarrow \text{cOTSim} \left( \text{crs}, D, L, \text{dataLabels}^{T, +1} \right) . \)
      \( X \leftarrow (\text{stateLabels}^{T, +1}, e_{\text{data}}, \text{digestLabels}^{T, +1}) . \)
      else
      \( e_{\text{digest}} \leftarrow \text{cOTSimWrite} \left( \text{crs}, D, L, wData, \text{digestLabels}^{T, +1} \right) \).
      \( X \leftarrow (\text{stateLabels}^{T, +1}, \text{dataLabels}^{T, +1}, e_{\text{digest}}, wData) \).

      \( \left( \left\{ \text{step}_{\tau, j} \right\}_{j=i+1}^n, \text{Labels}^\tau \right) \leftarrow \text{CircSim} \left( 1^\tau, U, (X, R/W, L) \right) \) such that the output labels of \( \text{C}_{\tau, j} \) are the same as input labels of \( \text{RdLabels}^{\tau, j} \) for \( \tilde{C}_{\tau, j} \).
      Parse \( \text{Labels}^\tau = (\text{stateLabels}^\tau, \text{dataLabels}^\tau, \text{digestLabels}^\tau, \{ \text{PLabels}^{\tau, j} \}_{j \in [i+1, n]} \).
      \( L_{\tau, j} \leftarrow \text{OT}_2 \left( \text{pk}_j, \{ \text{PLabels}^{\tau, j}, \text{PLabels}^{\tau, j} \} \right) \) for every \( j \in [i + 1, n] \).

      if \( \tau = T_{j-1} + 1 \) then
      Embed \( \text{stateLabels}^\tau \) and \( \text{digestLabels}^\tau \) in \( \tilde{C}_{\tau, j} . \)
      \( \left( \text{stateKeys}^\tau, \text{dataKeys}^\tau, \text{digestKeys}^\tau \right) \leftarrow \text{Transform} \left( \text{SampleKeys}(0^*) \right) , \)
      \( \left\{ F_{s_{\tau, i}} \left( \text{GC}_\tau \| \tau \right) \right\} \left| \cdots \left| \left\{ F_{s_{\tau, n}} \left( \text{GC}_\tau \| \tau \right) \right\} \right. \left\{ F(\text{GC}_\tau \| \tau) \right\}_{s \in \{ \text{STATE}, \text{DATA}, \text{DIGEST} \}} \).
      Compute \( \left( \text{dataLabels}^\tau, \text{digestLabels}^\tau \right) \) using \( (D_{j-1}, \text{digest}_j, R/W, L, wData) \) at step \( \tau - 1 . \)

   d) \( L_0 \leftarrow \text{OT}_2 \left( c_{t_0}, \{ \text{stateLabels}^3, \text{stateLabels}^3 \} \right) . \)

   e) \( \text{ct}_i := \left( L_0, \left\{ \tilde{C}_{\tau, j}^{\text{step}}, \{ \tilde{C}_{\tau, j}^{\text{PRF}} \}_{j=i+1}^n \right\}_{\tau \in [T_i]} \right) . \)

3. Compute \( \text{ct}_j \leftarrow \text{Eval} \left( j, \{ \text{pk}_k \}_{k=j+1}^n, \text{ct}_{j-1}, \text{sk}_j, (P_j, t_j), \text{digest}_j \right) \) for every \( j \in [i + 1, n] \).

4. Output \( \left( \tilde{D}_j, \text{ct}_j \right)_{j \in [n]} . \)

Figure 4.10: Simulator for multi-hop RAM.
1. Compute $\tilde{D}_j = (\text{digest}_j, \tilde{D}_j) \leftarrow \text{EncData}(\text{crs}, D_j)$ for all $j \in [n]$.

2. Compute $(c_{0,j}, x_{\text{secret}}) \leftarrow \text{lnpEnc}(x)$.

3. Compute $c_{t,j} \leftarrow \text{Eval} \left( j, \{pk_k\}_{k=j+1}^n, ct_{j-1}, sk_j, (P_j, t_j), \text{digest}_j \right)$ for every $j \in [i-1]$.

4. Output $(\tilde{D}_j, c_{t,j})$ for every $j \in [i+1, n]$.

2. Let $T_j := \sum_{k \in [j]} t_k$. Generate $c_t$ as follows:

   a) Compute $\{\tilde{C}_{\tau}^{\text{step}}, \{C_{\tau,j}^{\text{PRF}}, L_{\tau,j}\}_{j=i+1}^n\}_{\tau \in [m+1, T_i]}$ honestly as in Figure 4.4.

   b) Run the program $P_1 \mathcal{D}_1 (\cdots (P_1 \mathcal{D}_1(x)) \cdots)$ to obtain $(\text{state}^\tau, \text{R}/\text{W}^\tau, L^\tau, \text{wData}^\tau)$ for every $\tau \in [T_i]$.

   c) Define $j$ s.t. $m \in [T_{j-1} + 1, T_j]$.

   d) Set $\text{stateKeys}^{m+1}, \text{dataKeys}^{m+1}, \text{digestKeys}^{m+1}$ as $\text{SampleKeys}(F_{s_j}((\text{GC}_-)^* || m + 1))$ where $* \in \{\text{STATE}, \text{DATA}, \text{DIGEST}\}$, respectively.

   - If $m = T_j$, then set $\text{stateKeys}^{m+1}, \text{dataKeys}^{m+1}, \text{digestKeys}^{m+1}$ to $\text{SampleKeys}(0^*)$.

   - If $j < i$, then $(\text{stateKeys}^{m+1}, \text{dataKeys}^{m+1}, \text{digestKeys}^{m+1})$

     $\leftarrow \text{Transform}\left( (\text{stateKeys}^{m+1}, \text{dataKeys}^{m+1}, \text{digestKeys}^{m+1}) \right.$

     $\left. \{F_{s_{j+1}}((\text{GC}_-)^* || m + 1) \} \cdots \{F_{s_{i-1}}((\text{GC}_-)^* || m + 1) \} \right)$

     $\{F((\text{GC}_-)^* || m + 1)\}_{* \in \{\text{STATE}, \text{DATA}, \text{DIGEST}\}}$.

   e) Compute $(\text{stateLabels}^{m+1}, \text{dataLabels}^{m+1}, \text{digestLabels}^{m+1})$ using $(\text{state}^m, \text{R}/\text{W}^m, L^m, \text{wData}^m)$ and $(D_j, \text{digest}_j)$ at step $m$.

   f) For $\tau = m$ downto 1, do the following:

      Follow the same steps as in Figure 4.10 step 2c.

   g) $L_0 \leftarrow \text{OT}_2\left( c_{t,0}, (\text{stateLabels}^1, \text{stateLabels}^1) \right)$.

   h) $c_t := \left( L_0, \{\tilde{C}_{\tau}^{\text{step}}, \{C_{\tau,j}^{\text{PRF}}, L_{\tau,j}\}_{j=i+1}^n\}_{\tau \in [T_i]} \right)$.

3. Compute $c_{t,j} \leftarrow \text{Eval} \left( j, \{pk_k\}_{k=j+1}^n, ct_{j-1}, sk_j, (P_j, t_j), \text{digest}_j \right)$ for every $j \in [i + 1, n]$.

4. Output $(\tilde{D}_j, c_{t,j})_{j \in [n]}$.

Figure 4.11: Decryption of hybrid $H_m$

- $H_0$: In this hybrid, replace $F_{s_i}(\cdot)$ with a truly random function $F$. In particular, when computing $c_t$ as in Figure 4.4, in steps 1a and 4, use the values generated by $F$; in step 3a embed labels corresponding to the values from $F$. The indistinguishability of this hybrid with $H_0$ follows from the pseudo-randomness of $F_{s_i}(\cdot)$ and privacy of oblivious transfer ($s_i$ is hidden in $pk_i$).

- $H_m (m \in [T_i])$: Next we consider a sequence of hybrids $H_1, \cdots, H_{T_i}$. The description
(R/W, L, wData) := (R/W^\tau, L^\tau, wData^\tau).
Define j s.t. \tau \in [T_{j-1} + 1, T_j].
Let (D, digest) be the database and digest of Q_j before step \tau.
Let state' be the CPU state after step \tau.

\begin{align*}
\text{(stateKeys}^{\tau+1}, \text{dataKeys}^{\tau+1}, \text{digestKeys}^{\tau+1}) & \leftarrow \text{Transform}\left((\text{stateKeys}^{\tau+1}, \text{dataKeys}^{\tau+1}, \text{digestKeys}^{\tau+1}),
\{ F_{s_{i+1}}(\text{GC}^\ast \| \tau + 1) \}_{s \in \{\text{STATE,DATA,DIGEST}\}} \cdots \{ F_{s_n}(\text{GC}^\ast \| \tau + 1) \}_{s \in \{\text{STATE,DATA,DIGEST}\}} \right) .
\text{if R/W = \text{read then}}
\quad e_{\text{data}} \leftarrow \text{Send}(\text{crs}, \text{digest}, L, \text{dataKeys}^{\tau+1}; F(\text{PSI}\|\tau) \oplus \bigoplus_{j>i} F_{s_j}(\text{LACONIC}_\text{OT}\|\tau)).
\quad X \leftarrow (\text{stateKeys}^{\tau+1}\}_{\text{state}'}, e_{\text{data}}, \text{digestKeys}^{\tau+1}).
\text{else}
\quad e_{\text{digest}} \leftarrow \text{SendWrite}(\text{crs}, \text{.digest}, L, \text{wData}, \text{digestKeys}^{\tau+1}; F(\text{PSI}\|\tau) \oplus \bigoplus_{j>i} F_{s_j}(\text{LACONIC}_\text{OT}\|\tau)).
\quad X \leftarrow (\text{stateKeys}^{\tau+1}\}_{\text{state}'}, \text{dataKeys}_0^{\tau+1}, e_{\text{digest}}, \text{wData}).
\end{align*}

\begin{align*}
\left( \tilde{C}_\text{step}^{\tau}, \tilde{C}_\text{PRF}^{\tau}, \{ \text{Labels}^\tau \} \right) & \leftarrow \text{CircSim}(1^n, U, (X, R/W, L)) \text{ such that the output labels of } \tilde{C}_\text{step}^{\tau} \text{ are the same as input labels of RdLabs}^{\tau,j} \text{ for } \tilde{C}_\text{step}^{\tau}.
\text{Parse } \text{Labels}^\tau = (\text{stateLabels}^\tau, \text{dataLabels}^\tau, \text{digestLabels}^\tau, \{ \text{PLabels}^{\tau,j} \}_{j \in [i+1, n]}).
\text{L}_{\tau,j} \leftarrow \text{OT}_2(\text{pk}_j, (\text{PLabels}^{\tau,j}, \text{PLabels}^{\tau,j})) \text{ for every } j \in [i + 1, n].
\end{align*}

\text{if } \tau = T_{j-1} + 1 \text{ then }
\begin{align*}
\text{Embed } \text{stateLabels}^\tau \text{ and } \text{digestLabels}^\tau \text{ in } \tilde{C}_\text{step}^{\tau}.
\text{(stateKeys}^\tau, \text{dataKeys}^\tau, \text{digestKeys}^\tau) & \leftarrow \text{Transform}(\text{SampleKeys}(0^n)),
\{ F_{s_j}(\text{GC}^\ast \| \tau) \} \cdots \{ F_{s_{i+1}}(\text{GC}^\ast \| \tau) \} \cdots \{ F(\text{GC}^\ast \| \tau) \}_{s \in \{\text{STATE,DATA,DIGEST}\}} .
\text{Compute } (\text{dataLabels}^\tau, \text{digestLabels}^\tau) \text{ using } (D_{j-1}, \text{digest}_{j-1}; R/W, L, \text{wData}) \text{ at step } \tau - 1.
\end{align*}

Figure 4.12: Difference of H_m and \hat{H}_m.

of H_m is in Figure 4.11. Notice that c_t consists of T_i step circuits with corresponding PRF circuits. In hybrid H_m, the step circuits from 1 to m are simulated while the remaining step circuits (m + 1 to T_i) are generated honestly. Given all the programs and secret keys, the intermediate outputs as well as input/output labels of every step circuit can all be computed. Given the correct output of circuit C_m^{\text{step}}, the step circuits from 1 to m can be simulated one by one from the m-th to the first similarly as in ihopSim.

To show H_m is indistinguishable from H_{m-1}, first notice that they are the same except \left( \tilde{C}_m^{\tau}, \tilde{C}_m^{\tau,j}, \{ L_{m,j} \}_{j=1}^n \right) \text{ in } c_t. \text{ Consider an intermediate hybrid } \hat{H}_m \text{ which is the same as } H_m \text{ except that in step 2f when } \tau = m, \text{ follow the steps in Figure 4.12. In particular, when } \tau = m, \text{ } H_m \text{ computes the output of } C_m^{\text{step}} \text{ and uses that output to}
simulate $\tilde{C}_m^{\text{step}}$ by CircSim and $\text{OT}_2$. The output of $\tilde{C}_m^{\text{step}}$ is the same for $H_{m-1}$ and $\hat{H}_m$. The indistinguishability of $\hat{H}_m$ and $H_{m-1}$ follows from the security of garbled circuits directly when $m \in [T_{i-1} + 1, T_i]$. When $m \in [T_{i-1}]$, it follows from the security of garbled circuits and re-randomization. More precisely, the re-randomized garbled circuit is indistinguishable from a freshly generated garbled circuit, which is indistinguishable from a simulated one. Notice that the random coins used in re-randomization for $\tilde{C}_m^{\text{step}}$ is $F(GC_\star || m)$, which is not used anywhere else in $H_{m-1}$, so it can be treated as truly random coins.

To switch from $\hat{H}_m$ to $H_m$, we replace $X$ in Figure 4.12 with simulated $e_{\text{data}}$ and $e_{\text{digest}}$ for CircSim and $\text{OT}_2$. The indistinguishability follows from sender privacy of updatable laconic OT and that $\text{Send}$ and $\text{SendWrite}$ both take random coins $F(\text{PSI} || m)$.

- $\hat{H}_{T_i}$: Output in the simulated experiment. This hybrid is the same as $H_{T_i}$.

**Extension.** The above proof can be naturally extended to provide security for multiple clients and many executions. For example in the case of two clients $Q_{i_1}$ and $Q_{i_2}$, ihopSim first computes $\left( ct_0, \{ \tilde{D}_j \}_{j \in [n]}, \{ ct_j \}_{j \in [i_1-1]} \right)$ honestly, then computes $ct_{i_1}$ same as in Figure 4.10 step 2. It then computes $\{ ct_j \}_{j \in [i_1+1,i_2-1]}$ from $ct_{i_1}$ by $\text{Eval}$, and computes $ct_{i_2}$ same as in Figure 4.10 step 2. Finally it computes $\{ ct_j \}_{j \in [i_2+1,n]}$ from $ct_{i_2}$ by $\text{Eval}$. To show this is indistinguishable from the real execution, we consider the following hybrids:

- $H_0$: Output in the real experiment.
- $H_1$: First compute $\left( ct_0, \{ \tilde{D}_j \}_{j \in [n]}, \{ ct_j \}_{j \in [i_2-1]} \right)$ honestly, and then compute $ct_{i_2}$ same as in Figure 4.10 step 2. Finally it computes $\{ ct_j \}_{j \in [i_2+1,n]}$ from $ct_{i_2}$ honestly by $\text{Eval}$.
- $H_2$: Output in the simulated experiment.

The above hybrids are indistinguishable because an honestly generated $ct_{i_1}$ or $ct_{i_2}$ is indistinguishable from a simulated one, as we have shown in the single-client case.

To simulate multiple executions, ihopSim can simply repeat the procedure for every execution. We note that there is no connection between executions except the digest, so they can be simulated separately given the initial digests of every execution. The hybrids go from the real experiment to the simulated experiment by replacing all the honestly generated $ct$’s in one execution by simulated ones, one execution per hybrid.

\[ \text{Notice that different from Figure 4.10, the simulator here doesn’t take } P_i \text{ as input, but the simulation can still obtain } (R/W^\tau, L^\tau, w_{\text{Data}}^\tau) \text{ for every step } \tau \in [T_{i-1} + 1, T_i] \text{ given } D_{i_1} \text{ and } \text{MemAccess}_{i_1}, \text{ and that is enough for the simulation.} \]
4.3.5 Extending to Multiple Executions

Recall that for correctness we also proved that the after one execution, the resulting garbled database \( \tilde{D} = (D, \text{digest}) \) corresponds to the output of \( \text{Hash}(\text{crs}, D') \), where \( D' \) is the correct database resulting after the execution in the clear (See Property 2, Section 4.3.3).

Given this invariant after the first execution, the next execution happens identically as the first execution with minor differences. To run the algorithm \( \text{Eval} \), the clients need the updated digest of their respective databases. The updated digests of all the clients taking part in an execution would be sent by the server to the first client on that execution path, and would be passed along with each ciphertext. Also, to ensure that no PRF output is used twice, each PRF invocation would take the session identifier \( \text{sid} \) as an additional input. With these changes, the second execution is identical to the first execution and hence, its correctness follows in a straight-forward manner.

Also, this does not affect the UMA-security because the simulator of the ideal world is given the databases as well as memory access pattern of the honest clients as input.

Moreover, note that this generalizes to the scenario when different subset of clients take part in different executions. Only the digests of the relevant client are passed around by the server.\(^5\)

4.4 From UMA to Full Security

In this section we provide a fully secure multi-hop RAM scheme. We first review Oblivious RAM (ORAM), which was first introduced by Goldreich [Gol87; GO96] and Ostrovsky [Ost90; Ost92; GO96], in Section 4.4.1. We then use ORAM as a compiler to encode the memory and program into a special format that does not reveal the access pattern or data contents during an execution. We present the generic construction in Section 4.4.2 and its correctness and security proofs in Sections 4.4.3 and 4.4.4, respectively.

4.4.1 Oblivious RAM

Definition 4.4.1. An Oblivious RAM scheme consists of two procedures \((\text{OData}, \text{OProg})\) with syntax:

- \((D^*, s^*) \leftarrow \text{OData}(1^\kappa, D)\): Given a security parameter \( \kappa \) and memory \( D \in \{0, 1\}^M \) as input, \( \text{OData} \) outputs the encoded memory \( D^* \) and encoding key \( s^* \).

- \( P^* \leftarrow \text{OProg}(1^\kappa, 1^{\log M}, 1^t, P) \): Given a security parameter \( \kappa \), a memory size \( M \), and a program \( P \) that runs in time \( t \), \( \text{OProg} \) outputs an oblivious program \( P^* \) that can access \( D^* \) as RAM and takes two inputs \( x \) and \( s^* \).

\(^5\)It can also extended to the setting, when a client \( Q_i \) occurs multiple times in the chain of clients in an execution. To handle this setting, the digest of \( D_i \) is passed along all the programs between two instances of this client as additional state.
Efficiency. We require that the run-time of \(OData\) should be \(M \cdot \text{polylog}(M) \cdot \text{poly}(\kappa)\), and the run-time of \(OProg\) should be \(t \cdot \text{poly}(\kappa) \cdot \text{polylog}(M)\). Finally, the oblivious program \(P^*\) itself should run in time \(t' = t \cdot \text{poly}(\kappa) \cdot \text{polylog}(M)\). Both the new memory size \(M' = |D^*|\) and the running time \(t'\) should be efficiently computable from \(M, t, \) and \(\kappa\).

Correctness. Let \(P_1, \ldots, P_\ell\) be programs running in polynomial times \(t_1, \ldots, t_\ell\) on memory \(D\) of size \(M\). Let \(x_1, \ldots, x_\ell\) be the inputs and \(\kappa\) be a security parameter. Then we require that:
\[
\Pr[(P^*_1(x_1, s^*), \ldots, P^*_\ell(x_\ell, s^*))^{D^*} = (P_1(x_1), \ldots, P_\ell(x_\ell))^{D}] = 1
\]
where \((D^*, s^*) \leftarrow \text{OData}(1^\kappa, D), P^*_i \leftarrow \text{OProg}(1^\kappa, 1^{\kappa \log M}, 1^t, P_i)\) and \((P^*_1(x_1, s^*), \ldots, P^*_\ell(x_\ell, s^*))^{D^*}\) indicates running the ORAM programs on \(D^*\) sequentially.

Security. For security, we require that there exists a PPT simulator \(\text{Sim}\) such that for any sequence of programs \(P_1, \ldots, P_\ell\), initial memory data \(D \in \{0, 1\}^M\), and inputs \(x_1, \ldots, x_\ell\) we have that:
\[
(D^*, \text{MemAccess}) \approx \text{Sim}(1^\kappa, 1^M, \{1_i, y_i\}_{i=1}^\ell)
\]
where \((y_1, \ldots, y_\ell) = (P_1(x_1), \ldots, P_\ell(x_\ell))^D\), \((D^*, s^*) \leftarrow \text{OData}(1^\kappa, D)\), and \(\text{MemAccess}\) corresponds to the access pattern of the CPU-step circuits during the sequential execution of the oblivious programs \((P^*_1(x_1, s^*), \ldots, P^*_\ell(x_\ell, s^*))^{D^*}\).

4.4.2 Generic Construction

We prove the following theorem.

Theorem 4.4.2. Assume there exists a UMA-secure multi-hop RAM scheme and an ORAM scheme. Then there exists a fully secure multi-hop RAM scheme. Moreover, we give a black-box construction of one given a UMA-secure multi-hop RAM and ORAM scheme.

Proof. We first give the construction of the scheme itself and then provide a construction of an appropriate simulator to prove security. Let \((\text{Setup}, \text{KeyGen}, \text{EncData}, \text{InpEnc}, \text{Eval}, \text{Dec})\) be a UMA-secure multi-hop RAM scheme and let \((\text{OData}, \text{OProg})\) be an ORAM scheme. We construct a new multi-hop RAM scheme \((\tilde{\text{Setup}}, \tilde{\text{KeyGen}}, \tilde{\text{EncData}}, \tilde{\text{InpEnc}}, \tilde{\text{Eval}}, \tilde{\text{Dec}})\) as follows:

- \(\tilde{\text{Setup}}(1^\kappa)\): Generate \(\text{crs}\) same as \(\text{Setup}\).
- \(\tilde{\text{KeyGen}}(1^\kappa)\): Generate \((\text{pk}, \text{sk})\) same as \(\text{KeyGen}\).
- \(\tilde{\text{EncData}}(\text{crs}, D)\): Execute \((D^*, s^*) \leftarrow \text{OData}(1^\kappa, D)\) followed by \(\tilde{D} \leftarrow \text{EncData}(1^\kappa, D^*)\).
- \(\tilde{\text{InpEnc}}(x)\): Execute \((\text{ct}, x, \text{secret}) \leftarrow \text{InpEnc}(x)\).
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For Multiple Executions. To prove correctness in multiple executions, we need to show that
\[
\Pr\left[\text{Dec}^{\tilde{D}_1, \ldots, \tilde{D}_n}(\text{x_secret}, ct_n) = P_n^{D_n} \left(\cdots (P_1^{D_1}(x)) \cdots \right)\right] = 1,
\]
where \(\tilde{D}_i \leftarrow \text{EncData}(1^\kappa, D_i)\), \((ct_0, \text{x_secret}) \leftarrow \text{InpEnc}(x), ct_i \leftarrow \text{Eval}(i, \{pk_j\}_{j=i+1}^n, ct_{i-1}, sk, P_i, t_i, \text{digest}_i)\).

By definition, \(\text{Dec}^{\tilde{D}_1, \ldots, \tilde{D}_n}(\text{x_secret}, ct_n) = \text{Dec}^{\tilde{D}_1, \ldots, \tilde{D}_n}(\text{x_secret}, ct_n)\). By the correctness of the UMA-secure multi-hop RAM scheme, we have that \(\text{Dec}^{\tilde{D}_1, \ldots, \tilde{D}_n}(\text{x_secret}, ct_n) = P_n^{s_n^{D_n}} \left(\cdots \left( P_n^{s_n^{D_n}}(x) \right) \cdots \right)\). Finally, by the correctness of the ORAM scheme, it holds that \(P_n^{s_n^{D_n}} \left(\cdots (P_1^{s_1^{D_1}}(x)) \cdots \right) = P_n^{D_n} \left(\cdots (P_1^{D_1}(x)) \cdots \right)\).

For Multiple Executions. To prove correctness in multiple executions, we need to show that
\[
\Pr\left[\text{Dec}^{\tilde{D}_1^{\text{sid}}, \ldots, \tilde{D}_n^{\text{sid}}}(\text{x_secret}^{\text{sid}}, ct_n^{\text{sid}}) = P_n^{(\text{sid})^{D_n^{\text{sid}}}} \left(\cdots (P_1^{(\text{sid})^{D_1^{\text{sid}}}}(x^{\text{sid}})) \cdots \right)\right] = 1,
\]
where \(\tilde{D}_i^{\text{sid}}\) is the resulting garbled database after executing sid-1 homomorphic evaluations, \((ct_0^{\text{sid}}, \text{x_secret}^{\text{sid}}) \leftarrow \text{InpEnc}(x^{\text{sid}}), ct_i^{\text{sid}} \leftarrow \text{Eval}(i, \{pk_j\}_{j=i+1}^n, ct_{i-1}^{\text{sid}}, sk, P_i^{\text{sid}}, t_i^{\text{sid}}, \text{digest}_i^{\text{sid}})\).

Recall that for correctness of the UMA-secure multi-hop RAM scheme we proved that the after every execution, the resulting garbled database \(\tilde{D}_i^{\text{sid}}\) corresponds to the output of \(\text{Hash}(\text{crs}, D_i^{\text{sid}})\), where \(D_i^{\text{sid}}\) is the correct \(D_i^*\) resulting after previous sid - 1 executions in the clear (see Property 2, Section 4.3.3). By correctness of ORAM and the underlying UMA-secure multi-hop RAM scheme, we conclude the correctness in multiple executions.

4.4.4 Security

For a Single Client in a Single Execution. Server privacy is follows by receiver privacy of oblivious transfer. Now we prove client privacy for a single honest client \(Q_i\) in a single
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execution. More precisely, we prove that there exists a PPT simulator \( \text{ihopSim} \) such that, for any set of databases \( \{ D_j \}_{j \in [n]} \), any sequence of compatible programs \( P_1, \cdots, P_n \) running time \( t_1, \cdots, t_n \) and input \( x \), the outputs of the following two experiments are computational indistinguishable:

Real experiment

\( (pk_j, sk_j) \leftarrow \text{KeyGen}(1^\kappa) \) for \( \forall j \in [n] \).

\( \tilde{D}_j = (\text{digest}_j, \tilde{D}_j) \leftarrow \text{EncData}(\text{crs}, D_j) \) for \( \forall j \in [n] \).

\( (ct_0, x_{\text{secret}}) \leftarrow \text{InpEnc}(x) \).

\( ct_j \leftarrow \text{Eval}(j, \{ pk_k \}_{k=j+1}^n, ct_{j-1}, sk_j, (P_j, t_j), \text{digest}_j) \) for \( \forall j \in [n] \).

Output \( ct_0, \{ \tilde{D}_j, ct_j \}_{j \in [n]} \).

Simulated experiment

\( (pk_i, sk_i) \leftarrow \text{ihopSim}(1^\kappa, i) \).

\( (pk_j, sk_j) \leftarrow \text{KeyGen}(1^\kappa; r_j) \) for \( \forall j \in [n] \setminus \{i\} \). Here, \( r_j \) are uniform random coins.

\( (ct_0, \{ \tilde{D}_j, ct_j \}_{j \in [n]} ) \leftarrow \text{ihopSim}(\text{crs}, x, \{ pk_j, sk_j, t_j \}_{j \in [n]}, \{ D_j, P_j, r_j \}_{j \in [n] \setminus \{i\}}, 1^{\kappa_i}, y_i) \), where \( y_i = P_1^{D_i} (\cdots (P_i^{D_i}(x)) \cdots) \).

Output \( ct_0, \{ \tilde{D}_j, ct_j \}_{j \in [n]} \).

We let \( \text{OSim} \) be the ORAM simulator, and \( \text{USim} \) be the simulator for the UMA-secure multi-hop RAM scheme. We describe the two phases of \( \text{ihopSim} \). In the first phase, \( \text{ihopSim} \) generates the keys of honest client \( Q_i \) as \( (pk_i, sk_i) \leftarrow \text{KeyGen}(1^\kappa) \). In the second phase, \( \text{ihopSim} \) proceeds as follows.

1. Compute \( (D_i^*, \text{MemAccess}_i) \leftarrow \text{OSim}(1^\kappa, 1^{\kappa_i}, t_i, y_i) \).

2. Compute \( (D_j^*, s_j^*) \) from \( \text{EncData} \) and \( P_j^* \) from \( \text{Eval} \) for every \( j \in [n] \setminus \{i\} \).

3. Compute \( (ct_0, \{ \tilde{D}_j, ct_j \}_{j \in [n]} ) \leftarrow \text{USim}(\text{crs}, x, \{ pk_j, sk_j, t_j^* \}_{j \in [n]}, \{ D_j^*, P_j^*[s_j^*], r_j \}_{j \in [n] \setminus \{i\}}, D_i^*, \text{MemAccess}_i, y_i) \).

4. Output \( (ct_0, \{ \tilde{D}_j, ct_j \}_{j \in [n]} ) \).

We now prove the output of the simulator is computationally indistinguishable from the real distribution.

\( H_0: \) Output of the real experiment.
• H₁: Compute \((D^*_j, s^*_j)\) from \(\text{EncData}\) and \(P^*_j\) from \(\widehat{\text{Eval}}\) for every \(j \in [n] \setminus \{i\}\). Use the honestly generated \((D^*_i, s^*_i)\) from \(\text{EncData}\) and \(P^*_i\) from \(\widehat{\text{Eval}}\) to execute the program \(P^*_i[s^*_i]D^*_i(\cdots (P^*_1[s^*_1]D^*_1(x)) \cdots)\) and obtain \(y_i\) and a sequence of memory accesses \(\text{MemAccess}_i\). Then run \((c\text{t}_0, \{\widetilde{D}_j, c\text{t}_j\}_{j \in [n]} \leftarrow \text{USim}(\text{crs}, x, \{pk_j, sk_j, t^*_j\}_{j \in [n]}, \{D^*_j, P^*_j[s^*_j], r_j\}_{j \in [n] \setminus \{i\}}, D^*_i, \text{MemAccess}_i, y_i)\) and output. Since \((D^*_i, \text{MemAccess}_i)\) is the same as the real execution, the indistinguishability of this hybrid and \(H_0\) follows from UMA-security of the underlying multi-hop RAM scheme.

• H₂: Output of the simulated experiment. The only thing that differs in \(H_1\) and \(H_2\) is how we generate \(D^*_i\) and \(\text{MemAccess}_i\). In \(H_1\) they are generated honestly and in \(H_2\) they are generated by \(\text{OSim}\). \(H_1 \approx H_2\) follows from the security of ORAM.

For Multiple Clients in Multiple Executions. Similar as in the proof of UMA security, the above proof can be naturally extended to provide security for multiple clients and many executions. For example in the case of two clients \(Q_{i_1}\) and \(Q_{i_2}\), \text{ihopSim} first computes \((c\text{t}_0, \{\widetilde{D}_j\}_{j \in [n]}, \{c\text{t}_j\}_{j \in [i_1-1]})\) honestly, then simulates \(c\text{t}_{i_1}\) same as above as if there were only one honest client \(Q_{i_1}\). It then computes \(\{c\text{t}_j\}_{j \in [i_1+1,i_2-1]}\) from \(c\text{t}_{i_1}\) by \(\widehat{\text{Eval}}\), and simulates \(c\text{t}_{i_2}\) same as above as if there were only one honest client \(Q_{i_2}\). Notice that when simulating \(c\text{t}_{i_2}\), similar as in the UMA-secure scenario, \text{ihopSim} cannot generate \((D^*_i, s^*_i, P^*_i)\) honestly. Instead it will use the simulated \((D^*_{i_1}, \text{MemAccess}_{i_1})\) generated from \(\text{OSim}\), and that is enough for the simulation. Finally it computes \(\{c\text{t}_j\}_{j \in [i_2+1,n]}\) from \(c\text{t}_{i_2}\) by \(\widehat{\text{Eval}}\). To show this is indistinguishable from the real execution, we consider the following hybrids:

• H₆: Output in the real experiment.

• H₁: First compute \((c\text{t}_0, \{\widetilde{D}_j\}_{j \in [n]}, \{c\text{t}_j\}_{j \in [i_2-1]})\) honestly, and then compute \(c\text{t}_{i_2}\) same as above as if there were only one honest client \(Q_{i_2}\). Finally it computes \(\{c\text{t}_j\}_{j \in [i_2+1,n]}\) from \(c\text{t}_{i_2}\) honestly by \(\widehat{\text{Eval}}\).

• H₂: Output in the simulated experiment.

The above hybrids are indistinguishable because an honestly generated \(c\text{t}_{i_1}\) or \(c\text{t}_{i_2}\) is indistinguishable from a simulated one, as we have shown in the single-client case.

To simulate multiple executions, \text{ihopSim} should first use \(\text{OSim}\) to simulate \((D^*_i, \text{MemAccess}_i)\) for every honest client \(Q_i\) in all executions, and then repeat the above procedure for every execution. In the hybrids, we start from the real execution and first replace the honestly generated \(c\text{t}\)'s by simulated ones while using honestly generated \((D^*_i, \text{MemAccess}_i)\), and this step follows from the UMA-security of the underlying multi-hop RAM scheme. Afterwards we replace the honestly generated \((D^*_i, \text{MemAccess}_i)\) by the output of \(\text{OSim}\), and this step follows from the security of ORAM supporting multiple executions.
Chapter 5

Password-Based Threshold Authentication

Token-based authentication is commonly used to enable a single-sign-on experience on the web, in mobile applications and on enterprise networks using a wide range of open standards and network authentication protocols: clients sign on to an identity provider using their username/password to obtain a cryptographic token generated with a master secret key, and store the token for future accesses to various services and applications. The authentication server(s) are single point of failures that if breached, enable attackers to forge arbitrary tokens or mount offline dictionary attacks to recover client credentials.

In this chapter, we introduce and formalize the notion of password-based threshold token-based authentication which distributes the role of an identity provider among $n$ servers. Any $t$ servers can collectively verify passwords and generate tokens, while no $t-1$ servers can forge a valid token or mount offline dictionary attacks. This serves as a special functionality of secure computation.

For this specific application of secure computation, we introduce PASTA, a general framework that can be instantiated using any threshold token generation scheme, wherein clients can “sign-on” using a 2-round (optimal) protocol that meets our strong notions of unforgeability and password-safety.

We instantiate and implement our framework in C++ using two threshold message authentication codes (MAC) and two threshold digital signatures with different trade-offs. Our experiments show that the overhead of protecting secrets and credentials against breaches in PASTA, i.e. compared to a naïve single server solution, is extremely low (1-5%) in the most likely setting where client and servers communicate over the internet. The overhead is higher in case of MAC-based tokens over a LAN (though still only a few milliseconds) due to public-key operations in PASTA. We show, however, that this cost is inherent by proving a symmetric-key only solution impossible.

Organization. We first give a technical overview in Section 5.1. Then we introduce necessary background in Section 5.2. An important building block, threshold oblivious pseudo-
random function (TOPRF) is presented in Section 5.3. We formalize password-based threshold token-based authentication and present our PASTA framework in Section 5.4 and show experimental results in Section 5.5. Finally we prove necessity of using public-key operations in Section 5.6.

5.1 Technical Overview

We start with a plain password-based token generation protocol that is insecure against server breaches. As briefly mentioned in Section 1.2, the plain protocol works as follows. In the registration phase, a client registers with its username/password by storing its username and hashed password \( h = \text{Hash(password)} \) on the identity server. In the sign-on phase, client sends its username and hashed password \( h' \) to the server; server checks if \( h' = h \) for the username. If the check passes, server then uses a master secret key \( msk \) to compute a token \( \text{auth}_{msk}(x) \) and sends it to client, where \( \text{auth} \) is either a MAC or a digital signature and \( x \) is the data to be signed. In this solution, both the master secret key \( msk \) and the hashed password \( h \) are compromised if the server is breached. Hence clients’ passwords could be recovered using offline dictionary attacks.

Threshold Solution. A natural approach for protecting the master secret key \( msk \) is to combine the above plain solution with a threshold token generation (TTG) scheme (i.e. a threshold MAC or threshold signature). TTG schemes enable us to secret share \( msk \) among \( n \) servers such that any \( t \) servers can jointly generate valid tokens while any subset of up to \( t - 1 \) servers cannot forge valid tokens or recover \( msk \). To combine with the plain solution, the client registers to every server by sending its username and hashed password \( h \) in the registration phase. Then in the sign-on phase, client sends to \( t \) servers its username and hashed password \( h' \). Every server checks if the \( h = h' \) for the username, and performs its portion of the TTG scheme if the check passes. This solution guarantees the security of \( msk \) when at most \( t - 1 \) servers are breached, but clients’ passwords are still vulnerable against offline dictionary attacks even if a single server is breached.

Changing Secret Information Stored on Servers. The above two naïve solutions follow the same paradigm: server issues a token or executes the TTG scheme only if client is using the correct password. In order to check if client is using the correct password in the sign-on phase, server needs to store some “secret information” about client’s password in the registration phase. In the above solutions, this secret information is the hashed password. A fundamental problem with this is that the secret information can be computed given only the password, hence enabling offline dictionary attacks on the password. To resolve this issue, we make the stored secret information also depend on a server-side secret.

This can be achieved by a threshold oblivious pseudorandom function (TOPRF) [FIPR05]. In a TOPRF protocol, a secret key \( k \) for a pseudorandom function \( F \) is initially shared among \( n \) servers. A client can obtain a PRF value of its password \( h = F_k(\text{password}) \) by interacting
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with \( t \) servers, without revealing any information about its password to servers. Moreover, the function \( F_k(\cdot) \) is computable by any \( t \) servers, but cannot be computed by up to \( t - 1 \) servers. To this end, the PRF value \( h = F_k(\text{password}) \) serves as our new secret information stored on servers, and the protocol is now secure against offline dictionary attacks.

**From Four Rounds to Two Rounds.** A TOPRF protocol requires at least two rounds. Hence the sign-on phase in the above protocol requires at least four rounds: client and servers run the TOPRF protocol which requires two rounds for the client to obtain \( h \); client then sends \( h \) back to servers as a third-round message; servers verify and respond with token shares of the TTG scheme as fourth-round messages. We would like to reduce the interaction to two rounds because network latency is a major bottleneck in the protocol especially over WAN networks (see Section 5.5.2 for details).

On the one hand, in order to prevent offline dictionary attack, we require that the “secret information” be computed jointly by client and servers, which requires at least two rounds. On the other hand, servers must ensure that generation of token is only performed after the secret information is checked, which also requires two rounds, so it seems that four rounds is necessary to achieve our goal.

We resolve this deadlock by observing that the check does not have to be done on the server side. Instead of checking the secret information and then participating in the TTG scheme to generate token shares, the servers generate token shares directly and encrypt them under the secret information \( h \) using a symmetric-key encryption scheme. The ciphertexts are sent along with the second-round message of the TOPRF protocol. Now the protocol only has two rounds, and the check is done on the client side: only if the client has used the correct password in the first round of TOPRF can it calculate the correct \( h \) and decrypt the ciphertexts to obtain \( t \) token shares, and combine them to recover the final token.

**Mitigating Client Impersonation Attacks.** There is still a subtle security problem. Consider an attacker who compromises a single server and retrieves the secret information \( h \) of a client, and then impersonates the client to which \( h \) belongs without knowing its password by participating in a sign-on protocol with the servers. The servers generate token shares, encrypt them under \( h \), and send back to the attacker. Since the attacker already knows \( h \), it can decrypt all the ciphertexts and combine the token shares to obtain a valid token without ever knowing the client’s password or the master secret key. This issue occurs because when reducing the round complexity from four to two, we make the servers generate token shares without checking the secret information, but encrypt them using the secret information.

We address this issue by further modifying the secret information stored on servers. A client who computes \( h \) in the registration phase only sends \( h_i = \text{Hash'}(h, i) \) to server \( i \) where \( \text{Hash'} \) is assumed to be a random oracle. In other words, \( h \) is never revealed to or stored by any server, and each server only learns its corresponding \( h_i \). Later in the sign-on phase, token shares are encrypted under the \( h_i \)s. The client impersonation attack no longer works
since compromising certain servers only reveals the $h_i$s of these servers to the attacker, while the remaining $h_i$s are still kept secret.

**Multi-Client Security.** In our final protocol, we require that the only allowed attack is to impersonate a certain client and try different passwords by participating in an online sign-on protocol. This type of online attack is easy to detect in practice (e.g. if the same client is trying to sign-on very frequently within a short period of time). But enforcing the same across a large set of clients is not possible. Hence an important security requirement is that attacking one client should not help in attacking any other client.

This is not true for the protocol we have described so far. Consider an attacker who does not compromise any server, and performs the above online attack on one client\(^1\), trying all possible passwords. As a result, the attacker would obtain all the PRF values $h = \text{PRF}_k$\((\text{password})\) for all possible passwords. Then the attacker impersonates another client by participating in a single sign-on protocol with the servers. Since the attacker already knows all possible PRF values, it could try decrypting the ciphertexts sent from servers using the collected dictionary of PRF values (offline) to find the correct value and hence recover the password. In other words, he can leverage his online attack against one client to perform offline attacks (after a single online interaction) on many other clients. Note that including client username as part of the input to the PRF does not solve the problem either since servers have no way of checking what username the attacker incorporates in the TOPRF protocol without adding expensive zero-knowledge proofs to this effect to the construction.

One natural idea is to have a distinct TOPRF key for every client, so that PRF values learned from one client would be useless for any other client. This means that servers need to generate a sufficiently large number of TOPRF keys in the global setup phase, which is not practical. There is a simple and efficient fix: we let every client generate its own TOPRF key and secret share it between servers in the registration phase. This yields our final protocol which we formally prove to meet all our security requirements under the gap TOMDH assumption [JKKX17] in the random oracle model.

**Related Work.** Password-based techniques are the most common methods for authenticating users. However, the traditional approach of storing hashed passwords on the servers is susceptible to offline dictionary attacks [WW15; Dan]. Standard remedies such as salting or more advanced remedies such as memory-hard functions [Scrypt; ACK+16; DKAN; BD16; ACP+17; BZ17], pursued in the recent password-hashing competition [PHC], surely make the task of the attacker harder, but do not resolve the fundamental issue of trusting a single server.

A large body of work considers distributed token generation through threshold digital signatures [DF90; DDFY94; GHKR08; DK01; Bol03; AMN01; GJKR96; GGN16; Sho00;\(^1\)A smarter attacker would distribute its online attack across many clients to avoid detection. We use the single client in this example just to highlight the underlying multi-client security issue.]
BS01] and threshold message authentication codes [BLMR13; NPR99; MPS+03] which can protect the master key against $t - 1$ breached servers. A separate line of work on threshold password-authenticated key exchange (T-PAKE) [MSJ02; DG03; AFP05; ACFP05; CLN15; KMTG05] aims to prevent offline dictionary attacks in standard password-authenticated key exchange (PAKE) [BPR00; BMP00; KOY01; GK10; KV11; CHK+05; PS10; KOY03] by employing multiple servers.

While PAKE and T-PAKE solve the problem of establishing a secret key between a server and a client, where the client authenticates with a password, they do not solve the problem posed in this paper of distributing trust in password-based token generation. Specifically, PbTA generates tokens and provides token unforgeability, which T-PAKE does not deal with. Moreover, PbTA works in a setting where multiple clients share the same token generation set-up, and guarantees that attacks on one client do not affect the security of others. Finally, PbTA has a per-client registration phase that further differentiates it from PAKE and T-PAKE.

It is also worth noting that a straightforward composition of a T-PAKE followed by a threshold signature/MAC meets neither the efficiency nor the security requirements for PbTA. For efficiency, recall that we require minimal interaction where servers need not communicate with each other after a one-time setup procedure, and both the password verification and the token generation can be performed simultaneously in two rounds. The most efficient T-PAKE schemes require at least three rounds of interaction between the client and servers and additional communication among the servers (which could further increase when combined with threshold token generation). For security, it is unclear how to make such a composition meet our strong unforgeability and password-safety properties which we elaborate on shortly.

Another line of work focuses on constructing password-based server-aided signatures [CLNS16; XS03; Gan95; GT11; MR01]. However, they assume that apart from the password, a client also needs to use a secret state (e.g. a shared secret key) to generate a signature. In contrast, we focus on a solution in which a client only needs to use a password to generate a signature (more generally, a token).

Password-protected secret sharing (PPSS) [BJSL11; JKKX17; ACNP16; JKK14; YHCL15; CLLN14; CLN12; CEN15; JKKX16] considers the related problem of sharing a secret among multiple servers where $t$ servers can reconstruct the secret if client’s password verifies. This line of work does not meet our goal of keeping the master secret distributed at all times for use in a threshold token generation scheme. Moreover, PPSS is commonly studied in a single client setting where each client has its own unique secret. As we will see shortly, the multi-client setting and the common master-key used for all clients introduces additional technical challenges.

A very recent work of Harchol et al. [HAP18] implements and uses similar building blocks to ours, i.e. a threshold oblivious PRF [JKKX17] and a proactive variant of threshold RSA signature scheme [Sho00]. But it uses them for the different end goal of distributing server secret keys and protecting client secret keys with a password in SSH implementations. As such, it neither formalizes nor addresses the security/efficiency requirements of a password-
5.2 Background

We use $\kappa$ to denote the security parameter. Let $\mathbb{Z}$ denote the set of all integers and $\mathbb{Z}_n$ the set $\{0, 1, 2, \ldots, n-1\}$. $\mathbb{Z}_n^*$ is defined as $\mathbb{Z}_n^* := \{ x \in \mathbb{Z}_n | \gcd(x, n) = 1 \}$. We use $[a, b]$ for $a, b \in \mathbb{Z}$, $a \leq b$, to denote the set $\{a, a+1, \ldots, b\}$. $[b]$ denotes the set $\{1, b\}$. $\mathbb{N}$ denotes the set of natural numbers.

We use $x \leftarrow S$ to denote that $x$ is sampled uniformly at random from a set $S$. We use PPT as a shorthand for probabilistic polynomial time and negl to denote negligible functions.

We use $J^aK^b$ as a shorthand for $(a, a_1, \ldots, a_n)$ where $a_1, \ldots, a_n$ are shares of $a$. A concrete scheme will specify how the shares will be generated. The value of $n$ will be clear from context.

We use a 'require' statement in the description of an oracle to enforce some checks on the inputs. If any of the checks fail, the oracle outputs $\perp$.

In a security game, we use $\langle O \rangle$ to denote the collection of all the oracles defined in the game. For e.g., if a game defines oracles $O_1, \ldots, O_t$, then for an adversary $A$, $A^{\langle O \rangle}$ denotes that $A$ has access to the collection $\langle O \rangle := (O_1, \ldots, O_t)$.

**Shamir’s Secret Sharing.** Shamir’s secret sharing is a simple way to generate shares of a secret so that a threshold of the shares are sufficient to reconstruct the secret, while any smaller number hides it completely. We consider a slightly more general form of Shamir’s sharing here. Let GenShare be an algorithm that takes inputs $p, n, t$, $\{(i, \alpha_i)\}_{i \in S}$ s.t. $t \leq n < p$, $p$ is prime, $S \subseteq [0, n]$ and $|S| < t$. It picks a random polynomial $f$ of degree at most $t - 1$ over $\mathbb{Z}_p$ s.t. $f(i) = \alpha_i$ for all $i \in S$, and outputs $f(0), f(1), \ldots, f(n)$.

To generate a $(t, n)$-Shamir sharing of a secret $s \in \mathbb{Z}_p$, GenShare is given $p, n, t$ and $(0, s)$ as inputs to produce shares $s_0, s_1, \ldots, s_n$. Using the shorthand defined above, one can write the output compactly as $[s]$. Given any $t$ or more of the shares, say $\{s_j\}_{j \in T}$ for $|T| \geq t$, one can efficiently find coefficients $\{\lambda_j\}_{j \in T}$ such that $s = f(0) = \sum_{j \in T} \lambda_j \cdot s_j$. However, knowledge of up to $t-1$ shares reveals no information about $s$ if it is chosen at random from $\mathbb{Z}_p$.

**Cyclic Group Generator.** Let GroupGen be a PPT algorithm that on input $1^\kappa$ outputs $(p, g, \mathbb{G})$ where $p = \Theta(\kappa)$, $p$ is prime, $\mathbb{G}$ is a group of order $p$, and $g$ is a generator of $\mathbb{G}$. We will use multiplication to denote the group operation.

5.2.1 Hardness Assumption

Threshold oblivious PRF (TOPRF) was introduced by Jarecki et al. [JJKX17] in a recent work. They propose a simple TOPRF protocol called 2HashTDH and prove that it is UC-secure under the Gap Threshold One-More Diffie-Hellman (Gap-TOMDH) assumption in based token generation scheme.
the random oracle model. They also show that Gap-TOMDH is hard in the generic group model.

Rather than modeling TOPRF as a functionality in the UC-sense, we will explicitly formalize two natural properties for it, obliviousness and unpredictability, in Section 5.3. We will show that Jarecki et al.’s 2HashTDH protocol satisfies these properties under the same assumption. Here, we formally state the assumption.

For \( q_1, \ldots, q_n \in \mathbb{N} \) and \( t', t \in \mathbb{N} \) where \( t' < t \leq n \), define \( \text{MAX}_{t',t}(q_1, \ldots, q_n) \) to be the largest value of \( \ell \) such that there exists binary vectors \( u_1, \ldots, u_\ell \in \{0,1\}^n \) such that each \( u_i \) has \( t - t' \) number of 1’s in it and \( (q_1, \ldots, q_n) \geq \sum_{i \in [\ell]} u_i \). (All operations on vectors are component-wise integer operations.) Looking ahead, \( t \) and \( t' \) will be the parameters in the security definition of TOPRF and PbTA (\( t \) will be the threshold and \( t' \) the number of corrupted parties).

**Definition 5.2.1** (Gap-TOMDH). A cyclic group generator \( \text{GroupGen} \) satisfies the Gap Threshold One-More Diffie-Hellman (Gap-TOMDH) assumption if for all \( t', t, n, N \) such that \( t' < t \leq n \) and for all PPT adversary \( \mathcal{A} \), there exists a negligible function \( \text{negl} \) s.t. \( \text{One-More}_\mathcal{A}(1^\kappa, t', t, n, N) \) (Figure 5.1) outputs 1 with probability at most \( \text{negl}(\kappa) \).

In this game, a random polynomial of degree \( t - 1 \) is picked but \( \mathcal{A} \) gets to choose its value at \( t' \) points (steps 3 and 4). \( \mathcal{A} \) gets access to two oracles:

- \( \mathcal{O} \) allows it to compute \( x^{k_i} \), where \( k_i \) is the value of the randomly chosen polynomial at \( i \), for \( k_i \) that it does not know. A counter \( q_i \) is incremented for every such call.
- \( \mathcal{O}_{\text{DDH}} \) allows it to check if the discrete log of \( g_2 \) w.r.t. \( g_1 \) is the same as the discrete log of \( h_2 \) w.r.t. \( h_1 \).

Intuitively, to compute a pair of the form \((g, g^{k_0})\), \( \mathcal{A} \) should somehow get access to \( k_0 \). It clearly knows \( k_i \) for \( i \in \mathcal{U} \), but shares outside \( \mathcal{U} \) can only be obtained in the exponent, with the help of oracle \( \mathcal{O} \). One option for \( \mathcal{A} \) is to invoke \( \mathcal{O} \) with \((i, g)\) for at least \( t - t' \) different values of \( i \) outside of \( \mathcal{U} \), and then combine them together along with the \( k_i \) it knows to obtain \( g^{k_0} \).

If \( \mathcal{A} \) sticks to this strategy, it would have to repeat it entirely to compute \( h^{k_0} \) for a different base \( h \). It could invoke \( \mathcal{O} \) on different subsets of \([n]\) for different basis, but \( \text{MAX}_{t',t}(q_1, \ldots, q_n) \) will be the maximum number of pairs of type \((x, x^{k_0})\) it will be able to generate through this process.

Certainly, an adversary is not restricted to producing pairs in the way described above. However, Gap-TOMDH assumes that no matter what strategy a PPT adversary takes, it can effectively do no better than this.

### 5.2.2 Threshold Token Generation

A threshold token generation (TTG) scheme distributes the task of generating tokens for authentication among a set of \( n \) servers, such that at least a threshold \( t \) number of servers
One-More\(_A(1^\kappa,t',t,n,N)\):

1. \((p,g,\mathbb{G}) \xleftarrow{\$} \text{GroupGen}(1^\kappa)\)
2. \(g_1,\ldots,g_N \leftarrow \$ \mathbb{G}\)
3. \((\{(i,\alpha_i)\}_{i \in U},\hat{D}) \xleftarrow{\$} A(p,g,\mathbb{G},g_1,\ldots,g_N), \text{ where } U \subseteq [n], |U| = t'\)
4. \((k_0,k_1,\ldots,k_n) \leftarrow \$ \text{GenShare}(p,n,t,\{(i,\alpha_i)\}_{i \in U})\)
5. \(q_1,\ldots,q_n := 0\)
6. \(((g'_1,h_1),\ldots,(g'_\ell,h_\ell)) \xleftarrow{\$} A^{(\mathcal{O})}(\hat{D})\)
7. output 1 iff
   \(\ell > \text{MAX}_{t',t}(q_1,\ldots,q_n),\)
   \(\forall i \in [\ell], g'_i \in \{g_1,\ldots,g_N\} \text{ and } h_i = g'_ik_0, \text{ and}\)
   \(\forall i,j \in [\ell] \text{ s.t. } i \neq j, g'_i \neq g'_j.\)

\(\mathcal{O}(i,x):\)

- require: \(i \in [n] \setminus U, x \in \mathbb{G}\)
- increment \(q_i\) by 1
- return \(x^k_i\)

\(\mathcal{O}_{\text{DDH}}(g_1,g_2,h_1,h_2)\)

- require: \(g_1,g_2,h_1,h_2 \in \mathbb{G}\)
- return 1 iff \(\exists a \in \mathbb{Z}_p \text{ s.t. } g_2 = g_1^a \text{ and } h_2 = h_1^a\)

Figure 5.1: The Gap-TOMDH game.

must be contacted to compute a token. TTG provides a strong unforgeability guarantee: even if \(t' < t\) of the servers are corrupt, any time a token on some new value \(x\) is needed, at least \(t - t'\) servers must be contacted.

We formally define a TTG scheme and the unforgeability guarantee associated with it below.

**Definition 5.2.2** (Threshold Token Generation). A threshold token generation scheme TTG is a tuple of four PPT algorithms (\(\text{Setup}, \text{PartEval}, \text{Combine}, \text{Verify}\)) that satisfies the consistency property below.

- \(\text{Setup}(1^\kappa,n,t) \to ([sk],\text{vk},\text{pp})\). It generates a secret key \(sk\), shares \(sk_1, sk_2, \ldots, sk_n\) of the key, a verification key \(\text{vk}\), and public parameters \(\text{pp}\). Share \(sk_i\) is given to party \(i\). (\(\text{pp}\) will be an implicit input in the algorithms below.)
• PartEval($sk_i, x) \rightarrow y_i$. It generates shares of token for an input. Party $i$ computes the $i$-th share $y_i$ for $x$ by running PartEval with $sk_i$ and $x$.

• Combine($\{(i, y_i)_{i \in S}\} = tk/\perp$. It combines the shares received from parties in the set $S$ to generate a token $tk$. If the algorithm fails, its output is denoted by $\perp$.

• Verify($vk, x, tk) =: 1/0$. It verifies whether token $tk$ is valid for $x$ or not using the verification key $vk$. (Output $1$ denotes validity.)

Consistency. For all $\kappa \in \mathbb{N}$, any $n, t \in \mathbb{N}$ such that $t \leq n$, all ($[sk], vk, pp$) generated by Setup$(1^\kappa, n, t)$, any value $x$, and any set $S \subseteq [n]$ of size at least $t$, if $y_i \leftarrow$ PartEval$(sk_i, x)$ for $i \in S$, then Verify$(vk, x, \text{Combine}(\{(i, y_i)\}_{i \in S})) = 1$.

Definition 5.2.3 (Unforgeability). A threshold token generation scheme $TTG := \langle \text{Setup}, \text{PartEval}, \text{Combine}, \text{Verify} \rangle$ is unforgeable if for all PPT adversaries $A$, there exists a negligible function $\text{negl}$ such the probability that the following game outputs $1$ is at most $\text{negl}(\kappa)$.

Unforgeability$_{\text{TOP}, A}(1^\kappa, n, t)$:

• Initialize. Run $\text{Setup}(1^\kappa, n, t)$ to get $([sk], vk, pp)$. Give $pp$ to $A$.

• Corrupt. Receive the set of corrupt parties $U$ from $A$, where $t' := |U| < t$. Give $\{sk_i\}_{i \in U}$ to $A$.

• Evaluate. In response to $A$’s query $(\text{Eval}, x, i)$ for $i \in [n]\setminus U$, return $y_i := \text{PartEval}(sk_i, x)$. Repeat this step as many times as $A$ desires.

• Challenge. $A$ outputs $(x^*, tk^*)$. Check if
  
  – $|\{i \mid A \text{ made a query } (\text{Eval}, x^*, i)\}| < t - t'$ and
  – $\text{Verify}(vk, x^*, tk^*) = 1$.

Output $1$ if and only if both checks succeed.

The unforgeability property captures the requirement that it must not be possible to generate a valid token on some value if less than $t - t'$ servers are contacted with that value.

Concrete Schemes. Below we describe the concrete threshold token generation schemes we implement.

• The DDH-based DPRF scheme of Naor, Pinkas and Reingold [NPR99] as a public-key threshold MAC (Figure 5.2).

• The PRF-only DRPF scheme of Naor, Pinkas and Reingold [NPR99] as a symmetric-key MAC (Figure 5.3).
• The threshold RSA-signature scheme of Shoup [Sho00] as a threshold signature scheme based on RSA assumption (Figure 5.4).

• The pairing-based signature scheme of Boldyreva [Bol03] as a threshold signature scheme based on the RSA assumption (Figure 5.5).

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Ingredients: Let \( G = \langle g \rangle \) be a multiplicative cyclic group of prime order \( p \) in which the DDH assumption holds and \( \text{Hash} : \{0,1\}^* \rightarrow G \) be a hash function modeled as a random oracle. Let \( \text{GenShare} \) be Shamir’s secret sharing scheme.

- **Setup**\((1^\kappa, n, t) \rightarrow ([sk], vk, pp)\). Sample \( s \leftarrow \mathbb{Z}_p \) and get \((s, s_1, \ldots, s_n) \leftarrow \text{GenShare}(n, t, p, (0, s))\). Set \( pp := (p, g, \mathbb{G}) \), \( sk_i := s_i \) and \( vk := s \). Give \((sk_i, pp)\) to party \( i \). (\( pp \) will be an implicit input in the algorithms below.)

- **PartEval**\((sk_i, x) \rightarrow y_i\). Compute \( w := \text{Hash}(x) \), \( h_i := w^{sk_i} \) and output \( h_i \).

- **Combine**\((\{i, y_i\}_{i \in S}) =: tc/\bot\). If \(|S| < t\) output \( \bot \). Otherwise parse \( y_i \) as \( h_i \) for \( i \in S \) and output \( \prod_{i \in S} h_i^{x_s} \).

- **Verify**\((vk, x, tc) =: 1/0\). Return 1 if and only if \( \text{Hash}(x)^v = tc \).

Figure 5.2: DDH-based DPRF construction of Naor et al. [NPR99] (public-key threshold MAC).

Ingredients: Let \( f : \{0,1\}^\kappa \times \{0,1\}^* \rightarrow \{0,1\}^* \) be a pseudo-random function.

- **Setup**\((1^\kappa, n, t) \rightarrow ([sk], vk, pp)\). Pick \( d := \binom{n}{n-t+1} \) keys \( k_1, \ldots, k_d \leftarrow \{0,1\}^\kappa \) for \( f \). Let \( D_1, \ldots, D_d \) be the \( d \) distinct \( (n-t+1)\)-sized subsets of \([n]\). For \( i \in [n] \), let \( sk_i := \{k_j | i \in D_j \text{ for all } j \in [d]\} \) and \( vk := (k_1, \ldots, k_d) \). Set \( pp := f \) and give \((sk_i, pp)\) to party \( i \).

- **PartEval**\((sk_i, x) \rightarrow y_i\). Compute \( h_{i,k} := f_k(x) \) for all \( k \in sk_i \) and output \( \{h_{i,k}\}_{k \in sk_i} \).

- **Combine**\((\{i, y_i\}_{i \in S}) =: tc/\bot\). If \(|S| < t\) output \( \bot \). Otherwise parse \( y_i \) as \( \{h_{i,k}\}_{k \in sk_i} \) for \( i \in S \). Let \( \{sk'_i\}_{i \in S} \) be mutually disjoint sets such that \( \cup_{i \in S} sk'_i = \{k_1, \ldots, k_d\} \) and \( sk'_i \subseteq sk_i \) for every \( i \). Output \( \oplus_{k \in sk'_i} (h_{i,k}) \).

- **Verify**\((vk, x, tc) =: 1/0\). Return 1 if and only if \( \oplus_{k \in [d]} (f_k(x)) = tc \) where \( vk = \{k_1, \ldots, k_d\} \).

Figure 5.3: PRF-based DPRF of Naor et al. [NPR99] (symmetric-key threshold MAC).
Ingredients: Let \texttt{GenShare} be a Shamir’s secret sharing scheme and \texttt{Hash}: \{0,1\}^* \rightarrow \mathbb{Z}_N^* be a hash function modeled as a random oracle.

- \textbf{Setup}(1^n, n, t) → ([sk], vk, pp). Let \( p', q' \) be two randomly chosen large primes of equal length and set \( p = 2p' + 1 \) and \( q = 2q' + 1 \). Set \( N := pq \). Choose another large prime \( e \) at random and compute \( d \equiv e^{-1} \mod \Phi(N) \) where \( \Phi(\cdot) : \mathbb{N} \rightarrow \mathbb{N} \) is the Euler’s totient function. Then \( (d, d_1, \ldots, d_n) \leftarrow \texttt{GenShare}(n, t, \Phi(N), (0, d)) \). Let \( sk_i := d_i \) and \( vk := (N, e) \). Set \( pp := \Delta \) where \( \Delta := n! \). Give \((pp, vk, sk_i)\) to party \( i \).

- \textbf{PartEval}(sk_i, x) → y_i. Output \( y_i := \texttt{Hash}(x)^{2\Delta d_i} \).

- \textbf{Combine}(\{i, y_i\}_{i \in S}) =: tk/⊥. If \( |S| < t \) output \( ⊥ \), otherwise compute \( z := \prod_{i \in S} y_i^{2\lambda_i} \mod N \) where \( \lambda_i := \lambda_i \cdot \Delta \in \mathbb{Z} \). Find integer \((a, b)\) by Extended Euclidean GCD algorithm such that \( 4\Delta^2 a + eb = 1 \). Then compute \( tk := z^a \cdot \texttt{Hash}(x)^b \mod N \). Output \( tk \).

- \textbf{Verify}(vk, x, tk) = 1/0. Return 1 if and only if \( tk^c = \texttt{Hash}(x) \mod N \).

Figure 5.4: Threshold RSA-signature scheme of Shoup [Sho00].

Ingredients: Let \( \mathbb{G} = \langle g \rangle \) be a multiplicative cyclic group of prime order \( p \) that supports pairing and in which CDH is hard. In particular, there is an efficient algorithm \( \texttt{VerDDH}(g^a, g^b, g^c, g) \) that returns 1 if and only if \( c = ab \mod p \) for any \( a, b, c \in \mathbb{Z}_p^* \) and 0 otherwise. Let \texttt{Hash}: \{0,1\}^* \rightarrow \mathbb{G} \) be a hash function modeled as a random oracle. Let \texttt{GenShare} be the Shamir’s secret sharing scheme.

- \textbf{Setup}(1^n, n, t) → ([sk], vk, pp). Sample \( s \leftarrow \mathbb{Z}_p^* \) and get \((s, s_1, \ldots, s_n) \leftarrow \texttt{GenShare}(n, t, p, (0, s)) \). Set \( pp := (p, g, \mathbb{G}) \), \( sk_i := s_i \) and \( vk := g^s \). Give \((sk_i, pp)\) to party \( i \).

- \textbf{PartEval}(sk_i, x) → y_i. Compute \( w := \texttt{Hash}(x) \), \( h_i := w^{sk_i} \) and output \( h_i \).

- \textbf{Combine}(\{i, y_i\}_{i \in S}) =: tk/⊥. If \( |S| < t \) output \( ⊥ \). Otherwise parse \( y_i \) as \( h_i \) for \( i \in S \) and output \( \prod_{i \in S} h_i^\lambda_i \mod p \)

- \textbf{Verify}(vk, x, tk) =: 1/0. Return 1 if and only if \( \texttt{VerDDH}(\texttt{Hash}(x), vk, tk, g) = 1 \).

Figure 5.5: Pairing-based threshold signature scheme of Boldyreva [Bol03].

### 5.3 Threshold Oblivious Pseudo-Random Function

A pseudo-random function (PRF) family is a keyed family of deterministic functions. A function chosen at random from the family is indistinguishable from a random function. Oblivious PRF (OPRF) is an extension of PRF to a two-party setting where a server \( S \)
holds the key and a party \( P \) holds an input \( [FIPR05] \). \( S \) can help \( P \) in computing the PRF value on the input but in doing so \( P \) should not get any other information and \( S \) should not learn \( P \)’s input.

Jarecki et al. [JKKX17] extend OPRF to a multi-server setting so that a threshold number \( t \) of the servers are needed to compute the PRF on any input. Furthermore, a collusion of at most \( t-1 \) servers learns no information about the input. They propose a functionality for TOPRF and show how to realize it in a UC-secure way. We instead treat TOPRF as a set of algorithms that must satisfy two natural properties, unpredictability and obliviousness.

### 5.3.1 Syntax

**Definition 5.3.1 (Threshold Oblivious Pseudo-Random Function).** An \((\mathcal{X}, \mathcal{R})\)-threshold oblivious pseudo-random function (TOPRF) \( \text{TOP} \) is a tuple of four PPT algorithms (\text{Setup}, \text{Encode}, \text{Eval}, \text{Combine}) that satisfies the consistency property below.

1. **\text{Setup}(1^\kappa, n, t) \rightarrow ([\text{sk}], \text{pp}).** It generates \( n \) secret key shares \( \text{sk}_1, \text{sk}_2, \ldots, \text{sk}_n \) and public parameters \( \text{pp} \). Share \( \text{sk}_i \) is given to party \( i \). (\( \text{pp} \) will be an implicit input in the algorithms below.)

2. **\text{Encode}(x, \rho) \rightarrow c.** It generates an encoding \( c \) of \( x \in \mathcal{X} \) using randomness \( \rho \in \mathcal{R} \).

3. **\text{Eval}(\text{sk}_i, c) \rightarrow z_i.** It generates shares of TOPRF value from an encoding. Party \( i \) computes the \( i \)-th share \( z_i \) from \( c \) by running \text{Eval} with \( \text{sk}_i \) and \( c \).

4. **\text{Combine}(x, \{(i, z_i)\}_{i \in S}, \rho) \rightarrow h/\bot.** It combines the shares received from parties in the set \( S \) using randomness \( \rho \) to generate a value \( h \). If the algorithm fails, its output is denoted by \( \bot \).

**Consistency.** For all \( \kappa \in \mathbb{N} \), any \( n, t \in \mathbb{N} \) such that \( t \leq n \), all \(([\text{sk}], \text{pp}) \) generated by \text{Setup}(1^\kappa, n, t), any value \( x \in \mathcal{X} \), any randomness \( \rho, \rho' \in \mathcal{R} \), and any two sets \( S, S' \subseteq \mathbb{N} \) of size at least \( t \), if \( c := \text{Encode}(x, \rho) \), \( c' := \text{Encode}(x, \rho') \), \( z_i := \text{Eval}(\text{sk}_i, c) \) for \( i \in S \), and \( z'_j := \text{Eval}(\text{sk}_j, c') \) for \( j \in S' \), then \text{Combine}(x, \{(i, z_i)\}_{i \in S}, \rho) = \text{Combine}(x, \{(j, z'_j)\}_{j \in S'}, \rho') \neq \bot.

Thus, irrespective of the randomness used to encode an \( x \) and the set of parties whose shares are combined, the output of \text{Combine} does not change (as long as \text{Combine} is given the same randomness used for encoding). We call this output the output of the TOPRF on \( x \), and denote it by \( \text{TOP}(\text{sk}, x) \).

**Public Combine.** We also consider a public combine algorithm \( \text{PubCombine} \) that could be run by anyone with access to just the partial evaluations. It would be used to check if a purported set of evaluations can lead to the right PRF value or not. Formally, for \( Z := \{(i, z_i)\}_{i \in S} \) generated in the same manner as that for the consistency property, and any arbitrary \( Z^* := \{(i, z'_i)\}_{i \in S} \), if \( \text{PubCombine}(Z) = \text{PubCombine}(Z^*) \) then \( \text{Combine}(x, Z, \rho) = \text{Combine}(x, Z^*, \rho) \)
Combine\((x, Z^*, \rho)\). More importantly though, if the former equality does not hold then the later must not hold either (with high probability).

\section*{Security Properties}

We want a TOPRF scheme to satisfy two properties, unpredictability and obliviousness. Unpredictability mandates that it must be difficult to predict TOPRF output on a random value, and obliviousness mandates that the random value itself is hard to guess even if the TOPRF output is available.

\begin{figure}
\begin{center}
\begin{tabular}{|l|}
\hline
\textbf{Unpredictability}_{\text{TOP,}A}(1^\kappa, n, t):
\hline
\begin{itemize}
\item \((\{sk\}, pp) \xleftarrow{\$} \text{Setup}(1^\kappa, n, t)\)
\item \(\mathcal{U} \xleftarrow{\$} \mathcal{A}(pp)\)
\item \(\widehat{x} \xleftarrow{\$} \mathcal{X}\)
\item \(q_1, \ldots, q_n := 0\)
\item \(h^* \xleftarrow{\$} \mathcal{A}(\mathcal{O})(\{sk_i\}_{i \in \mathcal{U}})\)
\item output 1 iff \(\text{TOP}(sk, \widehat{x}) = h^*\)
\end{itemize}
\end{tabular}
\end{center}
\begin{tabular}{|l|}
\hline
\textbf{O}_{\text{enc}\&\text{eval}}():
\hline
\begin{itemize}
\item \(c := \text{Encode}(\widehat{x}, \rho)\) for \(\rho \xleftarrow{\$} \mathcal{R}\)
\item for \(i \in [n] \setminus \mathcal{U}\), \(z_i \xleftarrow{\$} \text{Eval}(sk_i, c)\)
\item return \(c, \{z_i\}_{i \in [n] \setminus \mathcal{U}}\)
\end{itemize}
\end{tabular}
\begin{tabular}{|l|}
\hline
\textbf{O}_{\text{eval}}(i, c):
\hline
\begin{itemize}
\item require: \(i \in [n] \setminus \mathcal{U}\)
\item increment \(q_i\) by 1
\item return \(\text{Eval}(sk_i, c)\)
\end{itemize}
\end{tabular}
\begin{tabular}{|l|}
\hline
\textbf{O}_{\text{check}}(h):
\hline
\begin{itemize}
\item return 1 if \(h = \text{TOP}(sk, \widehat{x})\); else return 0
\end{itemize}
\end{tabular}
\end{figure}

\textbf{Definition 5.3.2 (Unpredictability).} A \((\mathcal{X}, \mathcal{R})\)-TOPRF \(\text{TOP} := (\text{Setup, Encode, Eval, Combine})\) is unpredictable if for all \(n, t \in \mathbb{N}, t \leq n\), and PPT adversaries \(\mathcal{A}\), there ex-
ists a negligible function \( \text{negl} \) s.t.

\[
\Pr[\text{Unpredictability}_{\text{TOP}, A}(1^\kappa, n, t) = 1] \leq \frac{\text{MAX}_{q_1, \ldots, q_n} |q_1, \ldots, q_n|}{|\mathcal{X}|} + \text{negl}(\kappa),
\] (5.1)

where Unpredictability is defined in Figure 5.6.

Our unpredictability definition provides several interfaces to an adversary \( A \). Oracle \( \mathcal{O}_{\text{enc} \& \text{eval}} \) can be called any number of times to get different sets of partial evaluations on the challenge input \( \tilde{x} \), but the randomness used in this process is not revealed to \( A \). If no query is made to \( \mathcal{O}_{\text{eval}} \), so that none of the \( q_i \) change, then \( A \)'s probability of guessing the TOPRF output on \( \tilde{x} \) should be negligible (see Eq. (5.1)). In other words, any number of partial evaluations by themselves should not help at all.

\( A \) could, however, encode an arbitrary input itself, get partial evaluations through \( \mathcal{O}_{\text{eval}} \), and then combine them to learn the TOPRF output. It could also check if this output is same as the TOPRF output on the challenge input through \( \mathcal{O}_{\text{check}} \). Thus, by repeatedly querying \( \mathcal{O}_{\text{eval}} \), adversary can increase its chances of making the right guess. Eq. (5.1) requires that the probability of success should be no more than the maximum number of TOPRF outputs \( A \) can learn through this process over the size of password space. In some sense, this is the best we can hope to achieve.

**Definition 5.3.3 (Obliviousness).** An \((\mathcal{X}, \mathcal{R})\)-TOPRF \( \text{TOP} := (\text{Setup, Encode, Eval, Combine}) \) is oblivious if for all \( n, t \in \mathbb{N}, t \leq n \), and all PPT adversaries \( A \), there exists a negligible function \( \text{negl} \) s.t.

\[
\Pr[\text{Obliviousness}_{\text{TOP}, A}(1^\kappa, n, t) = 1] \leq \frac{\text{MAX}_{q_1, \ldots, q_n} |q_1, \ldots, q_n| + 1}{|\mathcal{X}|} + \text{negl}(\kappa),
\] (5.2)

where Obliviousness is defined in Figure 5.7.

The obliviousness definition differs from unpredictability in small but crucial ways. Unlike the unpredictability game, \( A \) directly gets \( h \) from \( \mathcal{O}_{\text{enc} \& \text{eval}} \) because our goal is not to challenge the adversary on guessing the TOPRF output. \( A \) can still use \( \mathcal{O}_{\text{eval}} \) to learn new TOPRF outputs and check (by itself) it they match with \( h \) or not. Thus it can improve its chances of guessing \( \tilde{x} \). The bound of Eq. 5.2 differs slightly from that of Eq. 5.1 though: there is an extra additive factor of 1 in the former case. This is to take into account that \( A \) can output a guess for \( \tilde{x} \) different from the ones it has tried in the game.

### 5.3.3 Construction

We recall here the TOPRF construction, 2HashTDH, of Jarecki et al. [JJKX17, Section 3], for an input space \( \mathcal{X} \). We refer to this construction as TOP from here onwards.
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Obliviousness\textsubscript{TOP,\mathcal{A}}(1^\kappa, n, t):

- ([sk], pp) \xleftarrow{\$} \text{Setup}(1^\kappa, n, t)
- \mathcal{U} \xleftarrow{\$} \mathcal{A}(pp)
- \tilde{x} \xleftarrow{\$} \mathcal{X}^\kappa
- q_1, \ldots, q_n := 0
- x^* \xleftarrow{\$} \mathcal{A}(\mathcal{O})(\{sk_i\}_{i \in \mathcal{U}})
- output 1 iff x^* = \tilde{x}

\text{O}_{\text{enc}}(\text{eval})():

- c := Encode(\tilde{x}, \rho) for \rho \xleftarrow{\$} \mathcal{R}
- for i \in [n], z_i \xleftarrow{\$} \text{Eval}(sk_i, c)
- h := \text{Combine}(\tilde{x}, \{(i, z_i)\}_{i \in [n]}, \rho)
- return c, \{z_i\}_{i \in [n]} \setminus \mathcal{U}, h

\text{O}_{\text{eval}}(i, c):

- require: i \in [n] \setminus \mathcal{U}
- increment q_i by 1
- return \text{Eval}(sk_i, c)

Figure 5.7: The obliviousness game.

- \text{Setup}(1^\kappa, n, t). Run \text{GroupGen}(1^\kappa) to get (p, g, \mathcal{G}). Pick an sk at random from \mathbb{Z}_p. Let [sk] \xleftarrow{\$} \text{GenShare}(p, n, t, (0, sk)) be a (t, n)-Shamir sharing of sk. Let Hash_1 : \mathcal{X} \times \mathcal{G} \rightarrow \{0, 1\}^\kappa and Hash_2 : \mathcal{X} \rightarrow \mathcal{G} be hash functions. Output [sk] and pp := (p, g, \mathcal{G}, n, t, Hash_1, Hash_2).

- \text{Encode}(x, \rho). Output \text{Hash}_2(x^\rho).

- \text{Eval}(sk_i, c). Output c^{sk_i}.

- \text{Combine}(x, \{(i, z_i)\}_{i \in S}, \rho). If |S| < t - 1, output \perp. Else, use S to find coefficients \{\lambda_i\}_{i \in S}, compute z := \prod_{i \in S} z_i^{\lambda_i}, and output Hash_1(x, z^{\rho - 1}). It is easy to see that \text{TOP} is a consistent (\mathcal{X}, \mathbb{Z}_p^\kappa)-\text{TOPRF} scheme.

Public Combine. One can define \text{PubCombine}(\{(i, z_i)\}_{i \in S}) for 2\text{HashTDH} to output z, the intermediate value in \text{Combine}. Given x, z and \rho, the output of \text{Combine} is fixed. So, if an arbitrary set of partial evaluations produces the same z, \text{Combine} would output the same
thing. Moreover, if PubCombine produces a \( z^* \) different from \( z \), then \( z^*p^{-1} \neq z^o^{-1} \), and output of Combine will be different with high probability (assuming that Hash \(_1 \) is collision-resistant).

### 5.3.4 Security Proof

We prove unpredictability and obliviousness of TOP in Sections 5.3.4.1 and 5.3.4.2, respectively.

#### 5.3.4.1 Unpredictability

In this section we prove output unpredictability of our construction. Suppose there exists a PPT adversary \( A \) such that

\[
\text{Pr}[	ext{Unpredictability}_{\text{TOP},A}(1^\kappa, n, t) = 1] \geq \frac{\text{MAX}_{\mu,t}(q_1, \ldots, q_n)}{|\mathcal{X}|} + \text{non-negl}(\kappa). \tag{5.3}
\]

We will consider two cases of \( A \). In the first case, there exists \( k \in \mathbb{N} \) such that when \( A \) calls \( \text{Hash}_1 \) with \( k \) distinct valid \( (x, y) \) pairs, \( \text{MAX}_{\mu,t}(q_1, \ldots, q_n) < k \). In this case, we will use \( A \) to break the gap TOMDH assumption. In the second case, for any \( k \in \mathbb{N} \), when \( A \) calls \( \text{Hash}_1 \) with \( k \) valid \( (x, y) \) pairs, \( \text{MAX}_{\mu,t}(q_1, \ldots, q_n) \geq k \). In this case, we will prove that information theoretically Formula 5.3 does not hold.

**First Case.** There exists \( k \in \mathbb{N} \) such that when \( A \) calls \( \text{Hash}_1 \) with \( k \) distinct valid \( (x, y) \) pairs, \( \text{MAX}_{\mu,t}(q_1, \ldots, q_n) < k \). Then we construct an adversary \( B \) that breaks the gap TOMDH assumption (see Definition 5.2.1).

We construct \( B \) as follows. It first receives \((p, g, G, g_1, \ldots, g_N)\) from the TOMDH game \( \text{One-More}_B(1^\kappa, t', t, n, N) \), presents \( pp := (p, g, G, n, t, \text{Hash}_1, \text{Hash}_2) \) to \( A \) and gets back \( U \). It then generates \( \{\alpha_i\}_{i \in U} \) at random, sends \( \{(i, \alpha_i)\}_{i \in U} \) to the TOMDH game, and sends \( \{\alpha_i\}_{i \in U} \) to \( A \). It then samples \( \tilde{x} \leftarrow \mathcal{X} \), set \( \mathcal{L} := \emptyset \), set \( \text{LIST} := \emptyset \), and set \( k := 0 \). Then \( B \) computes \( g_1^{\alpha_i} \) for \( i \in U \), calls \( \mathcal{O}(i, g_1) \) to get \( g_1^{\alpha_i} \) for all \( i \in [n] \setminus U \), and computes \( y_1 := g_1^k \). It adds \((g_1, y_1)\) to \( \text{LIST} \), sets \( q = 1 \), and handles \( A \)'s oracle queries as follows:

- On \( A \)'s call to \( \mathcal{O}_{\text{enc}}(\mathcal{E}) \): Pick an unused \( g_j \) where \( j \in [N] \), set \( c := g_j \), compute \( z_i \leftarrow \text{Eval}(sk_i, c) \) for \( i \in U \) and call \( \mathcal{O}(i, c) \) to get \( z_i \) for all \( i \in [n] \setminus U \). Use \([n] \) to find coefficients \( \{\lambda_i\}_{i \in [n]} \) and compute \( y_i := \prod_{i \in [n]} z_i^{\lambda_i} \). Add \((g_j, y_j)\) to \( \text{LIST} \), and increment \( q \) by 1. Compute \( h := \text{Hash}_1(\tilde{x}, y_j) \), and return \((c, \{z_i\}_{i \in [n] \setminus U}, h)\) to \( A \).
- On \( A \)'s call to \( \mathcal{O}_{\text{eval}}(i, c) \): Call \( \mathcal{O}(i, c) \) in the TOMDH game and return the output to \( A \).
- On \( A \)'s call to \( \text{Hash}_1(x, y) \): If \( x \notin \mathcal{L} \), compute \( \text{Hash}_1(x, y) \) honestly and return to \( A \). Otherwise, let \( g_j := \mathcal{L}[x] \). If \( \log_{g_j} y = \log_{g_j} y_1 \) and \((g_j, *) \notin \text{LIST} \) (i.e., \((g_j, y)\) is a new valid pair), increment \( k \) by 1, add \((g_j, y)\) to \( \text{LIST} \), and output \( \text{LIST} \) in the TOMDH game if \( \text{MAX}_{\mu,t}(q_1, \ldots, q_n) < k \). Compute \( \text{Hash}_1(x, y) \) honestly and return to \( A \).
• On $\mathcal{A}$'s call to $\text{Hash}_2(x)$: If $x \in \mathcal{L}$, return $\mathcal{L}[x]$; otherwise, pick an unused $g_j$ where $j \in [N]$, set $\mathcal{L}[x] := g_j$ and return $g_j$ to $\mathcal{A}$.

$\mathcal{A}$'s view in the game $\text{Unpredictability}_{\text{TOP},\mathcal{A}}(1^\kappa, n, t)$ is information theoretically indistinguishable from the view simulated by $\mathcal{B}$ in the random oracle model. This can be proved via a hybrid argument:

$H_0$: The first hybrid is $\mathcal{A}$'s view in the real-world game $\text{Unpredictability}_{\text{TOP},\mathcal{A}}(1^\kappa, n, t)$.

$H_1$: This hybrid is the same as $H_0$ except that in the response to $O_{\text{enc\&eval}}$, $c$ is randomly sampled as $c \leftarrow \mathcal{G}$. This hybrid is information theoretically indistinguishable from $H_0$ because $\mathcal{G}$ is a cyclic group of prime order.

$H_2$: This hybrid is the same as $H_1$ except that the output of $\text{Hash}_2(\cdot)$ is a truly random group element in $\mathcal{G}$. The indistinguishability of $H_1$ and $H_2$ follows from the random oracle model.

$H_3$: This hybrid is $\mathcal{A}$'s view simulated by $\mathcal{B}$. It is the same as $H_2$ except that the random group elements are replaced by $g_j$'s where $j \in [N]$. Since $g_j$'s are also randomly sampled from $\mathcal{G}$ in the TOMDH game, the two hybrids are indistinguishable.

From the construction of $\mathcal{B}$, we know that

$$\text{MAX}_{k',t}(q'_1, \ldots, q'_n) = \text{MAX}_{\mathcal{U},t}(q_1, \ldots, q_n) + q,$$

where $\text{MAX}_{k',t}(q'_1, \ldots, q'_n)$ is from the TOMDH game, and $\text{MAX}_{\mathcal{U},t}(q_1, \ldots, q_n)$ is from the unpredictability game. Since $\text{MAX}_{\mathcal{U},t}(q_1, \ldots, q_n) < k$, the output of $\mathcal{B}$ has the following number of valid pairs:

$$|\text{LIST}| = k + q > \text{MAX}_{\mathcal{U},t}(q_1, \ldots, q_n) + q = \text{MAX}_{k',t}(q'_1, \ldots, q'_n).$$

Therefore, $\mathcal{B}$ breaks the gap TOMDH assumption.

**Second Case.** For any $k \in \mathbb{N}$, when $\mathcal{A}$ calls $\text{Hash}_1$ with $k$ valid $(x, y)$ pairs, $\text{MAX}_{\mathcal{U},t}(q_1, \ldots, q_n) \geq k$.

We define a predicting game in Figure 5.8. Information theoretically we have that for any PPT adversary $\mathcal{A}$, there exists a negligible function $\text{negl}$ s.t.

$$\Pr[\text{Predicting}_{\mathcal{A}}(1^\kappa) = 1] \leq \frac{k}{|\mathcal{A}|} + \text{negl}(\kappa).$$

We will use $\mathcal{A}$ to construct an adversary $\mathcal{B}$ that breaks the predicting game. The construction of $\mathcal{B}$ is the following. It first runs $\text{Setup}(1^\kappa, n, t)$ to generate $(\llbracket \text{sk} \rrbracket, \text{pp})$, presents $\text{pp}$ to $\mathcal{A}$ and gets back $\mathcal{U}$. It then gives $\{\text{sk}_i\}_{i \in \mathcal{U}}$ to $\mathcal{A}$. It sets $\mathcal{L} := \emptyset$, and then handles $\mathcal{A}$'s oracle queries as follows:
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Predictingₐ(1κ):

1. for every $x \in X$: $h \leftarrow \{0,1\}^\kappa$, $M[x] := h$
2. $\tilde{x} \leftarrow X$, $\tilde{h} := M[\tilde{x}]$
3. $k := 0$
4. $h^* \leftarrow A^{(O)}(1^\kappa)$
5. output 1 iff $h^* = \tilde{h}$

$O_{\text{compute}}(x)$:

- increment $k$ by 1
- return $M[x]

$O_{\text{compare}}(h)$:

- return 1 if $h = \tilde{h}$; else return 0

\begin{figure}[h]
\centering
\begin{tabular}{|l|}
\hline
Predictingₐ(1κ):
\hline
1. for every $x \in X$: $h \leftarrow \{0,1\}^\kappa$, $M[x] := h$
2. $\tilde{x} \leftarrow X$, $\tilde{h} := M[\tilde{x}]$
3. $k := 0$
4. $h^* \leftarrow A^{(O)}(1^\kappa)$
5. output 1 iff $h^* = \tilde{h}$
\hline
$O_{\text{compute}}(x)$:
\hline
- increment $k$ by 1
- return $M[x]
\hline
$O_{\text{compare}}(h)$:
\hline
- return 1 if $h = \tilde{h}$; else return 0
\hline
\end{tabular}
\caption{The predicting game.}
\end{figure}

- On $A$’s call to $O_{\text{enc\&eval}}()$: Sample $c \leftarrow \mathcal{G}$, compute $z_i \leftarrow \text{Eval}(sk_i, c)$ for $i \in [n]$, and return $(c, \{z_i\}_{i \in [n]})$ to $A$.
- On $A$’s call to $O_{\text{eval}}(i, c)$: Return $\text{Eval}(sk_i, c)$.
- On $A$’s call to $O_{\text{check}}(h)$: Call $O_{\text{compare}}(h)$ and return the output to $A$.
- On $A$’s call to $\text{Hash}_1(x, y)$:
  a. If $(x, y) \in \mathcal{L}$, let $h := \mathcal{L}[(x, y)]$.
  b. If $(x, y) \notin \mathcal{L}$ and $y \neq \text{Hash}_2(x)^{sk}$, then sample $h \leftarrow \{0,1\}^\kappa$ and set $\mathcal{L}[(x, y)] := h$.
  c. If $(x, y) \notin \mathcal{L}$ and $y = \text{Hash}_2(x)^{sk}$ (i.e., $(x, y)$ is a valid pair), call $O_{\text{compute}}(x)$ to get $h$. Set $\mathcal{L}[(x, y)] := h$.

Return $h$ to $A$.
- On $A$’s call to $\text{Hash}_2(x)$: Compute $\text{Hash}_2(x)$ honestly and return the output.

$A$’s view in the game $\text{Unpredictability}_{\text{TOP, } A}(1^\kappa, n, t)$ is information theoretically indistinguishable from the view simulated by $B$ in the random oracle model. This can be proved via a hybrid argument:

$H_0$: The first hybrid is $A$’s view in the real-world game $\text{Unpredictability}_{\text{TOP, } A}(1^\kappa, n, t)$. 
H\(_1\): This hybrid is the same as \(H_0\) except that in the response to \(O_{\text{enc\&eval}}()\), \(c\) is randomly sampled as \(c \leftarrow \mathbb{G}\). This hybrid is information theoretically indistinguishable from \(H_0\) because \(\mathbb{G}\) is a cyclic group of prime order.

\(H_2\): This hybrid is the same as \(H_1\) except that the output of \(\text{Hash}_1(\cdot)\) is a truly random string. The indistinguishability of \(H_1\) and \(H_2\) follows from the random oracle model.

\(H_3\): This hybrid is \(A\)'s view simulated by \(B\). It is the same as \(H_2\) except that \(\tilde{x}\) is not sampled in the game, but sampled in the predicting game \(\text{Predicting}_B(1^\kappa)\), and that \(\text{Hash}_1(x, y)\) for valid \((x, y)\) pairs are sampled in predicting game. Since these values are randomly sampled in both hybrids, they are indistinguishable.

Therefore, if \(A\) breaks the game \(\text{Unpredictability}_{\text{TOP}}(1^\kappa, n, t)\), then \(B\) breaks the predicting game:

\[
\Pr[\text{Predicting}_B(1^\kappa) = 1] \geq \frac{\text{MAX}_{[\mathcal{U}, t]}(q_1, \ldots, q_n)}{|\mathcal{X}|} + \text{non-negl}(\kappa)
\]

\[
\geq \frac{k}{|\mathcal{X}|} + \text{non-negl}(\kappa).
\]

This is information theoretically impossible, leading to a contradiction, and hence concludes the proof.

### 5.3.4.2 Input Obliviousness

In this section we prove input obliviousness of our construction. Suppose there exists a PPT adversary \(A\) such that

\[
\Pr[\text{Obliviousness}_{\text{TOP}, A}(1^\kappa, n, t) = 1] \geq \frac{\text{MAX}_{[\mathcal{U}, t]}(q_1, \ldots, q_n) + 1}{|\mathcal{X}|} + \text{non-negl}(\kappa). \quad (5.4)
\]

We consider two cases of \(A\). In the first case, there exists \(k \in \mathbb{N}\) such that when \(A\) calls \(\text{Hash}_1\) with \(k\) distinct valid \((x, y)\) pairs, \(\text{MAX}_{[\mathcal{U}, t]}(q_1, \ldots, q_n) < k\). In this case, we will use \(A\) to break the gap TOMDH assumption. In the second case, for any \(k \in \mathbb{N}\), when \(A\) calls \(\text{Hash}_1\) with \(k\) valid \((x, y)\) pairs, \(\text{MAX}_{[\mathcal{U}, t]}(q_1, \ldots, q_n) \geq k\). In this case, we will prove that information theoretically Formula 5.4 does not hold.

**First Case.** There exists \(k \in \mathbb{N}\) such that when \(A\) calls \(\text{Hash}_1\) with \(k\) distinct valid \((x, y)\) pairs, \(\text{MAX}_{[\mathcal{U}, t]}(q_1, \ldots, q_n) < k\). Then we construct an adversary \(B\) that breaks the gap TOMDH assumption (see Definition 5.2.1). The proof is the same as the first case in Section 5.3.4.1.

**Second Case.** Whenever \(A\) calls \(\text{Hash}_1\) with a valid \((x, y)\) pair for the \(k\)-th time, \(\text{MAX}_{[\mathcal{U}, t]}(q_1, \ldots, q_n) \geq k\) at that time.
We define a guessing game in Figure 5.9. Information theoretically we have that for any PPT adversary $A$, there exists a negligible function $\negl$ s.t.

$$\Pr[\text{Guessing}_A(1^\kappa) = 1] \leq \frac{k + 1}{|X|} + \negl(\kappa).$$

<table>
<thead>
<tr>
<th>Guessing$_A(1^\kappa)$:</th>
</tr>
</thead>
<tbody>
<tr>
<td>1. $\tilde{x} \leftarrow X$</td>
</tr>
<tr>
<td>2. $k := 0$</td>
</tr>
<tr>
<td>3. $x^* \leftarrow A^{(O)}(1^\kappa)$</td>
</tr>
<tr>
<td>4. output 1 iff $x^* = \tilde{x}$</td>
</tr>
</tbody>
</table>

$O_{\text{guess}}(x)$:
- increment $k$ by 1
- return 1 if $x = \tilde{x}$; else return 0

Figure 5.9: The guessing game.

We will use $A$ to construct an adversary $B$ that breaks the guessing game. The construction of $B$ is the following. It first runs $\text{Setup}(1^\kappa, n, t)$ to generate $(\{sk\}, pp)$, presents $pp$ to $A$ and gets back $U$. It then gives $\{sk_i\}_{i \in U}$ to $A$. It samples $\tilde{h} \leftarrow \{0, 1\}^\kappa$, set $L := \emptyset$, and then handles $A$’s oracle queries as follows:

- On $A$’s call to $O_{\text{enc} \& \text{eval}}()$: Sample $c \leftarrow G$, compute $z_i \leftarrow \text{Eval}(sk_i, c)$ for $i \in [n]$, and return $(c, \{z_i\}_{i \in [n] \setminus U}, \tilde{h})$ to $A$.
- On $A$’s call to $O_{\text{eval}}(i, c)$: Return $\text{Eval}(sk_i, c)$.
- On $A$’s call to $\text{Hash}_1(x, y)$:
  a. If $(x, y) \in L$, let $h := L[(x, y)]$.
  b. If $(x, y) \notin L$ and $y \neq \text{Hash}_2(x)^{sk}$, then sample $h \leftarrow \{0, 1\}^\kappa$ and set $L[(x, y)] := h$.
  c. If $(x, y) \notin L$ and $y = \text{Hash}_2(x)^{sk}$ (i.e., $(x, y)$ is a valid pair), call $O_{\text{guess}}(x)$. If the output if 1, then $h := \tilde{h}$; otherwise sample $h \leftarrow \{0, 1\}^\kappa$. Set $L[(x, y)] := h$.

Return $h$ to $A$.
- On $A$’s call to $\text{Hash}_2(x)$: Compute $\text{Hash}_2(x)$ honestly and return the output.
A’s view in the game $\text{Obliviousness}_{\text{TOP},A}(1^\kappa, n, t)$ is information theoretically indistinguishable from the view simulated by $B$ in the random oracle model. This can be proved via a hybrid argument:

$H_0$: The first hybrid is $A$’s view in the real-world game $\text{Obliviousness}_{\text{TOP},A}(1^\kappa, n, t)$.

$H_1$: This hybrid is the same as $H_0$ except that in the response to $O_{\text{enc\&eval}}()$, $c$ is randomly sampled as $c \leftarrow \mathbb{G}$. This hybrid is information theoretically indistinguishable from $H_0$ because $\mathbb{G}$ is a cyclic group of prime order.

$H_2$: This hybrid is the same as $H_1$ except that the output of $\text{Hash}_1(\cdot)$ is a truly random string. The indistinguishability of $H_1$ and $H_2$ follows from the random oracle model.

$H_3$: This hybrid is $A$’s view simulated by $B$. It is the same as $H_2$ except that $\tilde{x}$ is not sampled in the game, but sampled in the guessing game $\text{Guessing}_B(1^\kappa)$. Since $\tilde{x}$ is randomly sampled as $\tilde{x} \leftarrow \mathcal{X}$ in both hybrids, they are indistinguishable.

Therefore, if $A$ breaks the game $\text{Obliviousness}_{\text{TOP},A}(1^\kappa, n, t)$, then $B$ breaks the guessing game:

$$\Pr[\text{Guessing}_B(1^\kappa) = 1] \geq \frac{\text{MAX}_{g|\mathcal{X}}(q_1, \ldots, q_n) + 1}{|\mathcal{X}|} + \text{non-negl}(\kappa)$$

$$\geq k + 1 + \text{non-negl}(\kappa).$$

This is information theoretically impossible, leading to a contradiction, and hence concludes the proof.

### 5.4 Password-Based Threshold Authentication

In a password-based threshold authentication (PbTA) system, there are $n$ servers and any number of clients. PbTA is naturally split into four phases: (i) during a global set-up phase, a master secret key is shared among the servers, which they later use to generate authentication tokens, (ii) in the registration phase, a client $C$ computes sign-up messages (one for each server) based on its username and password and sends them to the servers. Each server processes the message it receives and stores a unique record for that client. (iii) in the sign-on phase, a client initiates authentication by sending a request message that incorporates its username/password and additional information to be included in the token. Each server computes a response using its record for the client. This response contains shares of the authentication token the client eventually wants to obtain. If client’s password is a match he is able to combine and finalize the token shares into a single valid token for future accesses. (iv) The finalized token can be verified using a verification algorithm that takes a public or private (depending on the token type) verification key to validate that the token was generated using the unique master secret key. The verification process can also be distributed among multiple servers (may be required for MAC-based tokens) but for simplicity we use a centralized verification phase.
We also note that in a PbTA scheme, clients need not store any persistent secret information. The only secret they need to sign-on is their password which would not be stored anywhere. The device(s) a client uses to sign-on can store certain public parameters of the system (e.g. the identities of the servers).

For simplicity, we assume that clients choose passwords uniformly at random from a space $P$. Our definitions can be extended to the general case.

Below we define the syntax and security of PbTA in Sections 5.4.1 and 5.4.2, respectively. Next we present our PASTA framework in Section 5.4.3 and prove its security in Section 5.4.4.

### 5.4.1 Syntax

**Definition 5.4.1** (Password-based Threshold Authentication). A PbTA scheme $\Pi$ is a tuple of seven PPT algorithms ($\text{GlobalSetup}, \text{SignUp}, \text{Store}, \text{Request}, \text{Respond}, \text{Finalize}, \text{Verify}$) that satisfies the correctness requirement below.

- **GlobalSetup**($1^\kappa, n, t, P$) $\rightarrow$ ($[sk], vk, pp$). It takes the security parameter, number of servers $n$, a threshold $t$ and the space of passwords $P$ as inputs. It outputs a secret key $sk$, shares $sk_1, sk_2, \ldots, sk_n$ of the key, and a verification key $vk$. The public parameters $pp$ include all the inputs to $\text{GlobalSetup}$ and some other information if needed.

$pp$ will be an implicit input in the algorithms below. The $n$ servers will be denoted by $S_1, \ldots, S_n$. $S_i$ receives $(sk_i, pp)$ and initializes a set of records $rec_i := \emptyset$, for $i \in [n]$.

**Registration Phase.**

- **SignUp**($C, pwd$) $\rightarrow$ {$ (C, \text{msg}_i) $ $i \in [n]$}. It takes as inputs a client id $C$ and a password $pwd \in P$, and outputs a message for each server.

- **Store**($C, \text{msg}_i$) := $R_{i,C}$. It takes as input a client id $C$ and a message $\text{msg}_i$, and outputs a record $R_{i,C}$. $S_i$ stores $(C, R_{i,C})$ in its list of records $\text{REC}_i$ if no record for $C$ exists; otherwise, it does nothing.

**Sign-On Phase.**

- **Request**($C, pwd, x, T$) $\rightarrow$ ($\hat{D}, \{(C, x, \text{req}_i)\}_{i \in T}$). It takes as inputs a client id $C$, a password $pwd$, a value $x$, and a set $T \subseteq [n]$, and outputs a secret state $\hat{D}$ and request messages $\{\text{req}_i\}_{i \in T}$. For $i \in T$, $(C, x, \text{req}_i)$ is sent to $S_i$.

- **Respond**($sk_i, \text{REC}_i, C, x, \text{req}_i$) $\rightarrow$ $\text{res}_i$. It takes as inputs the secret key share $sk_i$, the record set $\text{REC}_i$, a client id $C$, a value $x$ and a request message $\text{req}_i$, and outputs a response message $\text{res}_i$.

- **Finalize**($\hat{D}, \{\text{res}_i\}_{i \in T}$) $=: \text{tk}$. It takes as input a secret state $\hat{D}$ and response messages $\{\text{res}_i\}_{i \in T}$, and outputs a token $\text{tk}$. 
CHAPTER 5. PASSWORD-BASED THRESHOLD AUTHENTICATION

Verification.

- Verify(vk, C, x, tk) → {0, 1}. It takes as inputs the verification key vk, a client id C, a value x and a token tk, and outputs 1 (denotes validity) or 0.

Correctness. For all κ ∈ N, any n, t ∈ N such that t ≤ n, any password space P, all (|sk⟩, vk, pp) generated by Setup(1κ, n, t, P), any client id C, any password pwd ∈ P, any value x, and any T ⊆ [n] of size at least t, if

- ((C, msg1), ..., (C, msgn)) ← SignUp(C, pwd),
- Ri,C := Store(C, msgi) for i ∈ [n],
- (D, {(C, x, reqi)}i∈T) ← Request(C, pwd, x, T),
- resi ← Respond(sk, rec, C, x, reqi) for i ∈ T, and
- tk := Finalize(D, {resi}i∈T),
then Verify(vk, C, x, tk) = 1.

5.4.2 Security Properties

We define security properties for PbTA with the help of a security game, described in Figure 5.10 in detail. In the security game, an adversary A gets access to a number of oracles, which run PbTA algorithms and do some bookkeeping. We do not allow the adversary to interfere with the registration phase. We assume that registration happens over secure channels. In practice, a client would establish a TLS connection with the servers over which it will send the sign-up messages. (Thus, the actual number of rounds in registration could be several.) The sign-on phase, however, is completely under the control of the adversary. Adversary can insert, delete or modify messages sent between clients and servers, even if client/server is not corrupt. This is captured by providing A access to three oracles for the three different algorithms of the sign-on phase (as opposed to just one oracle for registration). A can give any input to these oracles.

At the start of the game, GlobalSetup is run to generate shares of the master secret, verification key, public parameters and decryption keys. Public parameters are given to A. It outputs the set of servers U it wants to corrupt and the client C* it wants to target.

A number of variables are initialized before A is given access to the oracles. V keeps track of clients as they are corrupted in the game, through OC corrupt oracle. PwdList stores clients’ passwords in the form of (id, password) pairs, indexed by id, as they sign-up. ReqListC,i

2During a run of an oracle, if an algorithm does not produce a valid output, then the oracle stops immediately and returns ⊥. We do not make this explicit in Figure 5.10 for simplicity.
SecGame_{Π,A}(1^κ, n, t, P):
• ([sk], vk, pp, (SK_1, …, SK_n)) $\xleftarrow{\$}$ GlobalSetup(1^κ, n, t, P)
• (U, C*, st_{adv}) $\xleftarrow{\$}$ A(pp)
• $\mathcal{V} := \emptyset$
• PwdList := \emptyset
• ReqList_{C,i} := \emptyset for $i \in [n]$
• ct := 0, LiveSessions = []
• TokList := \emptyset
• $Q_{C,i} := 0$ for all $C$ and $i \in [n]$
• $Q_{C,x} := 0$ for all $C$ and $x$
• out $\xleftarrow{\$}$ $A^O((\{sk_i\}_{i \in U}, \{SK_i\}_{i \in U}, st_{adv})$

$O_{\text{corrupt}}(C)$.
• $\mathcal{V} := \mathcal{V} \cup \{C\}$
• if $(C, \star) \in \text{PwdList}$, return PwdList[C]

$O_{\text{register}}(C)$.
• require: PwdList[C] = ⊥
• pwd $\xleftarrow{\$}$ P
• add $(C, \text{pwd})$ to PwdList
• $((C, \text{msg}_1), \ldots, (C, \text{msg}_n)) \xleftarrow{\$}$ Signup(C, pwd)
• $R_{i,C} := \text{Store}(C, \text{msg}_i)$ for all $i \in [n]$
• add $R_{i,C}$ to Rec_i for all $i \in [n]$

$O_{\text{req}}(C, x, T)$.
• require: PwdList[C] ≠ ⊥
• $(\hat{D}, \{\text{req}_i\}_{i \in T}) \xleftarrow{\$}$ Request(C, PwdList[C])
• LiveSessions[ct] := $\hat{D}$
• add req_i to ReqList_{C,i} for $i \in T$
• increment ct by 1
• return $\{\text{req}_i\}_{i \in T}$

$O_{\text{resp}}(i, C, x, \text{req}_i)$.
• res_i $\xleftarrow{\$}$ Respond($sk_i, \text{Rec}_i, C, x, \text{req}_i$)
• if req_i $\notin$ ReqList_{C,i}, increment $Q_{C,i}$ by 1
• increment $Q_{C,x}$ by 1
• return res_i

$O_{\text{final}}(ct, \{res_i\}_{i \in S})$.
• $\hat{D} := \text{LiveSessions}[ct]$
• tk := Finalize($\hat{D}, \{res_i\}_{i \in S}$)
• add tk to TokList
• return tk

$O_{\text{verify}}(C, x, tk)$.
• return Verify(vk, C, x, tk)

Figure 5.10: Security game for PbTA.
stores the requests generated by \( \mathcal{C} \) for the \( i \)-th server. These requests will not be counted against the adversary, as we will see later.

\( \mathcal{O}_{\text{req}}(\mathcal{C}, x, \mathcal{T}) \) allows \( \mathcal{A} \) to start a sign-on session of \( \mathcal{C} \)—who may not be corrupt—with servers in \( \mathcal{T} \) to generate a token on \( x \). \( \mathcal{O}_{\text{req}} \) runs \texttt{Request} to generate request messages, using the password of \( \mathcal{C} \) stored in \texttt{PwdList}. While these messages are revealed to \( \mathcal{A} \), \( \mathcal{C} \)'s intermediate state \( \hat{D} \) is stored in \texttt{LiveSessions} at position \( \text{ct} \). \( \mathcal{A} \) can resume this sign-on session at any point in the future by invoking \( \mathcal{O}_{\text{final}} \) with \( \text{ct} \) and any arbitrary responses from the servers in \( \mathcal{T} \).

\( \mathcal{O}_{\text{resp}} \) can be invoked to get responses from a server as part of the sign-on phase. \( \mathcal{A} \) can invoke \( \mathcal{O}_{\text{resp}} \) with any message \((\mathcal{C}, x, \text{req}_i)\) of its choice. \( \mathcal{O}_{\text{server}} \) does not check if \( \text{req}_i \) was indeed generated by \( \mathcal{C} \) or not; a response is generated anyway, and returned to the adversary. However, if the request \( \text{req}_i \) was not generated by \( \mathcal{C} \) before, then this could give some advantage to \( \mathcal{A} \) in attacking \( \mathcal{C} \); so we increment a counter \( Q_{\mathcal{C}, i} \) in this case. A different counter \( Q_{\mathcal{C}, x} \) is incremented even if \( \text{req}_i \) was generated by \( \mathcal{C} \). This is just to count the number of times different servers are invoked on \( \mathcal{C} \) and \( x \). If this number is less than even \( t - |\mathcal{U}| \), then \( \mathcal{A} \) should not be able to generate a token on \((\mathcal{C}, x)\) except with negligible probability (see Def. 5.4.3, first point).

Note that the counters \( Q \) are separate for each client. If \( \mathcal{O}_{\text{server}} \) is invoked with a certain client id, then the counters for just that id are updated. When we define the security properties for PbTA below, only the counters for \( \mathcal{C}^* \) (the target client) are taken into account. Thus, we consider \( \mathcal{A} \) to be attacking \( \mathcal{C}^* \) only when it reveals this id to the servers. In other words, we do not allow \( \mathcal{A} \) to gain any advantage in attacking \( \mathcal{C}^* \) if it pretends to be someone else.

\( \mathcal{O}_{\text{final}} \), as mentioned before, can be used to resume a sign-on session. Client’s state \( \hat{D} \) is retrieved from \texttt{LiveSessions}, and \texttt{Finalize} is run on \( \hat{D} \) and the server responses given as input. \( \mathcal{A} \) can provide any arbitrary response on behalf of any server—even the ones that are not corrupt. The token generated through \texttt{Finalize} is given to \( \mathcal{A} \) and added to \texttt{TokList}. Finally, \( \mathcal{A} \) can use \( \mathcal{O}_{\text{verify}} \) to check if a token is valid or not.

We are now ready to formally state the two security properties we would like any PbTA scheme to satisfy.

**Definition 5.4.2 (Password Safety).** A PbTA scheme \( \Pi \) is password safe if for all \( n, t \in \mathbb{N}, t \leq n \), all password space \( \mathcal{P} \) and all PPT adversary \( \mathcal{A} \) in \texttt{SecGame}_{\Pi,\mathcal{A}}(1^\kappa, n, t, \mathcal{P}) \) (Figure 5.10), there exists a negligible function \( \text{negl} \) s.t.

\[
\Pr[\mathcal{C}^* \notin \mathcal{V} \land \text{out} = \text{PwdList}[\mathcal{C}^*] \neq \bot] \leq \frac{\text{MAX} |d| t(Q_{\mathcal{C}^*,1}, \ldots, Q_{\mathcal{C}^*,n}) + 1}{|\mathcal{P}|} + \text{negl}(\kappa). \tag{5.5}
\]

To get some intuition into the above definition, consider the following attack. \( \mathcal{A} \) guesses a password for \( \mathcal{C}^* \), generates request messages on its own (so that it knows the intermediate state), invokes \( \mathcal{O}_{\text{resp}} \) to get the corresponding responses, combines them using \texttt{Finalize} to get a token, and finally checks if the token is valid or not. If the password guess was correct, then the token would be valid by the correctness property of PbTA.
As such, $\mathcal{A}$ is not restricted to attacking a PbTA scheme in the above manner. However, we require that, essentially, this is the best it can do. $\text{MAX}_{\mathcal{A}, t}(Q_{C^*, 1}, \ldots, Q_{C^*, n})$ in Eq. 5.5 gives a bound on the number of password attempts $\mathcal{A}$ can make through the above attack.

We do not penalize $\mathcal{A}$ for just replaying the requests generated by $C$ itself by not incrementing $Q_{C, i}$ in those cases. The additive factor of 1 captures the possibility that $\mathcal{A}$ can output a new guess at the end of the game (similar to the obliviousness property for TOPRF, see Def. 5.3.3).

**Definition 5.4.3** (Unforgeability). A PbTA scheme $\Pi$ is unforgeable if for all $n, t \in \mathbb{N}, t \leq n$, all password space $P$ and all PPT adversary $\mathcal{A}$ in SecGame$_{\Pi, A}(1^\kappa, n, t, P)$ (Figure 5.10), there exists a negligible function $\text{negl}$ s.t.

- if $Q_{C^*, x^*} < t - |\mathcal{U}|$,
  \[ \Pr[\text{Verify}(vk, C^*, x^*, tk^*) = 1] \leq \text{negl}(\kappa); \] \hspace{1cm} (5.6)

- else
  \[ \Pr[C^* \not\in \mathcal{V} \land tk^* \not\in \text{TokList} \land \text{Verify}(vk, C^*, x^*, tk^*) = 1] \leq \frac{\text{MAX}_{\mathcal{A}, t}(Q_{C^*, 1}, \ldots, Q_{C^*, n})}{|P|} + \text{negl}(\kappa), \] \hspace{1cm} (5.7)

where $\mathcal{A}$’s output $\text{out}$ is parsed as $(x^*, tk^*)$.

The security game for unforgeability is the same as password-safety (Figure 5.10) but $\mathcal{A}$ produces a token $tk^*$ now. The probability of it being valid on $(C^*, x^*)$ depends on several cases. First, if the value of $Q_{C^*, x^*}$ is smaller than even $t - |\mathcal{U}|$, then $\mathcal{A}$ didn’t even contact enough servers on $(C^*, x^*)$. So we would like its probability of producing a valid token to be negligible. (Eq. 5.6 also captures that querying servers on $(C, x)$ for a different $C$ or $x$ than $C^*$ and $x^*$ should not help.)

If $\mathcal{A}$ does contact enough servers and $C^*$ was corrupted, then $\mathcal{A}$ can easily generate a valid token; so this case is not interesting. However, if $C^*$ is not corrupt but $\mathcal{A}$ is able to guess its password, then it can also produce a valid token (with respect to $C^*$ only). Comparing Eq. 5.7 and 5.5, one can see that unforgeability property basically requires that this is the best $\mathcal{A}$ can do.

### 5.4.3 PASTA: Our Construction

In this section we present PASTA, our framework for building PbTA schemes. PASTA provides a way to combine any threshold token generation scheme (TTG) and any threshold oblivious PRF (TOP) in a black-box way to build a PbTA scheme that provides strong password-safety and unforgeability guarantees. Figure 5.11 provides a complete description of the framework.
PASTA uses the two main underlying primitives, TTG and TOP, in a fairly light-weight manner. The sign-on phase, which consists of Request, Respond and Finalize, does not add any public-key operations on top of what the primitives may have. Request runs TOP.Encode once; Respond runs both TOP.Eval and TTG.PartEval, but only once each; and, Finalize runs TOP.Combine and TTG.Combine once each. Even though number of decryptions in Finalize is proportional to $t$, these operations are very fast symmetric-key operations. Thus, PASTA makes minimal use of the two primitives that it builds on and its performance is mainly governed by the efficiency of these primitives.

PASTA needs a key-binding symmetric-key encryption scheme so that when a ciphertext is decrypted with a wrong key, decryption fails [Fis99]. Key-binding can be obtained very efficiently in the random oracle model, for e.g., by appending a hash of the secret key to every ciphertext.

For the sign-on phase, PASTA assumes that the servers communicate to clients over authenticated channels so that an adversary cannot send arbitrary messages to a client on behalf of honest servers. PASTA does not assume that these channels provide any confidentiality. Observe that if there is an authenticated channel in the other direction, namely the servers can identity the sender of every message they receive, then passwords are not needed, and hence a PbTA scheme is moot.

An important feature of PASTA, especially from the point of view of proving security, is that the use of TOP and TTG overlaps very slightly. The output of TOP is used to encrypt the partial evaluations of TTG but, apart from that, they operate independently. Thus, even if TTG is broken in some manner, it would not affect the safety of clients’ passwords. Furthermore, even if TOP is broken, a threshold number of servers would still be needed to generate a token. However, PASTA must prevent against several other attack scenarios, as captured by the game in Figure 5.10. The formal security guarantee of PASTA is stated as follows.

**Theorem 5.4.4** (Security of PASTA). If TTG is an unforgeable threshold token generation scheme (Def. 5.2.3), TOP is an unpredictable (Def. 5.3.2) and oblivious (Def. 5.3.3) TOPRF, and SKE is a key-binding CPA-secure symmetric-key encryption scheme, then the PbTA scheme PASTA as described in Figure 5.11 is password-safe (Def. 5.4.2) and unforgeable (Def. 5.4.3) when Hash is modeled as a random oracle.

### 5.4.4 Security Proof

Password-safety and unforgeability properties are proved in Sections 5.4.4.1 and 5.4.4.2 respectively.

#### 5.4.4.1 Password Safety

PASTA’s password safety primarily relies on the obliviousness of TOP. Intuitively, if we use a TOPRF on clients’ passwords, then obliviousness of TOPRF would make it hard to guess
Ingredients:

- A threshold token generation scheme $TTG := (TTG.Setup, TTG.PartEval, TTG.Combine, TTG.Verify)$.
- A threshold oblivious PRF $TOP := (TOP.Setup, TOP.Encode, TOP.Eval, TOP.Combine)$.
- A symmetric-key encryption scheme $SKE := (SKE.Encrypt, SKE.Decrypt)$.
- A hash function $Hash$.

GlobalSetup($1^n, n, t, P$) $\rightarrow ([sk], vk, pp)$:

- Run $TTG.Setup(1^n, n, t)$ to get $([tsk], tvk, tpp)$.
- Set $sk_i := tsk_i$ for all $i \in [n]$, $vk := tvk$ and $pp := (k, n, t, P, tpp)$.

SignUp($C, pwd$) $\rightarrow ((C, msg_1), \ldots, (C, msg_n))$:

- Run $TOP.Setup(1^n, n, t)$ to get $([k], opp)$.
- Compute $h := TOP(k, pwd)$ and $h_i = Hash(h\|i)$ for $i \in [n]$.
- Set $msg_i := (k_i, h_i)$ for $i \in [n]$.

Store($SK_i, C, msg_i$) $:= R_{i,C}$:

- Parse $msg_i$ as $(k_i, h_i)$.
- Set $R_{i,C} := (k_i, h_i)$

Request($C, pwd, x, T$) $\rightarrow \{(C, x, req_i)\}_{i \in T}, \hat{D}$.

- If $|T| < t$, output $\perp$.
- Pick a $\rho$ at random. Run $TOP.Encode(pwd, \rho)$ to get $c$.
- Set $req_i := c$ for all $i \in [n]$ and $\hat{D} := (C, pwd, \rho, T)$.

Respond($sk_i, rec_i, C, x, req_i$) $\rightarrow res_i$:

- If $R_{i,C} \notin rec_i$, output $\perp$. Else, parse $R_{i,C}$ as $(k_i, h_i)$.
- Run $TOP.Eval(k_i, req_i)$ to get $z_i$.
- Run $TTG.PartEval(tsk_i, C\|x)$ to get $y_i$.
- Set $res_i := (z_i, SKE.Encrypt(h_i, y_i))$.

Finalize($\hat{D}, \{res_i\}_{i \in S}$) $\rightarrow tk$.

- Parse $res$, as $(z_i, ctxt_i)$ and $\hat{D}$ as $(C, pwd, \rho, T)$.
- If $S \neq T$, output $\perp$.
- Run $TOP.Combine(pwd, \{(i, z_i)\}_{i \in T}, \rho)$ to get $h$.
- For all $i \in T$, compute $h_i := Hash(h\|i)$ and $y_i := SKE.Decrypt(h_i, ctxt_i)$.
- Finally, set $tk$ to be $TTG.Combine(\{i, y_i\}_{i \in T})$.

(If any of $TOP.Combine$, $SKE.Decrypt$ or $TTG.Combine$ fail, $\perp$ is output.)

Verify($vk, C, x, tk$) $\rightarrow \{0, 1\}$.

- Output $TTG.Verify(tvk, C\|x, tk)$.

Figure 5.11: A complete description of PASTA.
CHAPTER 5. PASSWORD-BASED THRESHOLD AUTHENTICATION

they. Formally, we build an adversary $B$ that can translate an adversary $A$'s advantage in the password-safety game into a similar advantage in the TOPRF obliviousness game Obliviousness (Figure 5.7). $B$ will run $A$ internally simulating the password-safety for it, while playing the role of adversary externally in Obliviousness.

$B$ can implicitly set the targeted client $C^*$'s password to be the random value chosen in Obliviousness. If $A$ guesses the password, $B$ can output the same guess. However, to simulate SecGame properly for $A$, $B$ needs to run the oracles in a way that $A$ cannot tell the difference. In particular, $B$ needs partial TOPRF evaluations $z_i$ on the password for $O_{\text{resp}}$, the final TOPRF value for $O_{\text{register}}$ and the randomness $\rho$ used for encoding for $O_{\text{final}}$. $B$ can take help of the oracles $O_{\text{eval}}$ and $O_{\text{enc\&eval}}$ provided by Obliviousness to handle the first two problems, but there is no way to get $\rho$ in Obliviousness.

Intermediate Hybrid. We tackle the latter problem first by going through a hybrid. We refer to the original game as $H_0$ and the new game as $H_1$. $H_0$ is described in Figure 5.12; it basically replaces $\Pi$ in Figure 5.10 with PASTA. $H_1$ is described in Figure 5.13. In $H_1$, several oracles behave differently for the targeted client $C^*$. $O_{\text{req}}$ evaluates the TOPRF in advance for $C^*$. It stores the partial evaluations $z_i$ and the final result $h$ in LiveSessions itself. Importantly, it does not store $\rho$. When $O_{\text{resp}}$ is invoked, it checks if $C^*$ generated $\text{req}_i$ for $S_i$ before ($\text{req}_i \in \text{ReqList}_{C_i}$). If yes, then $z_i$ is picked up from LiveSessions. Now, whether a $z_i$ computed in advance is used in $O_{\text{resp}}$ or not makes no difference from the point of the adversary because $z_i$ is derived deterministically from $k_i$ and $\text{req}_i$.

Oracle $O_{\text{final}}$ also behave differently for $C^*$. First, note that if $\text{TOP.PubCombine}\{z_i\}_{i \in \mathcal{T}}$ is equal to $\text{TOP.PubCombine}\{z'_i\}_{i \in \mathcal{T}}$, then combining either set will lead to the same value. The only difference in $H_1$ is that $h$ was computed beforehand. Once again, for the same reason as above, this makes no difference.

The crucial step where $H_0$ and $H_1$ differ is when the two outputs of PubCombine do not match. While $H_0$ does not do any test of this kind, $H_1$ simply outputs $\bot$. For these hybrids to be indistinguishable, we need to argue that had the outputs of PubCombine not matched in $H_0$, it would have output $\bot$ as well (at least with a high probability).

Note that the right $z_i$ and $h$ are well-defined for $H_0$; they can be derived from $\text{pwd}$ and $\rho$. If one were to do the public combine test in this hybrid and it fails, then $h' \neq h$ with high probability. Therefore, using the collision resistance of Hash, one can argue that $h'_i \neq h_i$. Now, observe that there must be an honest $S_j$ in $\mathcal{T}$, so $\text{ctxt}'_{j_i}$ could only have been generated by $S_j$ (recall our authenticated channels assumption). When ciphertext $\text{ctxt}'_{j_i}$, which was encrypted under $h_j$, is decrypted with $h'_j \neq h_j$, decryption fails with high probability due to the key-binding property of SKE. Thus, $H_0$ returns $\bot$ just like $H_1$.

Reduction. Now that we know that absence of encoding randomness $\rho$ would not prevent a successful simulation of $O_{\text{final}}$, we come back to the task of exploiting TOPRF obliviousness to hide the targeted client's password. Towards this, adversary $B$ is formally described in Figure 5.14. When $B$ outputs a message, it should be interpreted as sending the message to
the obliviousness game. Let’s now go through the differences between $H_1$ and $B$’s simulation of it one by one.

Simulation of $O_{\text{register}}$ differs only for $C = C^*$. In $H_1$, a randomly chosen password for $C^*$ is used to compute $h$, while in $B$’s simulation, $C^*$’s password is implicitly set to be the random input $\tilde{x}$ chosen by $\text{Obliviousness}$ and $O_{\text{enc\&eval}}$ is called to get $h$. Clearly, this difference does not affect $A$. There is one other difference though: while all of $k_1, \ldots, k_n$ are known in $H_1$, $B$ knows $k_i$ for corrupt servers only. As a result, $B$ defines $R_{i, C^*}$ to be $(0, h_i)$ for $i \in [n] \setminus U$.

Like the registration oracle, request oracle behaves differently only when $C = C^*$. However, one can easily see that the difference is insignificant: while $H_1$ computes $c$, $z_i$ and $h$ using $\text{PwdList}[C^*]$, $B$ invokes $O_{\text{enc\&eval}}$ to get them, which uses $\tilde{x}$.

Finally, $B$ invokes $O_{\text{eval}}$ to get $z_i$ in the simulation of $O_{\text{resp}}$ (because it does not know $k_i$ for honest servers) but it is computed directly in $H_1$. This does not make any difference either. The important thing to note is that the counter $Q_{C^*, i}$ is incremented if and only if the counter $q_i$ of $\text{Obliviousness}$ is incremented. As a result, the final value of $Q_{C^*, i}$ will be the same as $q_i$. Therefore, $B$ will successfully translate $A$’s probability of guessing $C^*$’s password to guessing $\tilde{x}$.

### 5.4.4.2 Unforgeability

First we handle the easier case of $Q_{C^*, x^*} < t - |U|$. Here $C^*$ could even be corrupt, so $A$ may know its password. Note that $Q_{C^*, x^*}$ is incremented on every invocation of $O_{\text{resp}}(i, C^*, x^*, \text{req}_i)$ irrespective of the value of $i$ and whether or not $\text{req}_i \in \text{ReqList}_{i, C^*}$. So if $Q_{C^*, x^*} < t - |U|$, $A$ simply doesn’t have enough shares to generate a valid token, irrespective of whether $C^*$ is corrupt or not. One can formally prove unforgeability in this case by invoking the unforgeability of the threshold token generation scheme $\text{TTG}$ (Definition 5.2.3). We skip the details.

When $Q_{C^*, x^*} \geq t - |U|$, unforgeability can only be expected when $C^*$ is never corrupted. We need to show that generating a valid token for $(C^*, x^*)$ for any $x^*$ effectively amounts to guessing $C^*$’s password. Indistinguishability of $H_0$ (Figure 5.12) and $H_1$ (Figure 5.13) still holds because it just relies on the properties of $\text{PubCombine}$ and authenticated channels.

We now wish to build an adversary $B'$ that can use an adversary $A$ who breaks the unforgeability guarantee of PASTA to break the unpredictability of TOPRF. The first natural question to ask is whether $B'$ can break unpredictability of TOPRF in the same way as $B$ broke obliviousness. Not quite, because there are some key differences in the two settings:

- Even though both $B$ and $B'$ get access to an oracle $O_{\text{enc\&eval}}$ that both encodes and evaluates, $B$’s oracle returns the final TOPRF output $h$ while $B'$’s oracle doesn’t. So it is not clear how $h_i$ will be generated by $O_{\text{register}}$ and $O_{\text{final}}$ for $C^*$.

- $B$ was able to use the output of $A$ for the password-safety game directly into the obliviousness game, but $B'$ cannot. $A$ now outputs a token for the authentication
SecGame\textsubscript{PASTA, A}(1\textsuperscript{κ}, n, t, P):

- \([\langle \text{tsk} \rangle, \text{tvk}, \text{tp} \rangle \xleftarrow{\$} \text{TTG.Setup}(1\textsuperscript{κ}, n, t)\)
- set \(\text{sk}_{ii} := \text{tsk}_{ii}\) for all \(i \in [n]\), \(\text{vk} := \text{tvk}\) and \(\text{pp} := (\kappa, n, t, \text{tp})\).
- \((U, C^*, \text{st}_{\text{adv}}) \xleftarrow{\$} \mathcal{A}(\text{pp})\)
- \(\mathcal{V}, \text{PwdList}, \text{TokList} := \emptyset, \text{ReqList}_{C,i} := \emptyset\) for all \(i \in [n]\)
- \(\text{ct} := 0, \text{LiveSessions} = \emptyset\)
- \(Q_{C,i}, Q_{C,x} := 0\) for all \(C, i \in [n]\) and \(x\)
- \(\text{out} \xleftarrow{\$} \mathcal{A}(\mathcal{O})(\langle \text{sk}_{ii} \rangle_{i \in U}, \langle \text{SK}_{i} \rangle_{i \in U}, \text{st}_{\text{adv}})\)

\(\mathcal{O}_{\text{corrupt}}(C)\).
- if \((C, *) \in \text{PwdList}, \text{return PwdList}[C]\)

\(\mathcal{O}_{\text{register}}(C)\).
- require: \(\text{PwdList}[C] = \perp\)
- \(\text{pwd} \xleftarrow{\$} P\)
- add \((C, \text{pwd})\) to \(\text{PwdList}\)
- \((\frac{\langle \text{opp} \rangle}{\mathcal{U}}) \xleftarrow{\$} \text{TOP.Setup}(1\textsuperscript{κ}, n, t)\)
- \(h_{\text{\textsuperscript{opp}}} := \text{TOP}(\text{opp}, \text{pwd})\) and \(h_{i} := \text{Hash}(h_{\text{\textsuperscript{opp}}}i)\) for all \(i \in [n]\)
- \(R_{i, C} := (k_{i}, h_{i})\) for all \(i \in [n]\)
- add \(R_{i, C}\) to \(\text{Rec}_{i}\) for all \(i \in [n]\)

\(\mathcal{O}_{\text{req}}(C, x, T)\).
- if \(\text{PwdList}[C] = \perp\) or \(|T| < t\), output \(\perp\)
- \(c := \text{TOP.Encrypt}(\text{PwdList}[C], \rho)\) for a random \(\rho\)
- \(\text{set req}_{i} := c\) for all \(i \in [n]\)
- \(\text{LiveSessions}[\text{ct}] := (C, \text{PwdList}[C], \rho, T)\)
- add \(\text{req}_{i}\) to \(\text{ReqList}_{C,i}\) for all \(i \in T\)
- increment \(\text{ct}\) by 1
- return \(\langle \text{req}_{i} \rangle_{i \in T}\)

\(\mathcal{O}_{\text{resp}}(i, C, x, \text{req}_{i})\).
- require: \(i \in [n] \setminus U\)
- if \(R_{i, C} \notin \text{Rec}_{i}\), return \(\perp\); else, parse \(R_{i, C}\) as \((k_{i}, h_{i})\)
- if \(\text{req}_{i} \notin \text{ReqList}_{C,i}\), increment \(Q_{C,i}\) by 1
- \(z_{i} := \text{TOP.Eval}(k_{i}, \text{req}_{i})\)
- \(y_{i} \xleftarrow{\$} \text{TTG.PartEval}(\text{tsk}_{i}, C|x)\)
- set \(\text{res}_{i} := (z_{i}, \text{SKE.Encrypt}(h_{i}, y_{i}))\)
- increment \(Q_{C,x}\) by 1
- return \(\text{res}_{i}\)

\(\mathcal{O}_{\text{mal}}(\text{ct}, \langle \text{res}_{i} \rangle_{i \in S})\).
- \(D := \text{LiveSessions}[\text{ct}]\)
- parse \(\text{res}_{i}\) as \((z_{i}', \text{ctxt}'_{i})\) and \(D\) as \((C, \text{pwd}, \rho, T)\).
- if \(S \neq T\), output \(\perp\)
- \(h' := \text{TOP.Combine}(\text{pwd}, \langle (i, z_{i}') \rangle_{i \in T}, \rho)\)
- for \(i \in T\), \(h_{i}' := \text{Hash}(h'_{i})\) and \(y_{i}' := \text{SKE.Decrypt}(h_{i}', \text{ctxt}'_{i})\)
- set \(\text{tk} := \text{TTG.Combine}(\langle i, y_{i}' \rangle_{i \in T})\)
- add \(\text{tk}\) to \(\text{TokList}\)
- return \(\text{tk}\)

\(\mathcal{O}_{\text{verify}}(C, x, \text{tk})\).
- return \(\text{TTG.Verify}(\text{tvk}, C|x, \text{tk})\)

Figure 5.12: SecGame in hybrid \(H_{0}\) for PASTA.
SecGame\textsubscript{PASTA, A}(1^n, n, t, P):
Same as $H_0$, except the following oracles behave differently when $C = C^*$. Below, we describe their behavior for this case only, highlighting the differences in red. When $C \neq C^*$, they behave in the same way as $H_0$.

$O_{\text{req}}(C^*, x, T)$.
- if $\text{PwdList}[C^*] \neq \perp$ or $|T| < t$, output $\perp$
- $c := \text{TOP.\text{Encode}}(\text{PwdList}[C^*], \rho)$ for a random $\rho$
- set $\text{req}_i := c$ for $i \in [n]$
- $z_i := \text{TOP.\text{Eval}}(k_i, \text{req}_i)$
- $h := \text{TOP.\text{Combine}}(\text{PwdList}[C^*], \{(i, z_i)\}_{i \in T}, c)$
- $\text{LiveSessions}[ct] := (C^*, c, \{(i, z_i)\}_{i \in T}, h)$
- add $\text{req}_i$ to ReqList$_C$, for $i \in T$
- increment $ct$ by 1
- return $\{\text{req}_i\}_{i \in T}$

$O_{\text{resp}}(i, C^*, x, \text{req}_i)$.
- require: $i \in [n] \setminus \mathcal{U}$
- if $R_{i,C^*} \notin \text{rec}_i$, return $\perp$; else, parse $R_{i,C^*}$ as $(k_i, h_i)$
- if $\text{req}_i \notin \text{ReqList}_{C^*, i}$, increment $Q_{C^*, i}$ by 1
- if $(\text{req}_i \notin \text{ReqList}_{C^*, i})$
  - $z_i := \text{TOP.\text{Eval}}(k_i, \text{req}_i)$
- else:
  - let $z_i$ be the value associated with $i$ in the entry $(C^*, \text{req}_i, \ldots, (i, z_i) \ldots)$ in LiveSessions
- $y_i \leftarrow \text{TTG.\text{PartEval}}(\text{tsk}_i, C^* || x)$
- set $\text{res}_i := (z_i, \text{SKE.\text{Encrypt}}(h_i, y_i))$
- increment $Q_{C^*, x}$ by 1
- return $\text{res}_i$

$O_{\text{final}}(ct, \{\text{res}_i\}_{i \in S})$.
- $(C^*, c, \{(i, z_i)\}_{i \in T}, h) := \text{LiveSessions}[ct]$
- parse $\text{res}_i$ as $(z'_i, \text{ctxt}'_i)$
- if $S \neq T$, output $\perp$
- if $(\text{TOP.\text{PubCombine}}(\{z_i\}_{i \in T}) \neq \text{TOP.\text{PubCombine}}(\{z'_i\}_{i \in T}))$:
  - return $\perp$
  - for $i \in T$, $h'_i := \text{Hash}(h_i || i)$ and $y'_i := \text{SKE.\text{Decrypt}}(h'_i, \text{ctxt}'_i)$
  - set $\text{tk} := \text{TTG.\text{Combine}}(\{i, y'_i\}_{i \in T})$
  - add $\text{tk}$ to TokList
- return $\text{tk}$

Figure 5.13: SecGame in hybrid $H_1$. 

Same as $H_1$, except the following oracles are simulated differently when $C = C^*$. Below, these oracles are described for this case only, with the differences highlighted in red. Output whatever $A$ does.

$O_{\text{register}}(C^*)$:

- require: $\text{PwdList}[C^*] = \bot$
- add $(C^*, \text{unknown})$ to PwdList
- output $U$, get back $\{k_i\}_{i \in U}$
- query $O_{\text{enc\&eval}}$ to get $(c, \{z_i\}_{i \in [n]}, h)$
- $h_i := \text{Hash}(h \| i)$ for $i \in [n]$
- for $i \in [n] \setminus U$, $R_{i,C^*} := (0, h_i)$
- for $i \in U$, $R_{i,C^*} := (k_i, h_i)$
- add $R_{i,C^*}$ to $\text{rec}_i$ for all $i \in [n]$

$O_{\text{req}}(C^*, x, T)$:

- if $\text{PwdList}[C^*] = \bot$ or $|T| < t$, output $\bot$
- query $O_{\text{enc\&eval}}$ to get $(c, \{z_i\}_{i \in [n]}, h)$
- set $\text{req}_i := c$ for $i \in [n]$
- $\text{LiveSessions}[ct] := (C, c, \{i, z_i\}_{i \in T}, h)$
- add $\text{req}_i$ to $\text{ReqList}_{C^*, i}$ for $i \in T$
- increment $ct$ by 1
- return $\{\text{req}_i\}_{i \in T}$

$O_{\text{resp}}(i, C^*, x, \text{req}_i)$:

- require: $i \in [n] \setminus U$
- if $R_{i,C^*} \notin \text{rec}_i$, return $\bot$; else, parse $R_{i,C^*}$ as $(k_i, h_i)$
- if $\text{req}_i \notin \text{ReqList}_{C^*, i}$, increment $Q_{C^*, i}$ by 1
- if $(\text{req}_i \notin \text{ReqList}_{C^*, i})$:
  - query $O_{\text{eval}}(i, \text{req}_i)$ to get $z_i$
  - else:
    - let $z_i$ be the value associated with $i$ in the entry $(C^*, \text{req}_i, \ldots, (i, z_i), \ldots)$ in $\text{LiveSessions}$
    - $y_i \leftarrow \text{TTG} \cdot \text{PartEval}(\text{tsk}_i, C^* \| x)$
    - set $\text{res}_i := (z_i, \text{SKE}.\text{Encrypt}(h_i, y_i))$
    - increment $Q_{C^*, x}$ by 1
    - return $\text{res}_i$

Figure 5.14: Description of adversary $B$. 
scheme while $B'$ is supposed to guess the TOPRF output $h$ on the (hidden) password of $C^\star$.

As a result, $B'$’s behavior differs from $B$ in the following manner. At the start of the simulation, $B'$ picks random numbers $r_1, \ldots, r_n$ and will use them instead of $h_1, \ldots, h_n$ in the registration oracle. LiveSessions cannot contain $h$ anymore, so when it is needed in the finalize oracle, $r_1, \ldots, r_n$ will be used once again. If $A$ queries $\text{Hash}$ on $h'|i$ at any time, $B'$ will query $O_{\text{check}}$ on $h'$ to check if $h' = h$ or not. If $O_{\text{check}}$ returns 1, then $B'$ sends $r_i$ to $A$.

This also gives a way for $B'$ to guess $h$. If $A$ queries $\text{Hash}$ for some $h'||i$ and $O_{\text{check}}$ returns 1 on $h'$, then $B'$ just outputs $h'$ in the unpredictability game. However, we don’t have the guarantee that $A$ will make such a query. All we know is that $A$ can produce a valid token. Hence, we must argue that $A$ can produce a valid token only if it queries $\text{Hash}$ on $h$.

Any token share sent by the $i$-th server is encrypted with $h_i$. At a high level, $A$ needs to decrypt at least one token share from an honest server, say $j$-th, to construct a token. The only way to get this key is by querying $\text{Hash}$ on $h|j$.

## 5.5 Performance Evaluation

We implement PASTA for four types of threshold token generation schemes: a block-cipher based MAC [NPR99], a DDH-based (requires exponentiations) MAC [NPR99], a pairing based signature [Bol03] and an RSA based signature [Sho00]. In this section we report on the performance of these instantiations.

### 5.5.1 Implementation Details

PASTA is a generic construction consisting of two building blocks: a threshold oblivious pseudo-random function and a threshold token generation scheme. We implement PASTA with the 2HashTDH TOPRF protocol of Jarecki et al. [JKKX17] and the aforementioned TTG schemes (see Section 5.2.2 for their descriptions) to obtain four types of tokens. To the best of our knowledge, most of these TTG schemes were not implemented before.

We implement pseudorandom functions (PRFs) using AES-NI and hash functions using Blake2 [Blake2]. The elliptic curve operations, pairing operations, and RSA operations are implemented using the Relic library [Relic]. The key length in AES-NI is 128 bits. The cyclic group used in 2HashTDH TOPRF and the DDH based MAC is the group $G_1$ on 256-bit Barreto-Naehrig curves (BN-curves) [BN06]. Pairing is implemented on 256-bit BN-curves. The key length in RSA based signature is 2048 bits.

In order to evaluate the performance, we implement various settings described below. The experiments are run on a single server with 2x 24-core 2.2 GHz CPUs and 64 GB of RAM. We run all the parties on different cores of the same server (1 core per server), and simulate network connections using the Linux tc command: a LAN setting with 10 Gbps network bandwidth and 0.1 ms round-trip latency; a WAN setting with 40 Mbps network bandwidth and a simulated 80 ms round-trip latency.
5.5.2 Token Generation Time

Table 5.1 shows the total runtime for a client to generate a single token in the sign-on phase after registration in our PASTA protocol. We show experiments for various types of tokens in the LAN and WAN settings and different values of \((n, t)\) where \(n\) is the number of servers and \(t\) is the threshold. The reported time is an average of 10,000 token requests. We discuss a few observations below.

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<th>Public-MAC</th>
<th>Pairing-Sig</th>
<th>RSA-Sig</th>
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Table 5.1: Total runtime (in milliseconds) of our PASTA protocol for generating a single token for the number of servers \(n\) and threshold \(t\) in LAN and WAN settings.

Notice that for the same threshold \(t = 2\) and the same type of token, different values of \(n\) result in similar runtime. This is aligned with our construction: for a threshold \(t\), the client only needs to communicate with \(t\) servers, and the communication and computation cost for every server is the same, hence the total runtime should also be the same. Therefore, the total runtime is independent of \(n\) and only depends on the threshold \(t\). For other values of threshold \(t\), we only report the runtime for \(n = 10\); the runtime for other values of \(n\) would be roughly the same.

Also notice that for the same \((n, t)\) and same type of token, the runtime in the WAN setting is roughly the runtime in the LAN setting plus 80 ms round-trip latency. This is because in our protocol, the client sends a request to \(t\) servers and receive their responses in parallel. The communication complexity is very small, hence the bulk of communication overhead is roughly the round-trip latency. It is worth noting that the PASTA protocol has the minimal two rounds of interaction, and hence this overhead is inevitable in the WAN setting.
The runtime of public-key based MAC and pairing based signature are almost the same under the same setting. This is because in our implementation, TTG schemes for public-key based MAC and pairing based signature are both implemented on the 256-bit Barreto-Naehrig curves (BN-curves) [BN06] in group $G_1$. This group supports Type-3 pairing operation and is believed to satisfy the Decisional Diffie-Hellman (DDH) assumption, hence a good fit for both primitives.

We do not report the runtime for user registration because (i) it is done only once for every user and (ii) it is more efficient than obtaining a token.

5.5.3 Time Breakdown

We show the runtime breakdown for three different $(n, t)$ values in Table 5.2 in the LAN setting. For each value of $(n, t)$ in the table, the first row is the total runtime, and the second and third rows are the computation time on the client side and on a single server, respectively.

<table>
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<tr>
<th>$(n, t)$</th>
<th>Sym-MAC</th>
<th>Public-MAC</th>
<th>Pairing-Sig</th>
<th>RSA-Sig</th>
</tr>
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<tbody>
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<td>1.7</td>
<td>1.7</td>
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</tr>
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<td>Server</td>
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<td>10, 5</td>
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<tr>
<td>Server</td>
<td>0.2</td>
<td>0.4</td>
<td>0.4</td>
<td>11.5</td>
</tr>
</tbody>
</table>

Table 5.2: Breakdown of runtime (in milliseconds) in LAN setting.

As shown in the table, for the same token type the computation time on a single server does not vary. On the other hand, the computation on the client grows with the threshold. Figure 5.15 shows the dependence of the computation time at the client side on the threshold $t$. For all four types of tokens, the computation time grows linearly in the threshold $t$.

5.5.4 Comparison with Naïve Solutions

We implement two naïve solutions to compare with our PASTA protocol:

- **Plain Solution.** The client signs on to a single server with its username/password. The server verifies its credential and then issues an authentication token using a master secret key.
• **Threshold Solution.** This solution utilizes a threshold token generation scheme. The secret key shares $s_{k_1}, s_{k_2}, \ldots, s_{k_n}$ of the threshold scheme are distributed among the $n$ servers. The client signs on with its username/password to $t$ servers, where each server verifies its credential and then issues a share of the token. The client combines the shares received from the servers to generate the final token.

In the plain solution, a breached server would enable the attacker to (i) recover the master secret key and (ii) perform offline dictionary attacks to recover users’ passwords. Comparing our solution with the plain solution presents the extra cost of protecting both the master secret key and users’ passwords. In the threshold solution, if up to $t-1$ servers are breached, then the master secret key stays secure, but users’ passwords are vulnerable to offline dictionary attacks. Comparing our solution with the threshold solution gives the extra cost of protecting users’ passwords.

Table 5.3 shows the total runtime for a client to generate a single token after registration using the plain solution and the threshold solution for different values of $(n, t)$. The reported time is an average of 10,000 token requests in LAN and WAN settings. For the same setting and the same type of token, the runtime in the WAN network is roughly the runtime in the LAN network plus 80 ms round-trip latency, for the same reason discussed above for the PASTA protocol. Notice that in the threshold solution, the total runtime is independent of $n$ and only depends on the threshold $t$. Hence we only report the runtime for the same $n = 10$ and different thresholds.

We compare our solution with the two naïve solutions and show the multiplicative overhead of our solution in Figure 5.16. The two figures represent the comparison in the LAN
and WAN network, respectively. Different types of tokens are represented in different colors. In each picture, the first set of four bars represent the overhead of our solution compared to the plain solution. Note that there is no notion of \((n, t)\) in the plain solution, hence we pick a setting \((5, 3)\) for our solution to compare with the plain solution. If comparing the plain solution with other \((n, t)\) settings of our solution, the results would be slightly different. The remaining five sets of bars in each figure represent the overhead of our solution compared to the threshold solution for various values of \((n, t)\). When comparing with those, we use the same \((n, t)\) setting of our solution.

In the LAN network, notice that there is nearly no overhead for the RSA-based token generation. The overhead for public-key based MAC and pairing based signature is a small constant. There is a higher overhead for symmetric-key based MAC. This is because the naïve solutions only involve symmetric-key operations while our solution involves public-key operations, which is much more expensive. This overhead is necessary as we prove in Section 5.6 that public-key operations are necessary to achieve a password-based threshold authentication (PbTA) system.

In the WAN network, since the most time-consuming component is the network latency in our protocol as well as the naïve solutions, the overhead of our solution compared with the naïve solutions is fairly small. As shown in Figure 5.16, the overhead is less than 5% in all the settings and all token types.

<table>
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<tr>
<th></th>
<th>((n, t))</th>
<th>Sym-MAC</th>
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<th>Pairing-Sig</th>
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<td>0.4</td>
<td>0.4</td>
<td>11.3</td>
</tr>
<tr>
<td>(10, 2)</td>
<td></td>
<td>0.1</td>
<td>0.6</td>
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<tr>
<td>(10, 3)</td>
<td></td>
<td>0.1</td>
<td>0.6</td>
<td>0.6</td>
<td>13.3</td>
</tr>
<tr>
<td>(10, 5)</td>
<td></td>
<td>0.2</td>
<td>0.9</td>
<td>0.9</td>
<td>14.4</td>
</tr>
<tr>
<td>(10, 7)</td>
<td></td>
<td>0.3</td>
<td>1.2</td>
<td>1.2</td>
<td>16.0</td>
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<td></td>
<td></td>
<td></td>
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<tr>
<td>Plain</td>
<td></td>
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<td>80.5</td>
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<td>81.6</td>
<td>98.8</td>
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Table 5.3: Total runtime (in milliseconds) for generating a single token through naïve solutions, for various settings in LAN and WAN networks.
5.6 Necessity of Public-Key Operations

In both the registration phase and sign-on phase of PASTA, we instantiate the TOPRF component with the 2HashTDH protocol of Jarecki et al. [JKKX17] which uses public-key operations. Therefore, all the instantiations of PASTA use public-key operations even if the threshold token generation scheme is purely symmetric-key. This could become a significant overhead in some cases compared to the naïve insecure solutions (see Section 5.5.4 for details).
So the natural question is whether public-key operations can be avoided, or, put differently, is it just an artifact of PASTA and its instantiations? In this section we prove that public-key operations are indeed necessary to construct any secure PbTA scheme.

In more detail, we prove that if one can construct PbTA that only makes black-box use of one-way functions, then a secure two-party key agreement protocol can also be constructed by only making black-box use of one-way functions, which would imply \( P \neq NP \) [IR90]. Hence this gives us evidence that it is unlikely to construct PbTA using only symmetric-key operations.

**Overview.** We now give an overview of our proof technique. At a high level, we construct a secure key-agreement protocol from PbTA in a black-box way. As a result, if one can construct PbTA that only makes black-box use of one-way functions, then our construction is a secure key-agreement protocol that only makes black-box use of one-way functions, contradicting the impossibility result of Impagliazzo and Rudich [IR90].

To construct the secure key-agreement protocol, think of the two parties \( P_1 \) and \( P_2 \) in the key-agreement protocol as a client \( C \) and the set of all servers in the PbTA protocol, respectively. We set the password space to contain only one password \( \text{pwd} \), which means the password of \( C \) is known to both parties. Thus \( P_2 \), which represents the set of all servers, can run \texttt{GlobalSetup} and the registration phase of \( C \) on its own. Then the two parties run the sign-on phase so that \( P_1 \) obtains a token for \( x = 0 \). Since both parties know the password, \( P_2 \) can emulate the sign-on phase on its own to generate a token for \( x = 0 \). The resulting token is the agreed key by both parties.

Notice that the generated token might not be a valid output for the key agreement protocol, but the two parties can apply randomness extractor to the token and obtain randomness to generate a valid key. We omit the details here.

The security of the key-agreement protocol relies on the unforgeability of the PbTA scheme. Intuitively speaking, if there exists a PPT adversary that outputs the agreed token by \( P_1 \) and \( P_2 \) with non-negligible probability, then the adversary is able to generate a valid token in the PbTA scheme with non-negligible probability, without making any fake requests to the servers (thus keeping all \( Q_{C,i} \) to zero), contradicting the unforgeability property. Next we give provide a formal proof.

**Theorem 5.6.1.** A secure two-party key agreement protocol can be constructed from any PbTA scheme in a black-box way.

**Proof.** Let \( \Pi = (\texttt{GlobalSetup}, \texttt{SignUp}, \texttt{Request}, \texttt{Respond}, \texttt{Finalize}, \texttt{Verify}) \) be a PbTA scheme. The secure two-party key agreement protocol is presented in Figure 5.17.

The protocol uses PbTA in a black-box way. Since the tokens \( tk, tk' \) are generated using the same \( C, x, \) and secret key, they are equivalent. Hence the two parties agree on a token (which can be used to extract randomness to generate a key). Now we show that if there exists a PPT adversary \( \mathcal{A} \) that outputs the agreed token by \( P_1, P_2 \) in the key-agreement
Let the password space be \( P := \{ \text{pwd} \} \), set \( n := 2, t := 2 \), let \( \mathcal{T} \) be the set of all servers in \( \Pi_{\text{sym}} \), and set \( x := 0 \).

1. \( P_2 \) executes the following:
   - Run \( \text{GlobalSetup}(1^\kappa, n, t, P) \rightarrow ([\text{sk}], \text{vk}, \text{pp}) \).
   - Run \( \text{SignUp}(C, \text{pwd}) \rightarrow ((C, \text{msg}_1), \ldots, (C, \text{msg}_n)) \).
   - Set \( R_{i,C} := \text{msg}_i \).
   - Send \((\text{pp}, C)\) to \( P_1 \).

2. On receiving the first message from \( P_2 \), \( P_1 \) does the following:
   - Run \( \text{Request}(C, \text{pwd}, x, \mathcal{T}) \rightarrow ((C, x, \text{req}_i)_{i \in \mathcal{T}}, \hat{D}) \).
   - Send \( \{\text{req}_i\}_{i \in \mathcal{T}} \) to \( P_2 \).

3. On receiving the message from \( P_1 \), \( P_2 \) does the following:
   - Run \( \text{Respond}(\text{sk}_i, \text{rec}_i, C, x, \text{req}_i) \rightarrow \text{res}_i \) for \( i \in \mathcal{T} \).
   - Send \( \{\text{res}_i\}_{i \in \mathcal{T}} \) to \( P_1 \).
   - Emulate the protocol:
     a. \( \text{Request}(C, \text{pwd}, x, \mathcal{T}) \rightarrow ((C, x, \text{req}'_i)_{i \in \mathcal{T}}, \hat{D}') \).
     b. \( \text{Respond}(\text{sk}_i, \text{rec}_i, C, x, \text{req}'_i) \rightarrow \text{res}'_i \) for \( i \in \mathcal{T} \).
     c. \( \text{Finalize}(\hat{D}', \{\text{res}'_i\}_{i \in \mathcal{T}}) \rightarrow \text{tk}' \).
   - Output \( \text{tk}' \).

4. On receiving the second message from \( P_2 \), \( P_1 \) executes the following:
   - Run \( \text{Finalize}(\hat{D}, \{\text{res}_i\}_{i \in \mathcal{T}}) \rightarrow \text{tk} \).
   - Output \( \text{tk} \).

Figure 5.17: Secure two-party key agreement protocol.

protocol, then we can construct another adversary \( B \) that breaks unforgeability of the PbTA scheme.

\( B \) does not corrupt any server or client. It then calls \( \mathcal{O}_{\text{signup}}(C) \) to obtain \( \{\text{msg}_i\}_{i \in [n]} \), and calls \( \mathcal{O}_{\text{server}}(i, \text{store}, \text{msg}_i) \) to register \( C \) for all \( i \in \mathcal{T} \). Then it calls \( \mathcal{O}_{\text{req}}(C, \text{pwd}, 0, \mathcal{T}) \) to obtain \( \{\text{req}_i\}_{i \in \mathcal{T}} \), and calls \( \mathcal{O}_{\text{server}}(i, \text{respond}, \text{req}_i) \) to obtain \( \text{res}_i \) for all \( i \in \mathcal{T} \). \( B \) runs \( \mathcal{A} \) with input being the transcript of the key-agreement protocol, consisting of \( \{(\text{pp}, C), \{\text{req}_i\}_{i \in \mathcal{T}}, \{\text{res}_i\}_{i \in \mathcal{T}}\} \), and obtains an output \( \text{tk} \) from \( \mathcal{A} \). Then \( B \) simply outputs \((C, 0, \text{tk})\).

In the security game \( \text{SecGame}_{\Pi,\mathcal{A}}(1^\kappa, n, t, P) \) (Figure 5.10) for \( B \), we have \( Q_{C,i} = 0 \) for all
i. By the unforgeability definition, there exists a negligible function \( \text{negl} \) such that

\[
\Pr[\text{Verify}(vk, C', 0, \tilde{tk}) = 1] \leq \text{negl}(\kappa).
\]

However, \( \mathcal{A}' \)’s token \( \tilde{tk} \) is valid with non-negligible probability, leading to a contradiction.

Combining the above theorem with the result of Impagliazzo and Rudich [IR90], we have the following corollary:

**Corollary 5.6.2.** If there exists a PbTA scheme that only makes black-box use of one-way functions, then \( P \neq NP \).

This corollary provides us with evidence that it is unlikely to construct PbTA schemes that only make black-box use of one-way functions. Notice that we did not rule out the possibility of getting around this problem by making non-black-box use of one-way functions. We leave that as an interesting open problem.
Bibliography


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