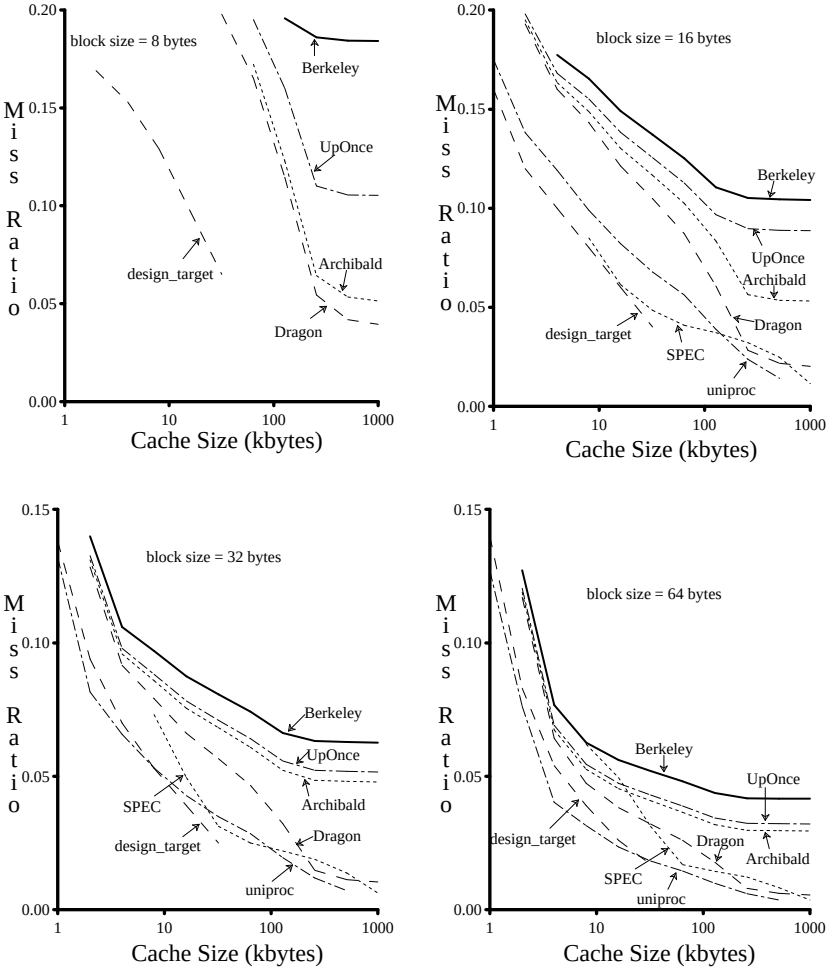

Figure 3: Multiprocessor cache miss ratios for the vector workload.
SPEC and uniprocessor vector results are unavailable for an 8 byte block size.

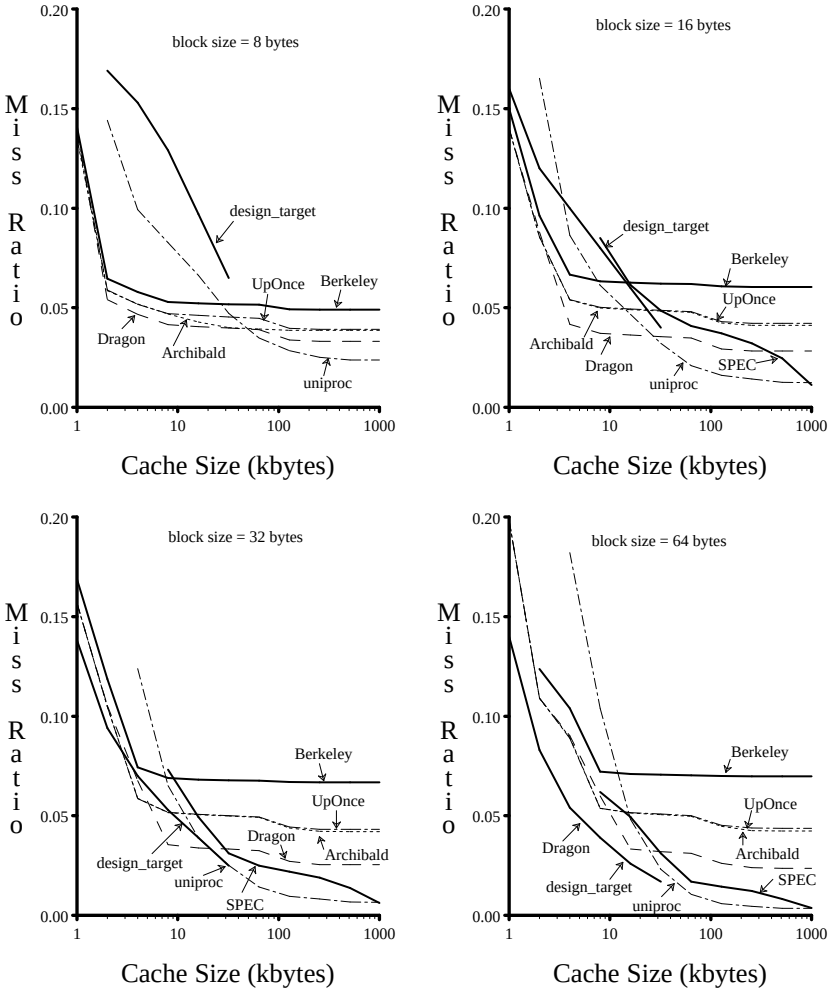

Figure 4: Multiprocessor cache miss ratios for the scalar workload.
SPEC results are unavailable for an 8 byte block size.

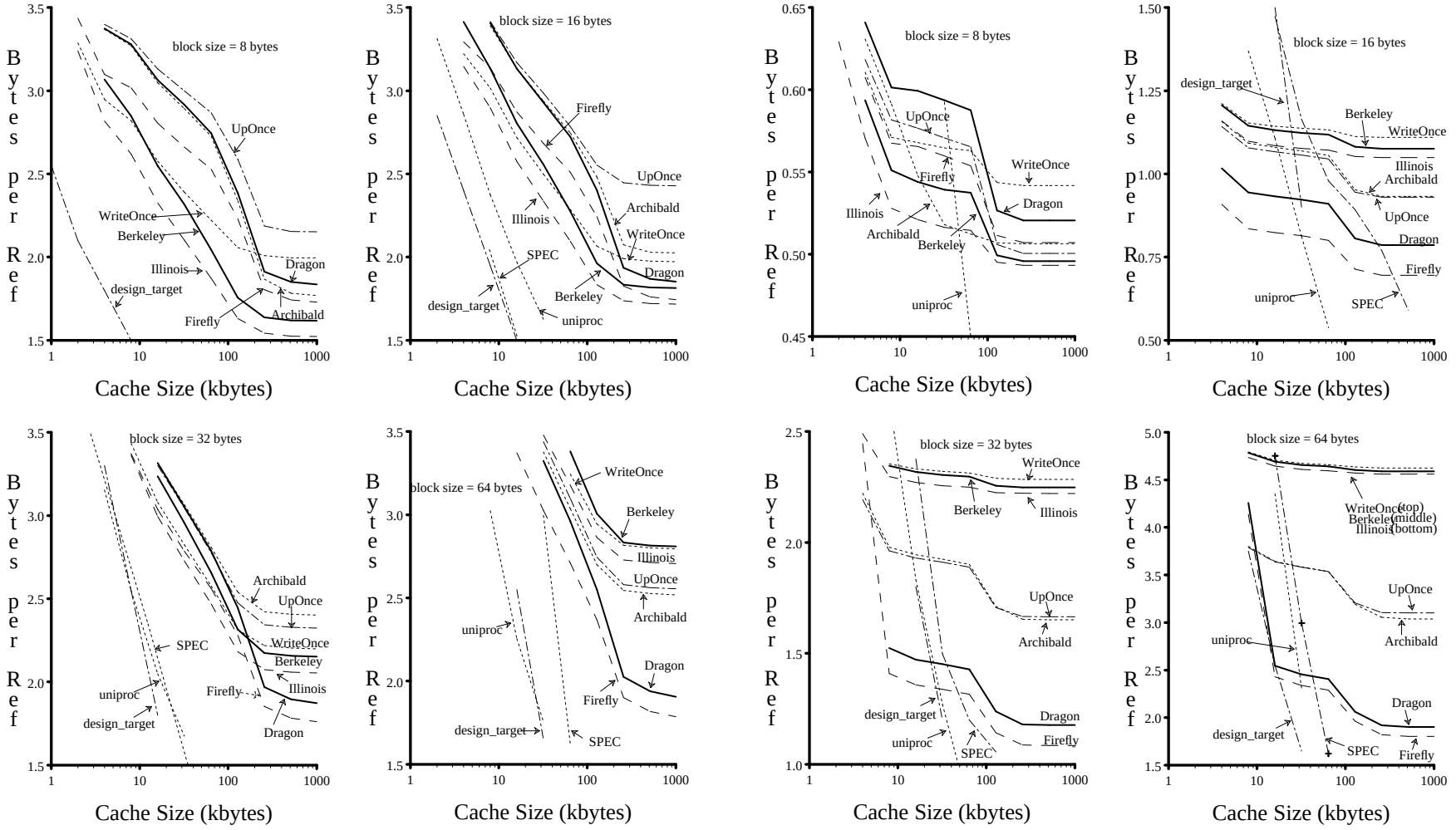

Figure 5: Data traffic in the vector workload.
SPEC and uniprocessor vector results are unavailable at an 8 byte block size.

Figure 6: Data traffic in the scalar workload At an 8 byte block size, SPEC results are unavailable and design targets are below the X-axis.

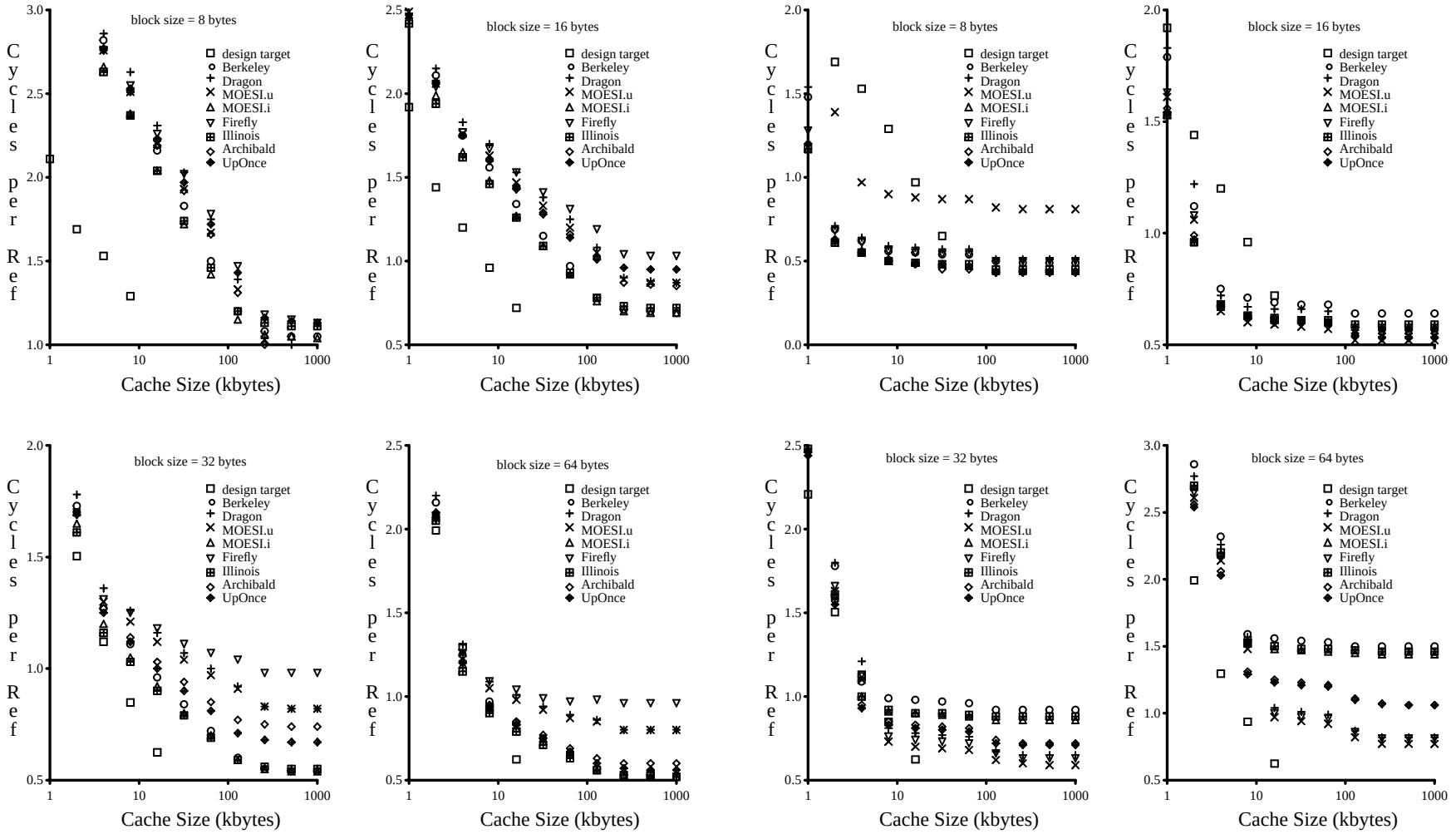

Figure 8. Vector workload bus cycles.
(snooping-caches, 8 cycle memory latency)

Figure 9. Scalar workload bus cycles.
(snooping-caches, 8 cycle memory latency)